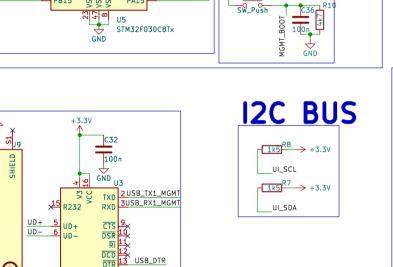
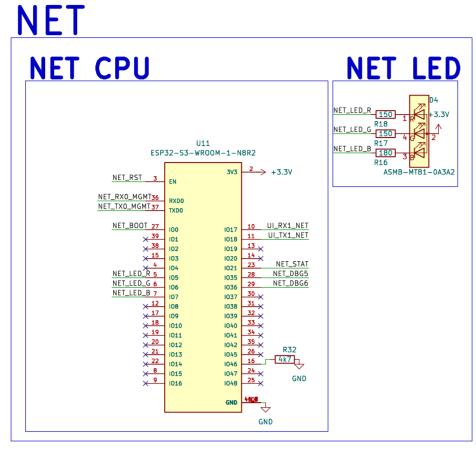
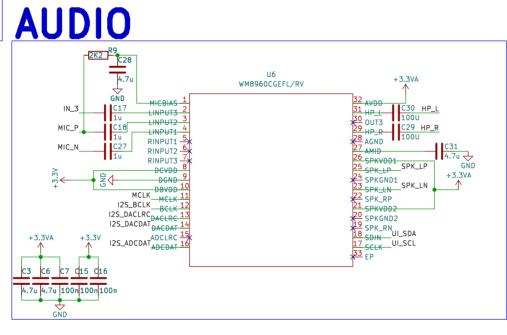
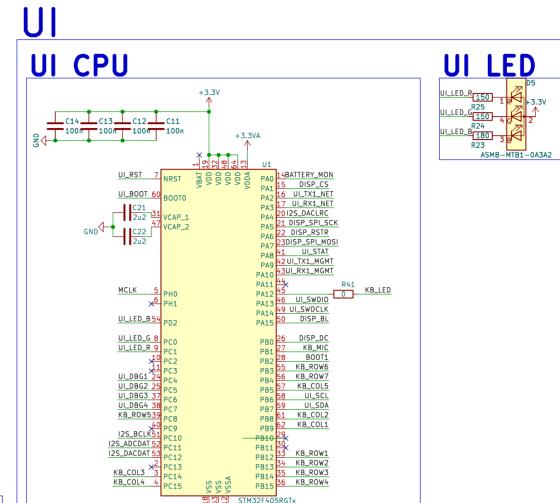


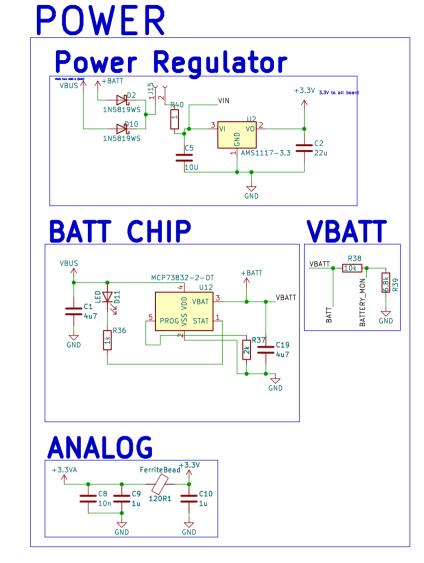
USB

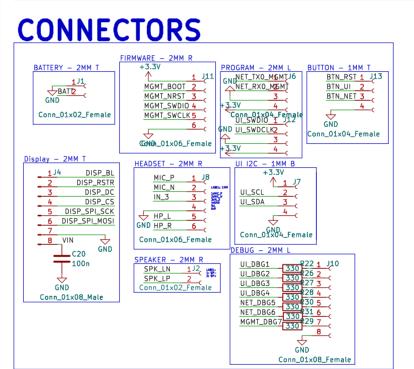


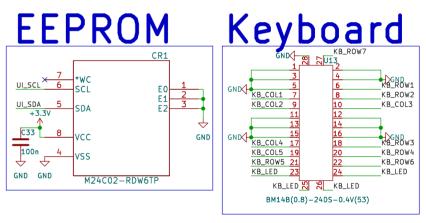












NOTES

EV9 layer build up: top: High Speed second: GND third: 3.3V bottom: Low Speed CISCO SYSTEMS

Sheet: /
File: EV9.5 Board Design.kicad_sch

Title: EV9 - FINN BERG

Size: A2 Date: 2023-10-02 Rev:
KiCad E.D.A. kicad 7.0.5 Id: 1/1