

[illegible]

The schematic shows the connection between the WM8960CEFL/RV audio codec and the ADXL345 digital accelerometer. The WM8960 is powered by +3.3V and GND. Its I2S interface is connected to the ADXL345: I2S\_SDA to SDOIN, I2S\_SCL to SCLK, I2S\_BCLK to BCLK, I2S\_DACLRC to DACLRC, I2S\_DACDAT to DACDAT, and I2S\_ADCCDAT to ADCCDAT. The WM8960's HP\_L and HP\_R outputs are connected to the MIC\_P and MIC\_N inputs of the ADXL345 through a 2k2 resistor (R9) and a 1uF capacitor (C18). The ADXL345 is also powered by +3.3V and GND.

[illegible]

SW1  
TS-1037-A11B3-D2

PTT\_BTN

GND

SW3  
TS-1037-A11B3-D2

PTT\_AI\_BTN

GND

Display - 1MM B

DISP_BL	1	J10
DISP_RST	2	<
DISP_DC	3	<
DISP_CS	4	<
SPI1_CLK	5	<
SPI1_MOSI	6	<
	7	<
+3.3V	8	<

GND

Conn\_01x08\_Socket

HEADSET - 3.5mm\_1

AudioJack1

S	MIC_P
R2	MIC_N
R4	HP_R
HP_L	

pinout:

- s = 1
- R2 = 3
- R4 = 2
- C145815

The diagrams show the following connections for the Conn\_01x06\_Female connector:

- MGMT ST-LINK - 2MM R:**
  - 1: +3.3V
  - 2: MGMT\_SWCLK
  - 3: MGMT\_SWDIO
  - 4: MGMT\_NRST
  - 5: MGMT\_BOOT
  - 6: GND
- Serial Debug:**
  - 1: +3.3V
  - 2: GND
  - 3: UI\_TX2\_NET
  - 4: UI\_RX2\_NET
  - 5: USB\_TX1\_MGMT
  - 6: USB\_RX1\_MGMT
  - 7: NET\_TX1\_MGMT
  - 8: NET\_RX1\_MGMT
- I2S & I2C Debug:**
  - 1: +3.3V
  - 2: GND
  - 3: I2S\_BCLK
  - 4: I2S\_DACLRC
  - 5: MCLK
  - 6: UI\_SDA
  - 7: UI\_SCL
  - 8: I2S\_DACDAT
  - 9: I2S\_ADCCAT
- GPIO Debug:**
  - 1: +3.3V
  - 2: GND
  - 3: UI\_DEBUG\_1
  - 4: UI\_DEBUG\_2
  - 5: UI\_DEBUG\_3
  - 6: R36 NET\_DEBUG\_3
  - 7: R37 NET\_DEBUG\_2
  - 8: R38 NET\_DEBUG\_1
- Net JTAG Debugger:**
  - 1: +3.3V
  - 2: GND
  - 3: NET\_TMS
  - 4: NET\_TCK
  - 5: NET\_MTD0
  - 6: NET\_MTD1
- Net Serial Uploader:**
  - 1: +3.3V
  - 2: GND
  - 3: NET\_BOOT
  - 4: NET\_TX1\_MGMT
  - 5: NET\_RX1\_MGMT
  - 6: NET\_NRST

EV11 layer build up:  
top: High Speed  
second: GND  
third: 3.3V  
bottom: Low Speed