

King Mongkut's University of Technology Thonburi

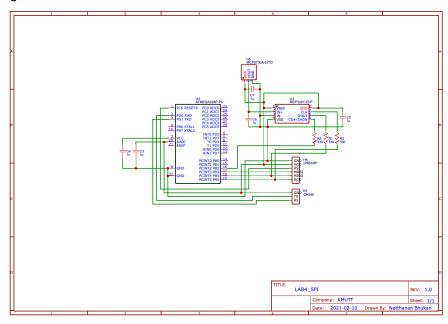
Faculty of Engineering, Department of Computer Engineering

CPE328 Embedded System, 2/2020

LAB Lecture 3: SPI Communication

Assign Date: 10 Feb 2021 Due Date: 16 Feb 2021

## Schematic Diagram



## Code

```
#include <avr/io.h>
#include <strio.h>
#include <stdio.h>
#include <std>include <st
```



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```
void SPI_Init()
{

/* set MOSI CLK CS as Output*/
    DDRB |= (1 << CS_DDR) | (1 << CLK) | (1 << MOSI);
    // Chip select high
    PORTB |= (1 << CS);
    // Chip select low
    PORTB &= ~(1 << CS);
    /* Enable SPI, Master mode, clk/16 */
    SPCR |= (1 << SPE) | (1 << MSTR) | (1 << SPR0);
}</pre>
```



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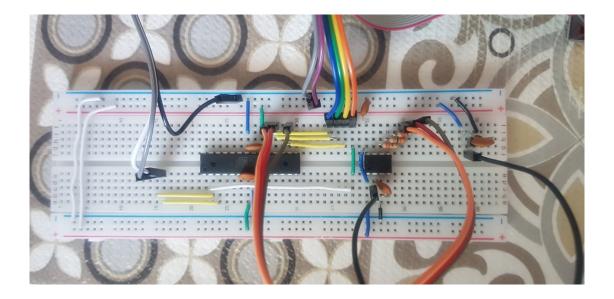
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## On board



## Result

