

# Semantics and Applications to Verification

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## Introduction

This document is Antoine Groudiev's class notes while following the class *Sémantique et applications à la vérification de programmes* (Semantics and Applications to Verification) at the Computer Science Department of ENS Ulm. It is freely inspired by the class notes of Xavier Rival and Sylvain Conchon.

## 1 Introduction to Semantics

### 1.1 Case studies

We will study some examples of software errors: what are the causes of these, what kind of properties do we want to verify in order to prevent such failures?

#### 1.1.1 Ariane 5 – Flight 501

Ariane 5 was a satellite launcher, aimed at replacing Ariane 4. Its first flight, June, 4th, 1996, was a failure, with more than \$370 000 000 of damages. 37 seconds after the launch, the rocket exploded.

The system contained sensors, two calculators (SRI, OBC), actuators, and redundant systems (failure tolerant system). The failure was due to an unhandled arithmetic error. Each register of the SRI has a size of 16, 32, or 64 bits. The error was due to a conversion of a 64-bit float to a 16-bit integer. The value was too large to be represented in 16 bits, and the conversion failed. The software was not able to handle this error, and the system crashed.

Several solutions would have prevented this mishapening:

- Deactivate interruptions on overflows
- Fix the SRI code, so that no overflow can happen. All conversions must be *guarded against overflows*:

```
double x = /* ... */ ;
short i = /* ... */ ;
if ( -32768. <= x && x <= 32767. )
    i = ( short ) x ;
else
    i = /* default value */ ;
```

This may be costly, but redundant tests can be removed.

- Handle conversion errors (not trivial): identify the problem and fix it at run time.

The piece of code that generated the error was used to do a useless task, the re-calibration process is not useful after take-off. Furthermore, the code was already used in Ariane 4; initially protected by a safety guard, many conversions and tests were removed for the sake of performance after being tested on Ariane 4.

The crash was not prevented by redundant systems: the two calculators were running the same code, and the same error was made on both. Redundancy can prevent hardware errors, but is not enough to prevent software errors.

### 1.1.2 Lufthansa Flight 2904

On November 22, 2003, a Lufthansa Airbus A320-200 crashed at the airport of Warsaw, Poland. The plane was landing, and the weather was bad. The plane was not able to stop before the end of the runway, and crashed into a building. The cause of the crash was a software error in the plane's computer. The plane was not able to compute the correct deceleration, and the pilot was not able to stop the plane in time.

### 1.1.3 Patriot missile (anti-missile system), Dahrhan

The purpose of the Patriot system was to destroy foe missiles before they reach their target, and was used in the first Gulf War with a success rate around 50%. The system was used to destroy Scud missiles, and the system was not able to destroy one of them, which hit a barrack, killing 28 people. The cause of the failure was a software error in the system's clock. The system was not able to compute the time correctly due to fixed precision arithmetic error, and the missile was not destroyed.

### 1.1.4 General remarks

The examples given so far are not isolated cases. The typical causes of software errors are:

- Improper specification
- Incorrect implementation of a specification (the code should be free of runtime errors, and should produce a result that meets some property)
- Incorrect understanding of the execution model (generation of too imprecise results)

This creates new challenges to ensure embedded systems do not fail. The main techniques to ensure software safety are software development techniques:

- software engineering
- programming rules
- make software cleaner

In this class, we will instead dive into formal methods:

- should have sound mathematical foundations
- should allow guaranteeing software meet some complex properties
- should be trustable
- increasingly used in real life applications

This course will contain two main parts. The first part will be about Semantics, which allow describing precisely the behavior of programs, express the properties to verify, and to transform and compile programs. The second part, Verification, aims at proving semantic properties of programs. A very strong limitation of verification is undecidability; several approaches make various compromises around undecidability.

## 1.2 Approaches to verification

### 1.2.1 The termination problem

**Definition** (Termination). A program  $P$  terminates on input  $X$  if and only if any execution of  $P$  with input  $X$  eventually reaches a final state. A final state is a final point in the program (i.e., not an error).

**Definition** (Termination problem). Can we find a program  $Pt$  that takes as argument a program  $P$  and data  $X$  and that returns **True** if  $P$  terminates on  $X$ , and **False** otherwise?

**Property 1.** The termination problem is not computable.

*Proof.* We assume there exists a program  $Pa$  such that  $Pa$  always terminates, and returns 1 if and only if  $P$  terminates on input  $X$ . We consider the following program:

```
void P0 ( P ) {  
    if ( Pa ( P , P ) == 1 ) {  
        while ( 1 ) {  
            // loop forever  
        }  
    } else {  
        return ; // do nothing  
    }  
}
```

and we consider the return value of  $Pa(P0, P0)$ . If  $Pa(P0, P0) == 1$ , then  $P0$  loops forever, and if  $Pa(P0, P0) == 0$ , then  $P0$  terminates. This is a contradiction, and the termination problem is not computable.  $\square$

**Property 2.** The absence of runtime errors is not computable. We cannot find a program  $Pc$  that takes a program  $P$  and input  $X$  as arguments, always terminates, and returns 1 if and only if  $P$  runs on input  $X$  without a runtime error.

**Theorem** (Rice theorem). Considering a Turing complete language, any non-trivial semantic specification is not computable. Therefore, all interesting properties are not computable (termination, absence of runtime/arithmetic errors, etc.).

The initial verification problem is not computable. Several compromises can be made: simulation, testing, assisted theorem proving, model checking, bug-finding, static analysis with abstraction.

**Definition** (Safety verification problem). The Semantics  $\llbracket P \rrbracket$  of a program  $P$  is the set of behaviors of  $P$  (e.g. states). A property to verify  $\mathcal{S}$  is the set of admissible behaviors (e.g. safe states). Our goal is to establish  $\llbracket P \rrbracket \subseteq \mathcal{S}$

$\llbracket P \rrbracket$  can be sound (identify any wrong program), complete (accept all correct programs), and automated, but not all three at the same time.

**Testing by simulation** The principle of testing by simulation is to run the program on finitely many finite inputs, to maximize coverage and inspect erroneous traces to fix bugs. It is very widely used, through unit testing, integration testing, etc. It is both automated and complete, but is unsound and costly.

**Machine assisted proof** The principle of machine assisted proof is to have a machine check proof that is partly human written: tactics or solvers may help in the inference, and the hardest invariants have to be user-supplied. It is sound and quasi-complete, but not fully automated and costly.

**Model checking** We consider finite systems only, using algorithms for exhaustive exploration, symmetry reduction, ... It is automated, sound, and complete *with respect to the model*.

**Bug finding** The principle of bug finding is to identify "likely" issues, patterns known to often indicate an error: it uses bounded symbolic execution, model exploration, and rank "defect" reports using heuristics. It is neither sound nor complete, but is fully automated.

**Static analysis with abstraction** The principle of static analysis with abstraction is to use some approximation, but always in a conservative manner. We can use under-approximation of the property to verify:

$$\mathcal{S}_{\text{under}} \subseteq \mathcal{S}$$

and over-approximation of the semantics:

$$\llbracket P \rrbracket \subseteq \llbracket P \rrbracket_{\text{upper}}$$

We let an automatic static analyser attempt to prove that:

$$\llbracket P \rrbracket_{\text{upper}} \subseteq \mathcal{S}_{\text{under}}$$

If it succeeds, then we have proven that  $\llbracket P \rrbracket \subseteq \mathcal{S}$ . It is automated, sound, but incomplete.

### 1.2.2 A summary of common verification techniques

	Automatic	Sound	Complete	Source level
Simulation	Yes	No	Yes	Yes
Assisted Proving	No	Yes	Almost	Partially
Model-checking	Yes	Yes	Partially	No
Bug-finding	Yes	No	No	Yes
Static analysis	Yes	Yes	No	Yes

## 1.3 Orderings, lattices and fixpoints

### 1.3.1 Basic definitions on orderings

**Definition** (Partially ordered set (poset)). Let a set  $\mathcal{S}$  and a binary relation  $(\sqsubseteq) \subseteq \mathcal{S} \times \mathcal{S}$  over  $\mathcal{S}$ . Then,  $\sqsubseteq$  is an order relation if and only if it is reflexive, transitive, antisymmetric. Furthermore, we define  $x \sqsubset y ::= (x \sqsubseteq y \wedge x \neq y)$ . Most orders in this class won't be total.

We often use Hasse diagrams to represent posets.

In the following, we illustrate order relations and their usefulness in semantics using the standard definition of *word automata*. The semantics of an automaton is the set of words recognized by it.

We can already define a few semantic properties:

- $\mathcal{P}_0$ : no recognized word contains two consecutive  $b$

$$\mathcal{L}[\mathcal{A}] \subseteq L^* \setminus L^*bbL^*$$

- $\mathcal{P}_1$ : all recognized words contain at least one occurrence of  $a$

$$\mathcal{L}[\mathcal{A}] \subseteq L^*aL^*$$

- $\mathcal{P}_2$ : recognized words do not contain  $b$

$$\mathcal{L}[\mathcal{A}] \subseteq (L \setminus \{b\})^*$$

**Definition** ( $\perp, \top$ ). When they exist, we denote by infimum  $\perp$  and supremum  $\top$  the smallest and largest elements of the poset.

**Definition** ( $\sqcup, \sqcap$ ). We denote  $\sqcap \mathcal{S}$  the glb (greatest lower bound) of  $\mathcal{S}$ , and  $\sqcup$  the lub (lowest upper bound) of  $\mathcal{S}$ .

### 1.3.2 Complete lattice

**Definition** (Complete lattice). A complete lattice is a tuple  $(\mathcal{S}, \sqsubseteq, \perp, \top, \sqcup, \sqcap)$  where  $(\mathcal{S}, \sqsubseteq)$  is a poset, and any subset  $\mathcal{S}'$  of  $\mathcal{S}$  has a glb  $\sqcap \mathcal{S}'$  and a lub  $\sqcup \mathcal{S}'$ .

**Definition** (Lattice). A lattice is a tuple  $(\mathcal{S}, \sqsubseteq, \perp, \top, \sqcup, \sqcap)$  where  $(\mathcal{S}, \sqsubseteq)$ , and any pair  $\{x, y\}$  of  $\mathcal{S}$  has a glb  $x \sqcap y$  and a lub  $x \sqcup y$ .

**Example.**  $\mathbb{Q} \cap [0, 1]$  is a lattice but not a complete lattice, since

$$\left\{ q \in \mathbb{Q} \cap [0, 1] \mid q \leq \frac{\sqrt{2}}{2} \right\} \subseteq \mathbb{Q} \cap [0, 1]$$

has no lowest upper bound in  $\mathbb{Q} \cap [0, 1]$ .

**Property 3.** A finite lattice is also a complete lattice.

**Definition** (Increasing chain). Let  $(\mathcal{S}, \sqsubseteq)$  be a poset and  $\mathcal{C} \subseteq \mathcal{S}$ . It is an increasing chain if and only if it is not empty, and  $(\mathcal{C}, \sqsubseteq)$  is total.

**Example.** In the powerset  $(\mathcal{P}(\mathbb{N}), \subseteq)$ ,

$$\mathcal{C} := \left\{ \{2^0, \dots, 2^i\} \mid i \in \mathbb{N} \right\}$$

is an increasing chain.

**Definition** (Increasing chain condition). The poset  $(\mathcal{S}, \sqsubseteq)$  satisfies the increasing chain condition if and only if any increasing chain  $\mathcal{C} \subseteq \mathcal{S}$  is finite.

**Definition** (Complete partial order). A complete partial order (cpo) is a poset  $(\mathcal{S}, \sqsubseteq)$  such that any increasing chain  $\mathcal{C}$  of  $\mathcal{S}$  has at least an upper bound. A pointed cpo is a cpo with a bottom element  $\perp$ .

### 1.3.3 How to prove semantic properties

#### 1.3.4 Operators over a poset

**Definition** (Operators and orderings). Let  $(\mathcal{S}, \sqsubseteq)$  be a poset and  $\varphi : \mathcal{S} \rightarrow \mathcal{S}$  be an operator over  $\mathcal{S}$ . Then,  $\varphi$  is:

- *monotone* if and only if  $x \sqsubseteq y \Rightarrow \varphi(x) \sqsubseteq \varphi(y)$
- *continuous* if and only if, for any **chain**  $\mathcal{S}' \subseteq \mathcal{S}$ , then:

$$\begin{cases} \text{if } \sqcup \mathcal{S}' \text{ exists, so does } \sqcup \varphi(\mathcal{S}') \\ \text{and } \sqcup \varphi(\mathcal{S}') = \varphi(\sqcup \mathcal{S}') \end{cases}$$

- $\sqcup$ -preserving if and only if:

$$\forall \mathcal{S}' \subseteq \mathcal{S}, \begin{cases} \text{if } \sqcup \mathcal{S}' \text{ exists, so does } \sqcup \varphi(\mathcal{S}') \\ \text{and } \sqcup \varphi(\mathcal{S}') = \varphi(\sqcup \mathcal{S}') \end{cases}$$

**Property 4** (Continuity  $\implies$  monotonicity). If  $\varphi$  is continuous, then it is monotone.

**Property 5** ( $\sqcup$ -preserving  $\implies$  monotonicity). If  $\varphi$  preserves  $\sqcup$ , then it is monotone.

#### 1.3.5 Fixpoints theorems

**Definition** (Fixpoints). Let  $(\mathcal{S}, \sqsubseteq)$  be a poset and  $\varphi : \mathcal{S} \rightarrow \mathcal{S}$  be an operator over  $\mathcal{S}$ .

- A fixpoint of  $\varphi$  is an element  $x$  such that  $\varphi(x) = x$ .
- A pre-fixpoint of  $\varphi$  is an element  $x$  such that  $x \sqsubseteq \varphi(x)$ .
- A post-fixpoint of  $\varphi$  is an element  $x$  such that  $\varphi(x) \sqsubseteq x$ .
- The least fixpoint of  $\varphi$  is a fixpoint  $x$  such that, for any fixpoint  $y$ ,  $x \sqsubseteq y$ .
- The greatest fixpoint of  $\varphi$  is a fixpoint  $x$  such that, for any fixpoint  $y$ ,  $y \sqsubseteq x$ .

**Theorem** (Tarski's). Let  $(\mathcal{S}, \sqsubseteq, \perp, \top, \sqcup, \sqcap)$  be a complete lattice and  $\varphi : \mathcal{S} \rightarrow \mathcal{S}$  be a monotone operator. Then:

- $\varphi$  has a least fixpoint  $\text{lfp } \varphi$  and  $\text{lfp } \varphi = \sqcap \{x \in \mathcal{S} \mid \varphi(x) \sqsubseteq x\}$
- $\varphi$  has a greatest fixpoint  $\text{gfp } \varphi$  and  $\text{gfp } \varphi = \sqcup \{x \in \mathcal{S} \mid x \sqsubseteq \varphi(x)\}$
- the set of fixpoints of  $\varphi$  is a complete lattice

**Example.** We consider a set  $\mathcal{E}$ , and a subset  $\mathcal{A} \subseteq \mathcal{E}$ . We let:

$$\begin{aligned} f : \mathcal{P}(\mathcal{E}) &\rightarrow \mathcal{P}(\mathcal{E}) \\ X &\mapsto X \cup \mathcal{A} \end{aligned}$$

According to Tarski's theorem, the smallest fixpoint of  $f$  is  $\mathcal{A}$ , and the greatest is  $\mathcal{E}$ .

**Theorem** (Kleene's). Let  $(\mathcal{S}, \sqsubseteq, \perp)$  be a pointed cpo and  $\varphi : \mathcal{S} \rightarrow \mathcal{S}$  be a continuous operator over  $\mathcal{S}$ . The  $\varphi$  has a least fixpoint, and

$$\text{lfp } \varphi = \bigsqcup_{n \in \mathbb{N}} \varphi^n(\perp)$$

## 2 Operational Semantics

Operational semantics are mathematical descriptions of the executions of a program. It is based on a model of programs, called transition systems.



## 2.1 Definition and properties

**Definition** (Transition systems (TS)). A transition system is a tuple  $(\mathbb{S}, \rightarrow)$  where  $\mathbb{S}$  is the *set of states of the system*, and  $\rightarrow \subseteq \mathbb{S} \times \mathbb{S}$  is the transition relation of the system.

Note that the set of states may be infinite. The majority of interesting examples come from the cases where  $\mathbb{S}$  is infinite.

**Definition** (Deterministic system). A deterministic system is such that a state fully determines the next state.

$$\forall s_0, s_1, s'_1 \in \mathbb{S}, (s_0 \rightarrow s_1 \wedge s_0 \rightarrow s'_1) \implies s_1 = s'_1$$

Otherwise, a transition system is non-deterministic.

The transition relation  $\rightarrow$  defines atomic execution steps. It is often called *small-step semantics* or *structured operational semantics*. Steps are *discrete*, and we do not consider non-deterministic systems with probability on transitions (probabilistic transition systems).

**Definition** (Initial and final states). We often consider transition systems with a set of initial and final states:

1. a set of initial states  $\mathbb{S}_I \subseteq \mathbb{S}$  denotes states where the execution should start
2. a set of final states  $\mathbb{S}_F \subseteq \mathbb{S}$  denotes states where the execution should reach the end of the program

When needed, we add these to the definition of the transition systems  $(\mathbb{S}, \rightarrow, \mathbb{S}_I, \mathbb{S}_F)$ .

**Definition** (Blocking state  $\neq$  final state). A state  $s_0 \in \mathbb{S}$  is blocking when it is the origin of no transition:

$$\forall s_1 \in \mathbb{S}, \neg(s_0 \rightarrow s_1)$$

As an example, we often introduce an error state (usually noted  $\Omega$ )

## 2.2 Examples

### 2.2.1 Word recognition

We can formalize the *word recognition* by a finite automaton using a transition system. We consider an automaton  $\mathcal{A} = (Q, q_i, q_f, \rightarrow)$ . A state is defined by the remaining of the word to recognize, and the automaton state that has been reached so far. Thus,

$$\mathbb{S} = Q \times \Sigma^*$$

We define the transition relation  $\rightarrow$  to be:

$$(q_0, aw) \rightarrow (q_1, w) \iff q_0 \xrightarrow{a} q_1$$

The initial and final states are defined by:

$$\begin{cases} \mathbb{S}_I = \{ (q_i, w) \mid w \in \Sigma^* \} \\ \mathbb{S}_F = \{ (q_f, \varepsilon) \} \end{cases}$$

### 2.2.2 Pure $\lambda$ -calculus

A bare-bones model of functional programming:

**Definition** ( $\lambda$ -terms). The set of  $\lambda$ -terms is defined by:

$$\begin{array}{ll} t, u, \dots ::= x & \text{variable} \\ | \lambda x \cdot t & \text{abstraction} \\ | tu & \text{application} \end{array}$$

**Definition** ( $\beta$ -reduction).

The  $\lambda$ -calculus defines a transition system.  $\mathbb{S}$  is the set of  $\lambda$ -terms and  $\rightarrow_\beta$  the transition relation.  $\rightarrow_\beta$  is non-deterministic, since multiple  $\beta$ -reduction are sometimes possible. Given a lambda term  $t_0$ , we may consider  $(\mathbb{S}, \rightarrow_\beta, \mathbb{S}_I)$  where  $\mathbb{S}_I = \{t_0\}$ . Blocking states are terms with no redex  $(\lambda x \cdot u)v$ .

## 2.3 A MIPS like assembly language

We now consider a very simplified assembly language, containing machine integers  $\mathbb{B}^{32}$ . Instruction are encoded over 32 bits and stored in the same space as data,  $\mathbb{B}^{32}$ . We assume a fixed set of addresses  $A$ .

The memory configuration contains the program counter  $\text{pc}$ , the general purpose register  $r_0, \dots, r_{31}$ , and the main memory (RAM):

$$\text{mem} : A \subseteq \mathbb{B}^{32} \rightarrow \mathbb{B}^{32}$$

**Definition** (State). A state is a tuple  $(\pi, \rho, \mu)$  which comprises a program counter value  $\pi \in \mathbb{B}^{32}$ , a function mapping each general purpose register to its value  $\rho : \{0, \dots, 32\} \rightarrow \mathbb{B}^{32}$ , and a function mapping each memory cell to its value,  $\mu : A \rightarrow \mathbb{B}^{32}$ .

We can define transition relations.

We now look at a more classical imperative language (a bare-bone subset of C), containing variables  $X$ , labels  $L$ , and values  $V$ . A syntax can be defined.

## 2.4 Traces semantics

**Definition** (Traces). A *finite trace* is a finite sequence of states  $s_0, \dots, s_n$  noted  $\langle s_0, \dots, s_n \rangle$ . An infinite trace is an infinite sequence of states  $\langle s_0, \dots \rangle$ . Besides, we write  $\mathbb{S}^*$  for the set of finite traces,  $\mathbb{S}^\omega$  for the set of infinite traces, and  $\mathbb{S}^\infty = \mathbb{S}^* \cup \mathbb{S}^\omega$ .

**Definition** (Concatenation operator  $\cdot$ ).

# 3 Traces Properties

## 3.1 A high level overview

### 3.1.1 Introduction

The goal of verification is to prove that  $\llbracket P \rrbracket \subseteq \mathcal{S}$  (i.e. that all behaviors of  $P$  satisfy specifications  $\mathcal{S}$ ) where  $\llbracket P \rrbracket$  is the program semantics and  $\mathcal{S}$  the desired specification. Today, we will mostly focus on program's properties,  $\mathcal{S}$ . We will see different families of properties, proof techniques, and specification of properties (are there languages to describe properties?).

A property is a set of traces, defining the admissible executions. There are *safety properties*: something will never happen, which is often proven by invariance; *liveness properties*: something will eventually happen, proven by variance; and beyond safety and liveness, there are *hyperproperties*.

As usual, we consider  $\mathcal{S} = (\mathbb{S}, \rightarrow, \mathbb{S}_T)$ .

**Definition** (Properties as sets of states). A property  $\mathcal{P} \subseteq \mathbb{S}$  is a set of states  $\mathcal{P} \subseteq \mathbb{S}$ .  $\mathcal{P}$  is satisfied if and only if all reachable states belong to  $\mathcal{P}$ .

This is the case of the absence of runtime errors, and non-termination.

**Definition** (Properties as sets of traces). A property  $\mathcal{T}$  is a set of traces  $\mathcal{T} \subseteq \mathbb{S}^\times$ .  $\mathcal{T}$  is satisfied if and only if all traces belong to  $\mathcal{T}$ , i.e.  $\llbracket \mathbb{S}^\times \rrbracket \subseteq \mathcal{T}$ .

State properties are trace properties. Functional properties (such as “program  $P$  takes one integer input  $x$  and returns its absolute value”) and termination ( $\mathcal{T} = \mathbb{S}^*$ ) are trace properties.

### 3.1.2 A few monotonicity properties

**Property 6.** Let  $\mathcal{P}_0, \mathcal{P}_1 \subseteq \mathbb{S}$  be two state properties. When  $\mathcal{P}_0 \subseteq \mathcal{P}_1$ , we say that  $\mathcal{P}_0$  is *stronger* than  $\mathcal{P}_1$ , i.e. if a program  $\mathcal{S}$  satisfies  $\mathcal{P}_0$  then it also satisfies  $\mathcal{P}_1$ .

**Property 7.** Let  $\mathcal{T}_0, \mathcal{T}_1 \subseteq \mathbb{S}$  be two trace properties. When  $\mathcal{T}_0 \subseteq \mathcal{T}_1$ , we say that  $\mathcal{T}_0$  is *stronger* than  $\mathcal{T}_1$ , i.e. if a program  $\mathcal{S}$  satisfies  $\mathcal{T}_0$  then it also satisfies  $\mathcal{T}_1$ .

**Property 8.** Let  $\mathcal{S}_0, \mathcal{S}_1$  be two transition systems. When  $\llbracket \mathcal{S}_0 \rrbracket \subseteq \llbracket \mathcal{S}_1 \rrbracket$ , we say that  $\mathcal{S}_1$  *has more behaviors than*  $\mathcal{S}_0$ , i.e. if a property  $\mathcal{P}$  is satisfied by  $\mathcal{S}_1$  then it is also satisfied by  $\mathcal{S}_0$ .

## 3.2 Safety properties

### 3.2.1 Informal and formal definitions

**Definition** (Informal definition of safety properties). A safety property is a property which specifies that some (bad) behavior defined by a finite, irrecoverable observation will never occur, at any time.

**Example.** The following properties are safety properties:

- Absence of runtime errors
- State properties (the “bad thing” is reaching  $\mathbb{S} \setminus \mathcal{T}$ )
- Non-termination
- “Not reaching state  $b$  after visiting state  $a$ ”

Termination is **not** a safety property, since no finite execution is a counter-example of its termination.

We now intend to provide a formal definition of safety. How to refute a safety property? We assume  $\mathcal{S}$  does not satisfy safety property  $\mathcal{P}$ . Thus, there exists a counter-example trace  $\sigma = \langle s_0, \dots, s_n, \dots \rangle \in \llbracket \mathcal{S} \rrbracket \setminus \mathcal{P}$ . At this point of our study, the trace may be finite or infinite. The intuitive definition says this trace *eventually exhibits some bad behavior*, that is *irrecoverable* at some *observed at some given time*, thus the observation corresponds to some index  $i$ . Therefore, trace  $\sigma' = \langle s_0, \dots, s_i \rangle$  violates  $\mathcal{P}$ , i.e.  $\sigma' \notin \mathcal{P}$ . Due to the irrecoverability of the observation, the same goes for any trace with the same prefix. We remark that  $\sigma'$  is finite.

**A safety property that does not hold can always be refuted with a finite, irrecoverable counter-example.**

### 3.2.2 A few operators on traces

**Definition** (Prefix). We write  $\sigma_{\upharpoonright i}$  for the prefix of length  $i$  of trace  $\sigma$ .

**Definition** (Suffix or tail). We write  $\sigma_{i\downarrow}$  for the suffix of length  $i$  of trace  $\sigma$ .

**Definition** (Upper closure operators (PCl)). In a preorder  $(\mathcal{S}, \sqsubseteq)$ , a function  $\varphi : \mathcal{S} \rightarrow \mathcal{S}$  is an upper closure operator if and only if it is monotone, extensive ( $\forall x \in \mathcal{S}, x \sqsubseteq \varphi(x)$ ) and idempotent.

**Definition** (Prefix closure). The prefix closure operator is defined by:

$$\begin{aligned} \text{PCl} : \mathcal{P}(\mathbb{S}^\infty) &\rightarrow \mathcal{P}(\mathbb{S}^*) \\ X &\mapsto \left\{ \sigma_{\upharpoonright i} \mid \sigma \in X, i \in \mathbb{N} \right\} \end{aligned}$$

PCl is monotone, idempotent, but not extensive on  $\mathcal{P}(\mathbb{S}^\infty)$  (infinite traces do not appear anymore). Its restriction to  $\mathcal{P}(\mathbb{S}^*)$  is extensive.

**Definition** (The Lim operator). The limit operator is defined by:

$$\begin{aligned} \text{Lim} : \mathcal{P}(\mathbb{S}^\infty) &\rightarrow \mathcal{P}(\mathbb{S}^\infty) \\ X &\mapsto X \cup \left\{ \sigma \in \mathbb{S}^\infty \mid \forall i \in \mathbb{N}, \sigma_{\upharpoonright i} \in X \right\} \end{aligned}$$

Note that the operator Lim is an upper-closure operator.

*Proof.* Left as an exercise! □

**Example.** Assume that:

$$\mathcal{S} = \{\varepsilon, \langle a \rangle, \langle a, b \rangle, \langle a, b, a \rangle, \langle a, b, a, b \rangle, \langle a, b, a, b, a \rangle, \dots\}$$

then,

$$\text{Lim}(\mathcal{S}) = \mathcal{S} \uplus \{\langle a, b, a, b, a, b, \dots \rangle\}$$

### 3.2.3 Formal definition of safety

**Definition** (The Safe operator). Operator Safe is defined by

$$\text{Safe} = \text{Lim} \circ \text{PCl}$$

Note that Safe is an upper closure operator over  $\mathcal{P}(\mathbb{S}^\infty)$ .

**Definition** (Safety property). A trace property  $\mathcal{T}$  is a safety property if and only if it is a fixpoint of the Safe operator, that is

$$\text{Safe}(\mathcal{T}) = \mathcal{T}$$

Furthermore, if  $\mathcal{T}$  is a trace property, then  $\text{Safe}(\mathcal{T})$  is a safety property, since Safe is idempotent.

**Theorem.** Any state property is also a safety property.

*Proof.* Consider a state property  $\mathcal{P}$ . It is equivalent to trace property  $\mathcal{T} = \mathbb{P}^\infty$ :

$$\begin{aligned} \text{Safe}(\mathcal{T}) &= \text{Lim} \circ \text{PCl}(\mathcal{P}^\infty) \\ &= \text{Lim}(\mathcal{P}^*) \\ &= \mathcal{P}^* \cup \mathcal{P}^\omega \\ &= \mathcal{P}^\infty \\ &= \mathcal{T} \end{aligned}$$

Therefore,  $\mathcal{T}$  is indeed a safety property. □

### 3.2.4 Intuition of the formal definition

Operator Safe saturates a set of traces  $S$  with prefixes and infinite traces of all finite prefixes of which can be observed in  $S$ . Thus, if  $\text{Safe}(S) = S$  and  $\sigma$  is a trace, to establish that  $\sigma \notin S$ , it is sufficient to discover a *finite prefix* of  $\sigma$  that cannot be observed in  $S$ .

Alternatively, if all finite prefixes of  $\sigma$  belong to  $S$  or can be observed as a prefix of another trace in  $S$ , by definition of the limit operator,  $\sigma$  belongs to  $S$ , *even if it is infinite*. Therefore, the definition captures properties that *can be disproved with a finite counter-example*.

### 3.2.5 Proof method for safety properties

We consider transition system  $\mathcal{S} = (\mathbb{S}, \rightarrow, \mathbb{S}_{\mathcal{I}})$  and a safety property  $\mathcal{T}$ . Finite traces semantics is the least fixpoint of  $F_*$ . We seek a way of verifying that  $\mathcal{S}$  satisfies  $\mathcal{T}$ , i.e. that  $\llbracket \mathcal{S} \rrbracket^\infty \subseteq \mathcal{T}$ .

**Definition** (Invariance proofs). Let  $\mathbb{I}$  be a set of finite traces; it is said to be an *invariant* if and only if:

$$\begin{cases} \forall s \in \mathbb{S}_{\mathcal{I}}, \langle s \rangle \in \mathbb{I} \\ F_*(\mathbb{I}) \subseteq \mathbb{I} \end{cases}$$

Where  $F_*$  is the semantic function, defined previously, that computes the traces of length  $i + 1$  from the traces of length  $i$ , and adds the traces of length 1.

$\mathbb{I}$  is *stronger* than  $\mathcal{T}$  if and only if  $\mathbb{I} \subseteq \mathcal{T}$ . The proof method *by invariance* is based on finding an invariant that is stronger than  $\mathcal{T}$ .

**Theorem** (Soundness of the invariance proof method). The invariance proof method is *sound*: if we can find an invariant for  $\mathcal{S}$ , that is stronger than safety property  $\mathcal{T}$ , then  $\mathcal{S}$  satisfies  $\mathcal{T}$ .

*Proof.* Assume that  $\mathbb{I}$  is an invariant of  $\mathcal{S}$  and that it is stronger than  $\mathcal{T}$ . Let's show that  $\mathcal{S}$  satisfies  $\mathcal{T}$ .

By induction over  $n$ , we can prove that  $F_*^n(\{\langle s \rangle \mid s \in \mathbb{S}_{\mathcal{I}}\}) \subseteq F_*^n(\mathbb{I}) \subseteq \mathbb{I}$ . Therefore,  $\llbracket \mathcal{S} \rrbracket^* \subseteq \mathbb{I}$  and thus,  $\text{Safe}(\llbracket \mathcal{S} \rrbracket^*) \subseteq \text{Safe}(\mathbb{I}) \subseteq \text{Safe}(\mathcal{T})$  since Safe is monotone.

We remark that  $\llbracket \mathcal{S} \rrbracket^\infty = \text{Safe}(\llbracket \mathcal{S} \rrbracket^*)$ , hence  $\llbracket \mathcal{S} \rrbracket^\infty = \text{Safe}(\llbracket \mathcal{S} \rrbracket^*) \subseteq \text{Safe} \mathcal{T} = \mathcal{T}$ . We conclude  $\llbracket \mathcal{S} \rrbracket^\infty \subseteq \mathcal{T}$ , i.e.  $\mathcal{S}$  satisfies property  $\mathcal{T}$ .  $\square$

**Theorem** (Completeness of the invariance proof method). The invariance proof method is *complete*: if  $\mathcal{S}$  satisfies safety property  $\mathcal{T}$ , then we can find an invariant  $\mathbb{I}$  for  $\mathcal{S}$ , that is stronger than  $\mathcal{T}$ .

*Proof.* We choose  $\mathbb{I} = \llbracket \mathcal{S} \rrbracket^*$ , which is both an invariant of  $\mathcal{S}$  and is stronger than  $\mathcal{T}$  since  $\mathcal{S}$  satisfies  $\mathcal{T}$ .  $\square$

Note that  $\llbracket \mathcal{S} \rrbracket^\infty$  is most likely not a very easy to express invariant, but it is just a convenient completeness argument. Therefore, completeness does not mean that the proof is easy.

**Example.** Consider the following program which computed the sum of the elements of an array: We want to prove that when the exit is reached,  $\mathbf{s} = \sum_{k=0}^{n-1} \mathbf{t}[k]$ . For each program point  $l$ , we have a local invariant  $\mathbb{I}_l$ , denoted below by a logical formula instead of a set of states:

The global invariant  $\mathbb{I}$  is defined by:

$$\mathbb{I} := \{ \langle (l_0, m_0), \dots, (l_n, m_n) \rangle \mid \forall n, m_n \in \mathbb{I}_{l_n} \}$$

which is the set of traces satisfying all the local invariants.

```

s = 0;
i = 0;
while (i < n) {
    s = s + t[i];
    i = i + 1;
}

```

Figure 1: A simple program which computes the sum of the elements of  $\mathbf{t}$

	$l_0 : \langle \text{true} \rangle$
1 $\mathbf{s} = 0;$	$l_1 : \langle s = 0 \rangle$
2 $\mathbf{i} = 0;$	$l_2 : \langle i = 1 \wedge s = 0 \rangle$
3 <b>while</b> ( $\mathbf{i} < \mathbf{n}$ ) {	$l_3 : \langle 0 \leq i < n \wedge s = \sum_{k=0}^{i-1} t[k] \rangle$
4 $\mathbf{s} = \mathbf{s} + \mathbf{t}[\mathbf{i}];$	$l_4 : \langle 0 \leq i < n \wedge s = \sum_{k=0}^i t[k] \rangle$
5 $\mathbf{i} = \mathbf{i} + 1;$	$l_5 : \langle 0 \leq i \leq n \wedge s = \sum_{k=0}^{i-1} t[k] \rangle$
6    }	$l_6 : \langle i = n \wedge s = \sum_{k=0}^{n-1} t[k] \rangle$

Figure 2: The program and the local invariants

### 3.3 Liveness properties

Similarly, we will start by sketching an informal definition of a *liveness property*, before introducing an operator-based formal definition and a proof mechanism.

#### 3.3.1 Informal definition

**Definition** (Informal definition of liveness properties). A liveness property is a property which specifies that some (good) behavior *will eventually occur*, and that this behavior *may still occur after any finite observation*.

- *Termination* is a liveness property, in which the “good behavior” is reaching a blocking state (no more transition are available).
- “State  $a$  will eventually be reached by any execution” is also a liveness property, in which the “good behavior” is reaching the state  $a$ .
- The absence of runtime errors is **not** a liveness property.

We can also ask ourselves how to refute a liveness property. Considering a liveness property  $\mathcal{T}$  (i.e.  $\mathcal{T}$  is termination), we assume that  $\mathcal{S}$  does **not** satisfy liveness property  $\mathcal{T}$ . Thus, there exists a counter-example trace  $\sigma \in \llbracket \mathcal{S} \rrbracket \setminus \mathcal{T}$ . The informal definition says “... may still occur after any finite observation”, thus each finite trace  $\sigma'$  can be extended into a good trace. Therefore,  $\sigma$  is necessarily *infinite*.

**To prove that a liveness property does not hold, we need to look for an infinite counter-example: no finite trace is a counter-example.**

For example, when  $\mathcal{T}$  is the termination property, no finite execution can guarantee us that the program will not eventually stop; to prove that a program will not halt, we need to exhibit an infinite execution.

### 3.3.2 Formal definition

**Definition** (Operator Live). The operator Live is defined by:

$$\text{Live}(\mathcal{T}) = \mathcal{T} \cup (\mathbb{S}^\infty \setminus \text{Safe}(\mathcal{T}))$$

**Definition** (Liveness property).  $\mathcal{T}$  is a liveness property if and only if it satisfies one of the three following equivalent statements:

- (i)  $\text{Live}(\mathcal{T}) = \mathcal{T}$
- (ii)  $\text{PCl}(\mathcal{T}) = \mathbb{S}^*$  (any finite trace is the prefix of a trace in  $\mathcal{T}$ )
- (iii)  $\text{Lim} \circ \text{PCl}(\mathcal{T}) = \mathbb{S}^\infty$

*Proof:* (i)  $\implies$  (ii). We assume that  $\text{Live}(\mathcal{T}) = \mathcal{T}$ , i.e. that  $\mathcal{T} \cup (\mathbb{S}^\infty \setminus \text{Safe}(\mathcal{T})) = \mathcal{T}$ . Therefore,  $\mathbb{S}^\infty \setminus \text{Safe}(\mathcal{T}) \subseteq \mathcal{T}$ .

Let  $\sigma \in \mathbb{S}^*$  and let us show that  $\sigma \in \text{PCl}(\mathcal{T})$ . Since we must have  $\sigma \in \mathbb{S}^\infty$ :

- either  $\sigma \in \text{Safe}(\mathcal{T}) = \text{Lim}(\text{PCl}(\mathcal{T}))$ , so all its prefixes are in  $\text{PCl}(\mathcal{T})$  and  $\sigma \in \text{PCl}(\mathcal{T})$ ;
- or  $\sigma \in \mathcal{T}$  which implies that  $\sigma \in \text{PCl}(\mathcal{T})$ .

□

*Proof:* (ii)  $\implies$  (iii). If  $\text{PCl} \mathcal{T} = \mathbb{S}^*$ , then  $\text{Lim} \circ \text{PCl}(\mathcal{T}) = \mathbb{S}^\infty$ .

□

*Proof:* (iii)  $\implies$  (i). If  $\text{Lim} \circ \text{PCl}(\mathcal{T}) = \mathbb{S}^\infty$ , then

$$\text{Live}(\mathcal{T}) = \mathcal{T} \cup (\mathbb{S}^\infty \setminus (\text{Lim} \circ \text{PCl}(\mathcal{T}))) = \mathcal{T} \cup (\mathbb{S}^\infty \setminus \mathbb{S}^\infty) = \mathcal{T}$$

□

**Example** (Termination). The property  $\mathcal{T} = \mathbb{S}^*$  corresponds to termination: there should be no infinite execution.  $\mathcal{T}$  clearly satisfies

$$\text{PCl}(\mathcal{T}) = \mathbb{S}^*$$

thus termination is indeed a liveness property according to the formal definition.

**Example.** Assume that  $\mathbb{S} = \{a, b, c\}$  and that  $\mathcal{T}$  is the set of states in which  $b$  has been visited after  $a$  has been visited.  $\mathcal{T}$  can be described by  $\mathcal{T} = \text{PCl}(\mathbb{S}^* \cdot a \cdot \mathbb{S}^* \cdot b \cdot \mathbb{S}^*)$ .

Then,  $\mathcal{T}$  is a liveness property. Let  $\sigma \in \mathbb{S}^*$ ; then  $\sigma \cdot a \cdot b \in \mathcal{T}$ , so  $\sigma \in \text{PCl} \mathcal{T}$ . Thus,  $\text{PCl} \mathcal{T} = \mathbb{S}^*$ , which corresponds to property (ii).

**Theorem** (Live is idempotent). If  $\mathcal{T}$  is a trace property, then  $\text{Live}(\mathcal{T})$  is a liveness property.

*Proof.* Let's show that  $\text{PCl} \circ \text{Live}(\mathcal{T}) = \mathbb{S}^*$ . Consider  $\sigma \in \mathbb{S}^*$ , we will prove that  $\sigma \in \text{PCl} \circ \text{Live}(\mathcal{T})$ . Note that:

$$\begin{aligned} \text{PCl} \circ \text{Live}(\mathcal{T}) &= \text{PCl}(\mathcal{T}) \cup \text{PCl}(\mathbb{S}^\infty \setminus \text{Safe}(\mathcal{T})) \\ &= \text{PCl}(\mathcal{T}) \cup \text{PCl}(\mathbb{S}^\infty \setminus \text{Lim} \circ \text{PCl}(\mathcal{T})) \end{aligned}$$

If  $\sigma \in \text{PCl}(\mathcal{T})$ , the result is obvious. If  $\sigma \notin \text{PCl}(\mathcal{T})$ , then  $\sigma \notin \text{Lim} \circ \text{PCl}(\mathcal{T})$  by definition of the limit, thus  $\sigma \in \mathbb{S}^\infty \setminus \text{Lim} \circ \text{PCl}(\mathcal{T})$ . Therefore,  $\sigma \in \text{PCl}(\mathbb{S}^\infty \setminus \text{Lim} \circ \text{PCl}(\mathcal{T}))$  as  $\text{PCl}$  is extensive when applied to sets of finite traces, which proves the above result. □

### 3.3.3 Termination proof with ranking function

In this section, we will only consider termination. Given a transition system  $\mathcal{S} = (\mathbb{S}, \rightarrow, \mathbb{S}_I)$ , and a liveness property  $\mathcal{T}$ , we look for a way of verifying that  $\mathcal{S}$  satisfies termination, i.e. that  $\llbracket \mathcal{S} \rrbracket^\infty \subseteq \mathbb{S}^*$ . To do so, we introduce the notion of ranking function.

**Definition** (Ranking function). A *ranking function* is a function  $\phi : \mathbb{S} \rightarrow E$  where  $(E, \sqsubseteq)$  is a *well-founded* ordering<sup>1</sup>, and

$$\forall s_1, s_2 \in \mathbb{S}, s_0 \rightarrow s_1 \implies \phi(s_1) \sqsubset \phi(s_0)$$

Intuitively, following a transition can only make  $\phi(s)$  strictly decrease, and  $\phi(s)$  cannot keep decreasing forever, since the order is well-founded.

**Theorem.** If  $\mathcal{S}$  has a ranking function  $\phi$ , it satisfies termination.

**Example.** We consider the termination of the array sum program. As seen on Figure 3.3.3,  $\phi$  is a ranking function, hence the transition system of our program satisfies termination.

	$\phi : \mathbb{S} \longrightarrow \mathbb{N}$
1 $s = 0;$	$(l_1, m) \longmapsto 3n + 6$
2 $i = 0;$	$(l_2, m) \longmapsto 3n + 5$
3 <b>while</b> ( $i < n$ ) {	$(l_3, m) \longmapsto 3n + 4$
4 $s = s + t[i];$	$(l_4, m) \longmapsto 3(n - m(i)) + 3$
5 $i = i + 1;$	$(l_5, m) \longmapsto 3(n - m(i)) + 2$
6 }	$(l_6, m) \longmapsto 3(n - m(i)) + 4$
	$(l_7, m) \longmapsto 0$

Figure 3: The program and the ranking function

### 3.3.4 Proof by variance

We consider a transition system  $\mathcal{S} = (\mathbb{S}, \rightarrow, \mathbb{S}_I)$ , and liveness property  $\mathcal{T}$ . Infinite traces semantics is the greatest fixpoint of  $F_\omega$ . We seek a way of verifying that  $\mathcal{S}$  satisfies  $\mathcal{T}$ , i.e. that  $\llbracket \mathcal{S} \rrbracket^\infty \subseteq \mathcal{T}$ .

**Definition** (Variance proofs). Let  $(\mathbb{I}_n)_{n \in \mathbb{N}}$  be elements of  $\mathbb{S}^\infty$ ; these are said to form a *variance proof* if and only if:

- $\mathbb{S}^\omega \subseteq \mathbb{I}_0$
- for all  $k \in 1, 2, \dots, \omega$ ,  $\forall s \in \mathbb{S}, \langle s \rangle \in \mathbb{I}_k$
- for all  $k \in 1, 2, \dots, \omega$ , there exists  $l < k$  such that  $F_\omega(\mathbb{I}_l) \subseteq \mathbb{I}_k$
- $\mathbb{I}_\omega \subseteq \mathcal{T}$

**Theorem.** The variance proof method is both sound and complete.

## 3.4 Decomposition of trace properties

**Theorem.** Let  $\mathcal{T} \subseteq \mathbb{S}^\infty$ ; it can be decomposed into the conjunction of safety property  $\text{Safe}(\mathcal{T})$  and liveness property  $\text{Live}(\mathcal{T})$ :

$$\mathcal{T} = \text{Safe}(\mathcal{T}) \cap \text{Live}(\mathcal{T})$$



```

s = 0;
i = 0;
while (i < n) {
    s = s + t[i];
    i = i + 1;
}

```

Figure 4: A simple program which computes the sum of the elements of  $t$

**Example.** Considering the same program: we try to prove its total correctness. We need to show that the program terminates, does not crash, and computes the sum of the elements in the array. We can apply the decomposition principle and express total correctness as the *conjunction of two proofs*: termination can be proved with a ranking function, and the absence of errors and result correctness can be proved with local invariants.

**Example.** Likewise, we consider a very simple greatest common divider code function:

```

int f(int a, int b) {
    while (a > 0) {
        int d = b/a;
        int r = r - a * d;
        b = a;
        a = r;
    }
    return b;
}

```

Figure 5: A simple program which computes the sum of the elements of  $t$

The specification of this function – that we want to prove – is: “When applied to positive integers, function  $f$  should always return their GCD”. The safety part is the conjunction of two properties: no runtime errors, and the final value of  $b$  is the GCD. The liveness part is termination on all traces starting with positive inputs.

## 3.5 A specification language: temporal logic

### 3.5.1 Notion of specification language

Ultimately, we would like to *verify* or *compute* properties, but so far, we simply describe properties with sets of executions or worse, natural language statements. Ideally, we would prefer to use a mathematical language for that, to gain in concision and avoid ambiguity.

**Definition** (Specification language). A specification language is a set of terms  $\mathbb{L}$  with an interpretation function (or semantics):

$$\llbracket \cdot \rrbracket : \mathbb{L} \rightarrow \mathcal{P}(\mathbb{S}^\infty)$$

### 3.5.2 A state specification language

We will consider the following example of a simple specification language.

---

<sup>1</sup>An ordering is well-founded when every non-empty subset of  $E$  has a minimal element for  $\sqsubseteq$ .

**Definition.** Syntax: we let terms of  $\mathcal{L}_{\mathbb{S}}$  be defined by:

$$p \in \mathcal{L}_{\mathbb{S}} ::= @l | x < x' | x < n | \neg p' | p' \wedge p'' | \Omega$$

Semantics:  $\llbracket p \rrbracket_s \subseteq \mathbb{S}_{\Omega}$  is defined by:

$$\begin{aligned} \llbracket @l \rrbracket_s &= \{l\} \times \mathbb{M} \\ \llbracket x \leq x' \rrbracket_s &= \{ (l, m) \in \mathbb{S} \mid m(x) \leq m(x') \} \\ \llbracket x \leq n \rrbracket_s &= \{ (l, m) \in \mathbb{S} \mid m(x) \leq n \} \\ \llbracket \neg p \rrbracket_s &= \mathbb{S}_{\Omega} \setminus \llbracket p \rrbracket_s \\ \llbracket p \wedge p' \rrbracket_s &= \llbracket p \rrbracket_s \cap \llbracket p' \rrbracket_s \\ \llbracket \Omega \rrbracket_s &= \{\Omega\} \end{aligned}$$

This specification language allows to formally express state properties.

**Example** (Unreachability of control state  $l_0$ ).

$$\llbracket \neg @l_0 \rrbracket_s = \mathbb{S}_{\Omega} \setminus \{ (l_0, m) \mid m \in \mathbb{M} \}$$

**Example** (Absence of runtime errors).

$$\llbracket \neg \Omega \rrbracket_s = \mathbb{S}_{\Omega} \setminus \{\Omega\} = \mathbb{S}$$

**Example** (Intermittent invariant). The principle is to attach a local invariant to each control state.

1	<b>if</b> ( $x \geq 0$ ) {	$@l_1 \implies x \geq 0$
2	$y = x$ ;	$@l_2 \implies x \geq 0 \wedge y \geq 0$
3	<b>}</b> <b>else</b> {	$@l_2 \implies x < 0$
4	$y = -x$ ;	$@l_4 \implies x < 0 \wedge y > 0$
5	<b>}</b>	$@l_5 \implies y \geq 0$

Figure 6: The program and the local invariants

### 3.5.3 Propositional temporal logic

We now consider the specification of trace properties, using temporal logic. Temporal logic specifies the properties in terms of events that occur at distinct times in the execution (hence the name “temporal”). There are many instances of temporal logic, but we will study a simple one: Pnueli’s Propositional Temporal Logic.

**Definition** (Syntax of PTL (Propositional Temporal Logic)). Properties over traces are defined as terms of the form:

$$\begin{array}{ll} t \in \mathbb{L}_{\text{PTL}} ::= & p \quad (\text{state property, i. } p \in \mathbb{L}_{\mathbb{S}}) \\ & | \quad t' \vee t'' \quad (\text{disjunction}) \\ & | \quad \neg t' \quad (\text{negation}) \\ & | \quad \bigcirc t' \quad (\text{next}) \\ & | \quad t' \mathbin{\text{U}} t'' \quad (t' \text{ until } t'') \end{array}$$

Figure 7: Propositional Temporal Logical syntax

The semantics of a temporal property is a set of traces, and it is defined by induction over the syntax:

$$\begin{aligned}
\llbracket p \rrbracket_t &= \{ s \cdot \sigma \mid s \in \llbracket p \rrbracket_s \wedge \sigma \in \mathbb{S}^\infty \} \\
\llbracket t_0 \vee t_1 \rrbracket_t &= \llbracket t_0 \rrbracket_t \cup \llbracket t_1 \rrbracket_t \\
\llbracket \neg t_0 \rrbracket_t &= \mathbb{S}^\infty \setminus \llbracket t_0 \rrbracket_t \\
\llbracket \bigcirc t_0 \rrbracket_t &= \{ s \cdot \sigma \mid s \in \mathbb{S} \wedge \sigma \in \llbracket t_0 \rrbracket_t \} \\
\llbracket t_0 \mathcal{U} t_1 \rrbracket_t &= \{ \sigma \in \mathbb{S}^\infty \mid \exists n \in \mathbb{N}, \forall i < n, \sigma_i \in \llbracket t_0 \rrbracket_t \wedge \sigma_n \in \llbracket t_1 \rrbracket_t \}
\end{aligned}$$

Many useful operators can be added:

- Boolean constants:

$$\text{true} ::= (x < 0) \wedge \neg(x < 0)$$

$$\text{false} ::= \neg \text{true}$$

- Sometime (there exists a rank  $n$  at which  $t$  holds):

$$\Diamond t ::= \text{true} \mathcal{U} t$$

- Always (there is no rank at which the negation of  $t$  holds):

$$\Box t ::= \neg(\Diamond(\neg t))$$

One can combine these operators to construct interesting properties:  $\Diamond\Box t$  means that  $t$  is true starting from a certain rank, and  $\Box\Diamond t$  means that  $t$  happens an infinite number of times.

## 3.6 Beyond safety and liveness

We now consider other interesting properties of programs, and show that they do not all reduce to trace properties.

For example, one can think about security properties. We can consider just one: “An unauthorized observer should not be able to guess anything about private information by looking at public information”.

### 3.6.1 Assumptions

We let  $\mathcal{S} = (\mathbb{S}, \rightarrow, \mathbb{S}_{\mathcal{I}})$  be a transition system. Its states are of the form  $(l, m) \in \mathbb{L} \times \mathbb{M}$ . Memory states are of the form  $\mathbb{X} \rightarrow \mathbb{V}$ . We let  $l, l' \in \mathbb{L}$  be the program entry and exit, and  $x, x' \in \mathbb{X}$  be the private and public variables. We look at the following security property:

“Observing the value  $x'$  at  $l'$  gives no information on the value of  $x$  at  $l$ .”

### 3.6.2 Examples

- A secure program – no information flow, no way to guess  $x$ :

$$x' = 84;$$

- An insecure program – explicit information flow,  $x'$  gives a lot of information about  $x$ , so that we can simply recompute it:

$$x' = x - 2;$$

- An insecure program – implicit information flow, through a test:

$$\text{if}(x < 0)\{x' = 0;\}$$

How can we characterize information flow in the semantic level?

### 3.6.3 Non-interference

We consider the transformer  $\Phi$  defined by:

$$\begin{aligned}\Phi : \mathbb{M} &\longrightarrow \mathcal{P}(\mathbb{M}) \\ m &\longmapsto \{ m' \in \mathbb{M} \mid \exists \sigma = \langle (l, m), \dots, (l', m') \rangle \in \llbracket \mathcal{S} \rrbracket \}\end{aligned}$$

**Definition** (Non-interference). There is no interference between  $(l, x)$  and  $(l', x')$ , and we write  $(l', x') \not\sim (l, x)$ , if and only if the following property holds:

$$\forall m \in \mathbb{M}, \forall v_0, v_1 \in \mathbb{V}, \quad \{ m'(x') \mid m' \in \Phi(m[x \leftarrow v_0]) \} = \{ m'(x') \mid m' \in \Phi(m[x \leftarrow v_1]) \}$$

Intuitively, if two observations at point  $l$  differ only in the value of  $x$ , there is no difference in observation  $x'$  at  $l'$ . In other words, observing  $x'$  at  $l'$  (even on many executions) gives no information about the value of  $x$  at point  $l$ .

**Property 9.** Non-interference is not a trace property.

### 3.6.4 Dependence properties

Many notions of dependences can be defined, but we will consider just one:

what inputs may have an impact on the observation of a given output

This as many applications in reverse engineering, slicing, and corresponds to the negation of non-interference.

**Definition** (Interference). There is interference between  $(l, x)$  and  $(l', x')$ , and we write  $(l', x') \rightsquigarrow (l, x)$  if and only if the following property holds:

$$\exists x \in \mathbb{M}, \exists v_0, v_1 \in \mathbb{V}, \quad \{ m'(x') \mid m' \in \Phi(m[x \leftarrow v_0]) \} \neq \{ m'(x') \mid m' \in \Phi(m[x \leftarrow v_1]) \}$$

This expresses that there is at least one case, where the value of  $x$  at  $l$  has an impact on that of  $x'$  at  $l'$ . It may not hold even if the computation of  $x'$  reads  $x$ :

$$x' = 0 \times x;$$

**Property 10.** Interference property is not a trace property.

### 3.6.5 Hyperproperties

In conclusion, the absence of interference between  $(l, x)$  and  $(l', x')$  is not a trace property; one cannot describe as the set of programs the semantics of which is included into a given set of traces. It can however be described by a set of sets of traces: we simply collect the set of program semantics that satisfy the property. This is what we call a hyperproperty: trace hyperproperties are described by sets of sets of executions, while trace properties are described by sets of executions.

To disprove the absence of interference (i.e. to show there exists an interference), we simply need to exhibit two finite traces: this is called *2-safety*.

### 3.7 Conclusion

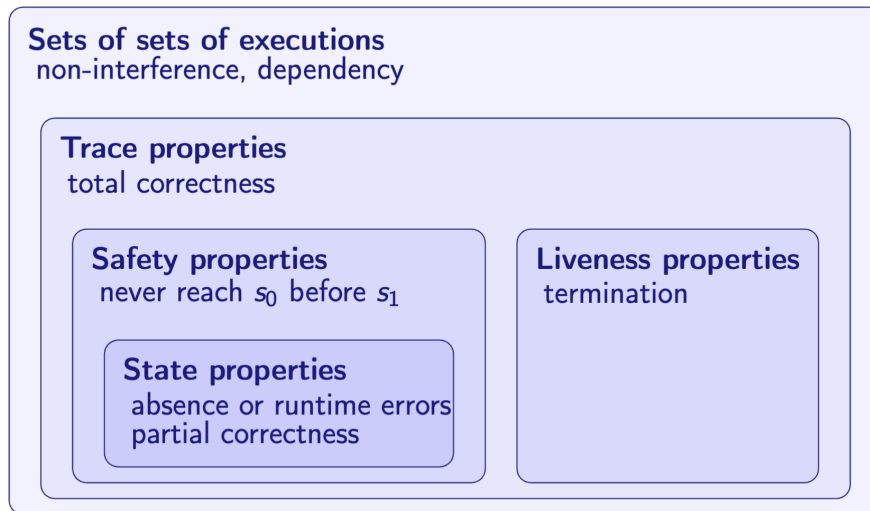


Figure 8: The Zoo of semantic properties

Trace properties allow to express a large range of program properties. Safety represents the absence of bad behaviors, while liveness expresses the existence of good behaviors. Trace properties can be decomposed as conjunctions of safety and liveness properties, with dedicated proof methods. Nevertheless, some interesting properties are not trace properties: we saw that security properties are sets of sets of executions. Finally, the notion of specification languages is useful to describe program properties.

## 4 Denotational semantics

The first chapters of this class focused on *operational semantics*, through state and trace properties. Such semantics are defined as small execution steps, over low-level internal configurations, with transitions chained to define maximal traces.

In this chapter, we will delve into denotational semantics. We will study direct function from programs to mathematical objects (denotations), defined by induction on the program syntax (compositional), ignoring intermediate steps and execution details. This is a higher-level, more abstract and modular approach, trying to decouple a program meaning from its execution, and focused on the mathematical structures that represent programs.

Consider the following two programs:

```
int swapped;
do {
    swapped = 0;
    for (int i=1; i<n; i++) {
        if (a[i-1]>a[i]) {
            swap(&a[i-1], &a[i]);
            swapped = 1;
        }
    }
} while (swapped);

let rec sort = function
| [] -> []
| a::rest ->
    let lo, hi = List.partition
        (fun y -> y < x) rest
    in
    (sort lo) @ [x] @ (sort hi)
```

Figure 9: Bubble sort in C (left) and quicksort in OCaml (right)

These programs use different languages, algorithms, programming principles and data types; nevertheless, can we give them the same semantics?

We will study two denotation worlds:

- Imperative programs, which mutate a memory state. The natural denotation of such a program is an input/output function, over a domain  $\mathcal{D} \simeq \text{memory} \rightarrow \text{memory}$ .
- Functional programs, returning a value without any side effect. One can model a program of type  $a \rightarrow b$  as a function  $\mathcal{D}_a \rightarrow \mathcal{D}_b$ .

## 4.1 Deterministic imperative programs

### 4.1.1 Syntax

We now consider a simple imperative language: IMP. Variables are in a fixed set:  $X \in \mathbb{V}$ . Constants are defined as a set  $\mathbb{I} := \mathbb{B} \cup \mathbb{Z}$ , containing booleans  $\mathbb{B} := \{\text{true}, \text{false}\}$  and integers  $\mathbb{Z}$ . Furthermore, we include unary and binary operators, represented by  $\Diamond$ . Such operators can be integer operations  $+$ ,  $-$ ,  $\times$ ,  $/$ ,  $<$ ,  $\leq$ , boolean operations  $\neg$ ,  $\wedge$ ,  $\vee$ , or polymorphic operations  $=$ ,  $\neq$ .

$\text{expr} ::= X$	<i>(variable)</i>	$\text{stat} ::= \text{skip}$	<i>(do nothing)</i>
$c$	<i>(constant)</i>	$X \leftarrow \text{expr}$	<i>(assignment)</i>
$\Diamond \text{ expr}$	<i>(unary op.)</i>	$\text{stat}; \text{stat}$	<i>(sequence)</i>
$\text{expr} \Diamond \text{ expr}$	<i>(binary op.)</i>	<b>if</b> $e$ <b>then</b> $s$ <b>else</b> $s$	<i>(conditional)</i>
		<b>while</b> $\text{expr}$ <b>do</b> $\text{stat}$	<i>(loop)</i>

Figure 10: IMP expressions and statements

### 4.1.2 Semantics

**Expression semantics** For expressions, we define a function  $E$  such that:

$$E[\![ \text{expr} ]\!] : \mathcal{E} \rightarrow \mathbb{I}$$

where  $\mathcal{E} := \mathbb{V} \rightarrow \mathbb{I}$  is an environment mapping variables in  $\mathbb{V}$  to values in  $\mathbb{I}$ . The symbol  $\rightarrow$  denotes a partial function: it is necessary because some operations are undefined for typing reasons ( $1 + \text{true}$ ,  $1 \wedge 2$ ), or are undefined ( $3/0$ ,  $\dots$ ). We will define  $E$  by structural induction on the abstract syntax tree.

$E\llbracket c \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$c$	$\in \mathbb{I}$
$E\llbracket V \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$\rho(V)$	$\in \mathbb{I}$
$E\llbracket -e \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$-v$	$\in \mathbb{Z}$ if $v = E\llbracket e \rrbracket \rho \in \mathbb{Z}$
$E\llbracket \neg e \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$\neg v$	$\in \mathbb{B}$ if $v = E\llbracket e \rrbracket \rho \in \mathbb{B}$
$E\llbracket e_1 + e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 + v_2$	$\in \mathbb{Z}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{Z}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{Z}$
$E\llbracket e_1 - e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 - v_2$	$\in \mathbb{Z}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{Z}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{Z}$
$E\llbracket e_1 \times e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 \times v_2$	$\in \mathbb{Z}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{Z}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{Z}$
$E\llbracket e_1 / e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 / v_2$	$\in \mathbb{Z}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{Z}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{Z} \setminus \{0\}$
$E\llbracket e_1 \wedge e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 \wedge v_2$	$\in \mathbb{B}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{B}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{B}$
$E\llbracket e_1 \vee e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 \vee v_2$	$\in \mathbb{B}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{B}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{B}$
$E\llbracket e_1 < e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 < v_2$	$\in \mathbb{B}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{Z}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{Z}$
$E\llbracket e_1 \leq e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 \leq v_2$	$\in \mathbb{B}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{Z}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{Z}$
$E\llbracket e_1 = e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 = v_2$	$\in \mathbb{B}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{I}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{I}$
$E\llbracket e_1 \neq e_2 \rrbracket \rho$	$\stackrel{\text{def}}{=}$	$v_1 \neq v_2$	$\in \mathbb{B}$ if $v_1 = E\llbracket e_1 \rrbracket \rho \in \mathbb{I}, v_2 = E\llbracket e_2 \rrbracket \rho \in \mathbb{I}$

Figure 11: Expression semantics

**Statement semantics** Similarly, we define for statements a function  $S$  of the form:

$$S\llbracket \text{stat} \rrbracket : \mathcal{E} \rightarrow \mathcal{E}$$

It maps an environment before the statement to an environment after the statement. Likewise, it is a partial function due to errors in expressions or non-termination. We also define it by structural induction as described in what follows:

- **Skip** – do nothing:

$$S\llbracket \text{skip} \rrbracket \rho := \rho$$

- **Assignment** – evaluate expression and mutate environment:

$$S\llbracket X \leftarrow e \rrbracket \rho := \rho[X \mapsto v] \quad \text{if } E\llbracket e \rrbracket \rho = v$$

where  $f[x \mapsto y]$  denotes the function that maps  $x$  to  $y$ , and any  $z \neq x$  to  $f(z)$ .

- **Sequence** – function composition:

$$S\llbracket s_1; s_2 \rrbracket := S\llbracket s_2 \rrbracket \circ S\llbracket s_1 \rrbracket$$

- **Conditional**:

$$S\llbracket \text{if } e \text{ then } s_2 \text{ else } s_1 \rrbracket \rho := \begin{cases} S\llbracket s_1 \rrbracket \rho & \text{if } E\llbracket e \rrbracket \rho = \text{true} \\ S\llbracket s_2 \rrbracket \rho & \text{if } E\llbracket e \rrbracket \rho = \text{false} \\ \text{undefined} & \text{otherwise} \end{cases}$$

- **Loops** – the semantics of loops must satisfy:

$$S\llbracket \text{while } e \text{ do } s \rrbracket = \begin{cases} \rho & \text{if } E\llbracket e \rrbracket \rho = \text{false} \\ S\llbracket \text{while } e \text{ do } s \rrbracket (S\llbracket s \rrbracket \rho) & \text{if } E\llbracket e \rrbracket \rho = \text{true} \\ \text{undefined} & \text{otherwise} \end{cases}$$

This is a recursive definition, and we must therefore prove that the equation has solution(s); in case there are several solutions, there is a single “right” one. To do so, we will use fixpoints of operators over partially ordered sets.

**Fixpoint semantics of loops** We introduce the *flat ordering*  $(\mathbb{I}_\perp, \sqsubseteq)$  on  $\mathbb{I}$ :

#### 4.1.3 Error vs. non-termination

### 4.2 Non-determinism

### 4.3 Link between operational and denotational semantics

### 4.4 Higher-order programs

### 4.5 Recursive domain equations

## 5 Axiomatic semantics

### 5.1 Introduction

### 5.2 Specifications

#### 5.2.1 An example of function specification

Consider the following program:

```
int mod(int A, int B) {  
    int Q = 0;  
    int R = A;  
    while (R >= B) {  
        R = R - B;  
        Q = Q + 1;  
    }  
    return R;  
}
```

Figure 12: An example of mod function

### 5.3 Floyd-Hoare logic

#### 5.3.1 Hoare triples and a small language

**Definition** (Hoare triple). A Hoare triple is of the form:

$$\{P\} \text{ prog } \{Q\}$$

where  $P$  (the *precondition*) and  $Q$  (the *postcondition*) are *logical assertions* over program variables, and **prog** is a program fragment. As an example, we could have:

$$P := (X \geq 0 \wedge Y \geq 0) \vee (X < 0 \wedge Y < 0)$$

A triple means that if  $P$  holds before **prog** is executed, then  $Q$  holds after the execution of **prog**, unless **prog** does not terminate or encounters an error. It is important to note that  $\{P\} \text{ prog } \{Q\}$  expresses *partial correctness*, but does not rule out errors and non-termination. Hoare triples serve as judgements in a proof system.



**Language** In what follow, we will consider the language defined by the following grammar:

stat ::=	X ← expr	(assignment)
	skip	(do nothing)
	fail	(error)
	stat; stat	(sequence)
	if expr then stat else stat	(conditional)
	while expr do stat	(loop)

Figure 13: The formal grammar of the considered language statements

where  $X \in \mathbb{V}$  are integer-valued variables, **expr** are integer arithmetic expressions, which we assume to be deterministic and causing no error. For instance, this can be achieved by assuming that all divisions are explicitly guarded by an **if** construction.

### 5.3.2 Hoare rules

We now introduce Hoare rules to infer Hoare triples.

**Axioms** Intuitively, any property true before **skip** is true afterwards. We formalize this using the following axiom:

$$\frac{}{\{P\} \text{ skip } \{P\}} \text{Ax.}$$

Similarly, any property is true after fail:

$$\frac{}{\{P\} \text{ fail } \{Q\}} \text{Ax.}$$

Finally, we can introduce the *assignment axiom*: for  $P$  over  $X$  to be true after  $X \leftarrow e$ ,  $P$  must be true over  $e$  before the assignment:

$$\frac{}{\{P[e/X]\} X \leftarrow e \{P\}} \text{Ax.}$$

where  $P[e/X]$  is  $P$  in which all free occurrences of  $X$  are replaced with  $e$ . Note that  $e$  must be deterministic, and that the rule is *backwards*:  $P$  appears as a postcondition.

**Example** (Assignment examples).

$$\begin{aligned} & \{\text{true}\} X \leftarrow 5 \{X = 5\} \\ & \{Y = 5\} X \leftarrow Y \{X = 5\} \\ & \{X + 1 \geq 0\} X \leftarrow X + 1 \{X \geq 0\} \\ & \{\text{false}\} X \leftarrow Y + 3 \{Y = 0 \wedge X = 12\} \\ & \{Y \in [0, 10]\} X \leftarrow Y + 3 \{X + Y + 2 \wedge Y \in [0, 10]\} \end{aligned}$$

**Consequence (weakening)** The rule of consequence expresses that we can weaken a Hoare triple by both weakening its postcondition  $Q \Leftarrow Q'$  and strengthening its precondition  $P \Rightarrow P'$ . We assume a logic system to be available to prove formulas on assertions, such as  $P \Rightarrow P'$ .

$$\frac{P \Rightarrow P' \quad Q' \Rightarrow Q \quad \{P'\} \text{ c } \{Q'\}}{\{P\} \text{ c } \{Q\}} \text{Weak.}$$

**Example** (Consequence rule). The axiom for **fail** can be replaced with:

$$\frac{}{\{\text{true}\} \text{ fail } \{\text{false}\}} \text{Ax.}$$

as  $P \implies \text{true}$  and  $\text{false} \implies Q$  always hold. Another example is:

$$\{X = 99 \wedge Y \in [1, 10]\} X \leftarrow Y + 10 \{X = Y + 10 \wedge Y \in [1, 10]\}$$

**Tests** To prove that  $Q$  holds after a test, we prove that it holds after each branch  $(s, t)$  under the assumption that the branch is executed, i.e.  $e$  or  $\neg e$ :

$$\frac{\{P \wedge e\} s \{Q\} \quad \{P \wedge \neg e\} t \{Q\}}{\{P\} \text{ if } e \text{ then } s \text{ else } t \{Q\}} \text{Test}$$

**Example** (Test rule).

$$\frac{\frac{\frac{}{\{X < 0\} X \leftarrow -X \{X > 0\}} \text{Ax.}}{\{(X \neq 0)\} X \leftarrow -X \{X > 0\}} \text{Weak.} \quad \frac{\frac{}{\{X > 0\} \text{ skip } \{X > 0\}} \text{Ax.}}{\{(X \neq 0) \wedge (X \geq 0)\} \text{ skip } \{X > 0\}} \text{Weak.}}{\{X \neq 0\} \text{ if } X < 0 \text{ then } X \leftarrow -X \text{ else skip } \{X > 0\}} \text{Test}$$

**Sequences** To prove a sequence  $s; t$ , we must invent an intermediate assertion  $R$  implied by  $P$  after  $s$ , and implying  $Q$  after  $t$ :

$$\frac{\{P\} s \{R\} \quad \{R\} t \{Q\}}{\{P\} s; t \{Q\}} \text{Seq.}$$

This is also often denoted by  $\{P\} s \{R\} t \{Q\}$ .

**Example** (Sequence rule).

$$\{X = 1 \wedge Y = 1\} X \leftarrow X + 1 \{X = 2 \wedge Y = 1\} Y \leftarrow Y - 1 \{X + 1 \wedge Y = 0\}$$

**Loops** To prove that  $P$  – true before a loop – still holds after the loop, we can prove that  $P$  is a loop invariant:  $P$  holds before each loop iteration, before even testing  $e$ . This is expressed with the following rule:

$$\frac{\{P \wedge e\} s \{P\}}{\{P\} \text{ while } e \text{ do } s \{P \wedge \neg e\}} \text{Loop}$$

In practice, we would rather prove the triple  $\{P\} \text{ while } e \text{ do } s \{Q\}$ . To do so, it is sufficient to invent an assertion  $I$  that:

- holds when the loop starts:  $P \implies I$
- is invariant by the body  $s$ :  $\{I \wedge e\} s \{I\}$
- implies the assertion when the loop stops:  $(I \wedge \neg e) \implies Q$

We can therefore derive the rule:

$$\frac{P \implies I \quad I \wedge \neg e \implies Q \quad \frac{\{I \wedge e\} s \{I\}}{\{I\} \text{ while } e \text{ do } s \{I \wedge \neg e\}} \text{Inv.}}{\{P\} \text{ while } e \text{ do } s \{Q\}}$$

**Remark.** Hoare logic is parametrized by the choice of logical theory of assertions. The logical theory is used both to prove properties of the form  $P \implies Q$ , and to simplify formulas by replacing a formula with a simpler one, equivalent in a logical sense ( $\iff$ ).

In practice, we often use first order theories, such as booleans  $(\mathbb{B}, \neg, \wedge, \vee)$ , Peano arithmetic  $(\mathbb{N}, +, \times)$ , ZF set theory or theory of arrays. Theories have different expressiveness, decidability and complexity results. This is an important factor when trying to automate program verification.

### 5.3.3 Summary of Hoare rules

$$\begin{array}{c}
\frac{}{\{P\} \text{ skip } \{P\}} \text{Ax.} \quad \frac{}{\{P\} \text{ fail } \{Q\}} \text{Ax.} \quad \frac{}{\{P[e/X]\} X \leftarrow e \{P\}} \text{Ax.} \\
\\
\frac{\{P \wedge e\} s \{Q\} \quad \{P \wedge \neg e\} t \{Q\}}{\{P\} \text{ if } e \text{ then } s \text{ else } t \{Q\}} \text{Test} \quad \frac{\{P\} s \{R\} \quad \{R\} t \{Q\}}{\{P\} s; t \{Q\}} \text{Seq.} \\
\\
\frac{P \implies P' \quad Q' \implies Q \quad \{P'\} c \{Q'\}}{\{P\} c \{Q\}} \text{Weak.} \quad \frac{\{P \wedge e\} s \{P\}}{\{P\} \text{ while } e \text{ do } s \{P \wedge \neg e\}} \text{Loop}
\end{array}$$

### 5.3.4 Proof tree example

### 5.3.5 Invariants and inductive invariants

### 5.3.6 Auxiliary variables

Auxiliary variables are mathematical variables that do not appear in the program. They are constant during the program execution. Such objects are widely used to simplify proofs, achieve more properties and relative completeness on extended languages (concurrency, recursive procedures).

**Example.** We consider the simple program:

**if**  $X < Y$  **then**  $Y \leftarrow X$  **else skip**

and we want to express the fact that at the end of the execution,  $Y$  is equal to the minimum value between the initial values of  $X$  and  $Y$ . We might use the following auxiliary-variables-free Hoare triple to express such a behavior:

$$\{\text{true}\} \text{ if } X < Y \text{ then } Y \leftarrow X \text{ else skip } \{Y = \min(X, Y)\}$$

Nevertheless, this is much less useful as a specification of a min function; indeed, even though the expected triple holds, the following also holds:

$$\{\text{true}\} Y \leftarrow X + 1 \{Y = \min(X, Y)\}$$

Any program ending with  $Y < X$  satisfies the conditions. To express a more interesting property – the fact that  $Y$  ends with a value that was present initially – we introduce auxiliary variables:

$$\{X = x \wedge Y = y\} \text{ if } X < Y \text{ then } Y \leftarrow X \text{ else skip } \{Y = \min(x, y)\}$$

### 5.3.7 Link with denotational semantics

## 5.4 Dijkstra's predicate calculus

We will now study *predicate calculus*, which is calculus to derive preconditions from postconditions. It orders and mechanizes the search for intermediate assertions. Note that similarly to manual techniques, it is easier to go backwards, mainly due to assignments.

### 5.4.1 Dijkstra's weakest liberal preconditions

**Definition** (Weakest liberal preconditions). We denote by  $wlp$  the function such that:

$$wlp : (\text{prog} \times \text{Prop}) \longrightarrow \text{Prop}$$

and that  $wlp(c, P)$  is the weakest, i.e. most general, precondition ensuring that  $\{wlp(c, P)\} c \{P\}$  holds. Said otherwise, it is the greatest set that ensures that the computation ends up in  $P$ . Formally:

$$\{P\} c \{Q\} \iff (P \implies wlp(c, Q))$$

“Liberal” means that we do not care about termination and errors.

**Example.**

$$\begin{aligned} wlp(X \leftarrow X + 1, X = 1) &= (X = 0) \\ wlp(\textbf{while } X < 0 \textbf{ do } X \leftarrow X + 1, X \geq 0) &= \text{true} \\ wlp(X \leftarrow X + 1, X \geq 0) &= \text{true} \end{aligned}$$

Note how in cases 2 and 3 we did not take into account non-terminating cases.

**Definition** (Calculus definition for  $wlp$ ).

$$\begin{aligned} wlp(\textbf{skip}, P) &:= P \\ wlp(\textbf{fail}, P) &:= \text{true} \\ wlp(X \leftarrow e, P) &:= P[e/X] \\ wlp(s; t, P) &:= wlp(s, wlp(t, P)) \\ wlp(\textbf{if } e \textbf{ then } s \textbf{ else } t, P) &:= (e \implies wlp(s, P)) \wedge (\neg e \implies wlp(t, P)) \\ wlp(\textbf{while } e \textbf{ do } s, P) &:= I \wedge ((e \wedge I) \implies wlp(s, I)) \wedge ((\neg e \wedge I) \implies P) \end{aligned}$$

Note that **while** loops require providing an invariant predicate  $I$ . Intuitively,  $wlp$  checks that  $I$  is an inductive invariant implying  $P$ . If so, it returns  $I$ , otherwise it returns false.  $wlp$  is the weakest precondition only if  $I$  is well-chosen.

**Example.** We will compute the weakest liberal precondition of a simple program using the inductive definition of  $wlp$ :

$$\begin{aligned} &wlp(\textbf{if } X < 0 \textbf{ then } Y \leftarrow -X \textbf{ else } Y \leftarrow X, Y \geq 10) \\ &= (X < 0 \implies wlp(Y \leftarrow -X, Y \geq 10)) \wedge (X \geq 0 \implies wlp(Y \leftarrow X, Y \geq 10)) \\ &= (X < 0 \implies -X \geq 10) \wedge (X \geq 0 \implies X \geq 10) \\ &= (X \geq 0 \wedge -X \geq 10) \wedge (X < 0 \vee X \geq 10) \\ &= X \geq 10 \vee X \leq -10 \end{aligned}$$

As seen in this example, this definition generates complex formulas. It is important to simplify them from time to time to avoid huge computational costs.

In what follows,  $A \equiv B$  means that formulas  $A$  and  $B$  are equivalent. It is stronger than syntactic equality. Formally,

$$\forall \rho, \quad \rho \models A \iff \rho \models B$$

**Property 11** (Excluded miracle).

$$\text{wlp}(c, \text{false}) \equiv \text{false}$$

**Property 12** ( $\wedge$ -distributivity).

$$\text{wlp}(c, P) \wedge \text{wlp}(d, Q) \equiv \text{wlp}(c, P \wedge Q)$$

**Property 13** ( $\vee$ -distributivity).

$$\text{wlp}(c, P) \vee \text{wlp}(d, Q) \equiv \text{wlp}(c, P \vee Q)$$

Note that  $\implies$  is always true, while  $\impliedby$  is only true for deterministic, error-free programs.

**Property 14** (Monotonicity).

$$(P \implies Q) \implies (\text{wlp}(c, P) \implies \text{wlp}(c, Q))$$

#### 5.4.2 Strongest liberal postconditions

Similarly, we can define the strongest liberal postcondition.

**Definition.** We denote by  $\text{slp}$  the function such that:

$$\text{slp} : (\text{prog} \times \text{Prop}) \longrightarrow \text{Prop}$$

and that  $\text{slp}(c, P)$  is the strongest, i.e. most general, postcondition ensuring that  $\{P\} \text{ } c \text{ } \{\text{slp}(P, c)\}$  holds. Said otherwise, it is the smallest state set. Formally:

$$\{P\} \text{ } c \text{ } \{Q\} \iff (\text{slp}(P, c) \implies Q)$$

Like  $\text{wlp}$ ,  $\text{slp}$  does not care about non-termination. It allows forward reasoning.

**Property 15** (Duality).

$$(P \implies \text{wlp}(c, Q)) \iff (\text{slp}(P, c) \implies Q)$$

**Definition** (Calculus definition for  $\text{slp}$ ).

$$\begin{aligned} \text{slp}(P, \text{skip}) &:= P \\ \text{slp}(P, \text{fail}) &:= \text{false} \\ \text{slp}(P, X \leftarrow e) &:= \exists v, P[v/X] \wedge X = e[v/X] \\ \text{slp}(P, s; t) &:= \text{slp}(\text{slp}(s, P), t) \\ \text{slp}(P, \text{if } e \text{ then } s \text{ else } t) &:= \text{slp}(P \wedge e, s) \vee \text{slp}(P \wedge \neg e, t) \\ \text{slp}(P, \text{while } e \text{ do } s) &:= (P \implies I) \wedge (\text{slp}(I \wedge e, s) \implies I) \wedge (\neg e \wedge I) \end{aligned}$$

Similarly, **while** loops require providing an invariant predicate  $I$ .

## 5.5 Verification conditions

### 5.5.1 Verification condition generation

The previously introduced notions define a framework to verify programs. How can we automate program verification using logic? Hoare logic is a deductive system and can therefore only automate the checking of proofs. Predicate transformers, such as *wlp* and *slp* calculus, allow constructing (big) formulas mechanically, but invention is still needed, for example in loops.

Such tools can nevertheless be used in verification condition generation: a verifier takes as input a program with annotations, such as contracts and loop invariants, and generates mechanically logic formulas ensuring the correctness. It reduces the input to a mathematical problem, which contains no reference to a program. Finally, an automatic SAT/SMT solver or an interactive theorem prover is used to discharge (prove) the formulas.

To do so, we slightly modify the grammar of statements to introduce optional assertions at any point:

$$\begin{aligned}
 \text{stat} &::= X \leftarrow \text{expr} \\
 &\quad | \text{skip} \\
 &\quad | \text{fail} \\
 &\quad | \text{stat}; \text{stat} \\
 &\quad | \text{if expr then stat else stat} \\
 &\quad | \text{while expr do stat} \\
 &\quad | \text{assert expr} \quad (\text{assertion}) \\
 \\
 \text{prog} &::= \{\text{Prop}\} \text{ stat } \{\text{Prop}\}
 \end{aligned}$$

Figure 14: Statements with assertions and programs annotated with a contract

### 5.5.2 Verification algorithm

We define two OCaml-like functions,  $\text{vcg}_p$  and  $\text{vcg}_s$ , which inductively define an algorithm for verification condition generation.

**Program verification**  $\text{vcg}_p$  is of the form:

$$\text{vcg}_p : \text{prog} \rightarrow \mathcal{P}(\text{Prop})$$

Since  $\text{prog}$  contains only one construction, its definition is simply:

$$\text{vcg}_p(\{P\} \ c \ \{Q\}) := \text{let } (R, C) = \text{vcg}_s(c, Q) \text{ in } C \cup \{P \implies R\}$$

**Statement verification**  $\text{vcg}_s$  is of the form:  $\text{vcg}_s : (\text{stat} \times \text{Prop}) \rightarrow (\text{Prop} \times \mathcal{P}(\text{Prop}))$ , and is defined inductively by:

$$\begin{aligned}
 \text{vcg}_s(\text{skip}, Q) &:= (Q, \emptyset) \\
 \text{vcg}_s(X \leftarrow e, Q) &:= (Q[e/X], \emptyset) \\
 \text{vcg}_s(s; t, Q) &:= \text{let } (R, C) = \text{vcg}_s(t, Q) \text{ in} \\
 &\quad \text{let } (P, D) = \text{vcg}_s(s, R) \text{ in } (P, C \cup D) \\
 \text{vcg}_s(\text{if } e \text{ then } s \text{ else } t, Q) &:= \text{let } (S, C) = \text{vcg}_s(s, Q) \text{ in} \\
 &\quad \text{let } (T, D) = \text{vcg}_s(t, Q) \text{ in } ((e \implies S) \wedge (\neg e \implies T), C \cup D) \\
 \text{vcg}_s(\text{while}_{\{I\}} e \text{ do } s, Q) &:= \text{let } (R, C) = \text{vcg}_s(s, I) \text{ in } (I, C \cup \{(I \wedge e) \implies R, (I \wedge \neg e) \implies Q\}) \\
 \text{vcg}_s(\text{assert } e, Q) &:= (e \implies Q, \emptyset)
 \end{aligned}$$

We use *wlp* to infer assertions automatically when possible.  $\text{vcg}_s(c, P) = (P', C)$  propagates postconditions backwards and accumulates into  $C$  the verification conditions (from loops).

**Example.** Consider the following program:

$$X \leftarrow 1; I \leftarrow 0; \textbf{while } I < N \textbf{ do } (X \leftarrow 2X; I \leftarrow I + 1)$$

We want to prove that under the precondition  $\{N \geq 0\}$ , the postcondition  $\{X = 2^N\}$  holds. To do so, we anotate the loop with the invariant:

$$\{X = 2^I \wedge 0 \leq I \leq N\}$$

Applying the foredescribed algorithm, we obtain three verification cconditions:

$$\begin{aligned} C_1 &:= (X = 2^I) \wedge (0 \leq I \leq N) \wedge (I \geq N \implies X = 2^N) \\ C_2 &:= (X = 2^I) \wedge (0 \leq I \leq N) \wedge (I < N \implies 2X = 2^{I+1} \wedge 0 \leq I + 1 \leq N) \\ C_3 &:= N \geq 0 \implies 1 = 2^0 \wedge 0 \leq 0 \leq N \end{aligned}$$

These three conditions can be checked independently using a SAT solver.

### 5.5.3 What about real languages?

We described verification rules in a minimal example; in a real language such as C, the rules are not so simple. Take as an example the assignement rule:

$$\frac{}{\{P[e/X]\} X \leftarrow e \{P\}} \text{Ax.}$$

It requires that  $e$  has no effect, that there is no pointer aliasing, that  $e$  has no runtime error. . . Moreover, the operations in the program and in the logic may not match: for integers, logic uses  $\mathbb{Z}$  while computers use  $\mathbb{Z}/2^n\mathbb{Z}$ . The same remark applies to continuous sets (logic uses  $\mathbb{Q}$  or  $\mathbb{R}$  while program use floating-point numbers) which might cause rounding-related differences. Finally, a logic for pointers and dynamic allocation is also required.

Some problems can be circumvented: you can see for instance how solutions to such differences have be implemented in the tool Why3.

## 5.6 Total correctness (termination)

## 5.7 Non-determinism

## 5.8 Arrays

## 5.9 Concurrency

## 6 Complement: SAT and Satisfiability Modulo Theories (SMT)

Since 2000, SAT solvers have become extremely performant. SMT is the extension of SAT with clauses instead of simply variables.

### 6.1 Modern SAT solvers

#### 6.1.1 The SAT problem

Given a boolean formula without quantifiers, e.g.

$$(p \vee q \vee \neg r) \wedge (r \vee \neg p)$$

we try to find if this formula is satisfiable. Techniques to do so include truth tables, resolution-based procedures, backtracking-based procedure. Since the 80s, there's been a focus on variable selection heuristics, associated with search-pruning techniques (non-chronological backtracking, learning clauses): these are the CDCL algorithms (Conflicts-Driven Clause Learning). New techniques such as indexing and scoring have since emerged.

#### 6.1.2 Resolution

Resolution uses a proof-finder procedure, which tries to reduce the equation.

The state of the procedure is represented by a variable  $F$ , to which we can apply some rules (Resolve, Empty, Tauto, Subsume, Fail). This allows to construct a proof tree.

#### 6.1.3 DPLL

DPLL is a model-finder procedure that builds incrementally a model  $M$  for a CNF formula  $F$  by

- deducing the truth value of a literal  $l$  from  $M$  and  $F$  by Boolean Constraint Propagations (BCP):

If  $C \vee l \in F$  and  $M \models \neg C$  then  $l$  must be true

- guessing the truth value of an unassigned literal:

If  $M \cup \{l\}$  leads to a model for which  $F$  is unsatisfiable  
then *backtrack* and try  $M \cup \{\neg l\}$

Similarly, DPLL is a set of rules (Success, Unit, Decide, Backtrack, Fail).

#### 6.1.4 Clause conflict, backjump

DPLL remains slow: instead of backtracking, we would prefer to *backjump* multiple steps before, to the node where the conflict has indeed been decided. Conflicts are reflected by backjump clauses. Given a backjump clause  $C \vee l$ , backjumping can undo several decisions at once: it goes back to the assignment  $M$  where  $M \models \neg C$ .

#### 6.1.5 CDCL algorithm

Conflict-Driven Clause Learning SAT solvers (CDCL) add backjump clauses to  $M$  as learned clauses (or lemmas) to prevent future similar conflicts.

Lemmas can also be removed from  $M$ .

The algorithm now has two modes: search and resolution.



### 6.1.6 Heuristics, Two-watched literals

The Variable State Independent Decaying Sum (VSIDS) heuristic associates a score to each literal in order to select the literal with the highest score when Decide is used.

CDCL performances are tightly related to their learning clause management. Keeping too many clauses decrease the BCP efficiency, but cleaning out too many clauses break the overall learning benefit.

Quality measures for learning clauses are based on scores associated with learned clauses.

BCP represents 80% of SAT-solver runtime. The two watched literals technique assigns two non-false watched literals per clause. Only if one of the two watched literal becomes false, the clause is inspected. If the other watched literal is assigned to true, then do nothing. Otherwise, try to find another (unassigned) watched literal. If no such literal exists, then apply Backjump. If the only possible literal is the other watched literal of the clause, then apply Unit.

Its main advantages are that clauses are inspected only when watched literal are assigned, and that no updating is required when backjumping.

## 6.2 Satisfiability Modulo Theories (SMT)

### 6.2.1 Introduction

The fundamental goal of research in SAT was to be able to solve first-order logic.

The SMT problem asks to decide of the satisfiability of logical formulae with some specific theory symbols.

In theory, SMT solvers can take as an input any first order logic formula. In practice, most SMT solvers only take as input boolean formulas with only one theory (e.g. linear arithmetic over the integers).

**Example.**

$$f(f(f(x))) = x \wedge f(f(f(f(f(x))))) = x \wedge f(x) \neq x$$

$$x + y = 19 \wedge x - y = 7 \wedge x \neq 13$$

Let  $F$  be the following formula using arithmetic symbols:

$$x + y \geq 0 \wedge (x = z \implies y + z = -1) \wedge z > 3t$$

We want to know whether  $F$  is satisfiable. We use the following algorithm:

1. Conversion in CNF
2. Replace arithmetical constraints with boolean variables
3. Call a SAT solver: a model  $M$  is returned if the formula is satisfiable
4. Convert  $M$  to the original theory
5. Check whether  $M$  is coherent with the theory
6. If not, add  $\neg M$  to  $F$  and start over

Figure 15: Simple SMT procedure using a SAT solver

SMT encounters multiple difficulties:

- The size of formulas (up to multiple Go)
- The complexity of boolean structures (e.g. when the formulas encode logical circuits of microprocessors)
- The combination of Theories
- The efficiency of decision procedures
- Theories over mathematical objects more and more complex
- The handling of quantifiers

- 6.2.2 Small proof engines
- 6.2.3 SAT and decision procedures
- 6.2.4 Combination of decision procedures