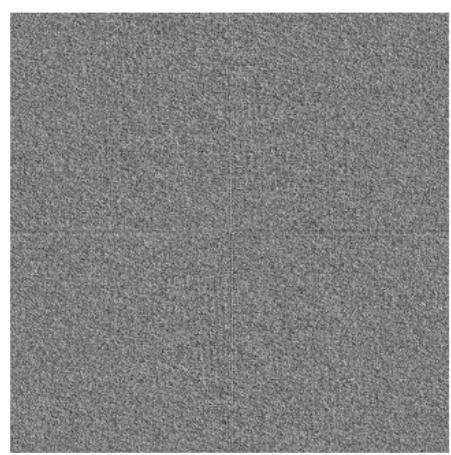


Module



Phase

