Title: **DAC**

File: DAC.SchDoc

Sheet: 1 of 4

Revision: B

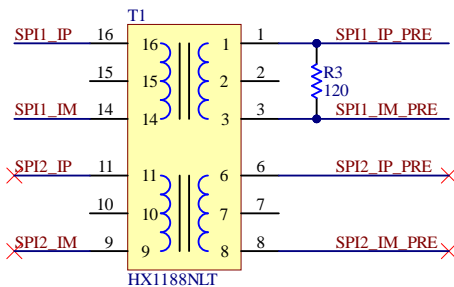
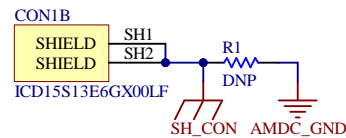
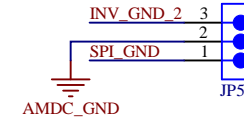
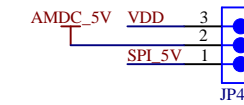
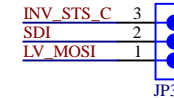
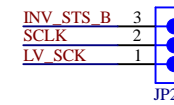
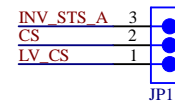
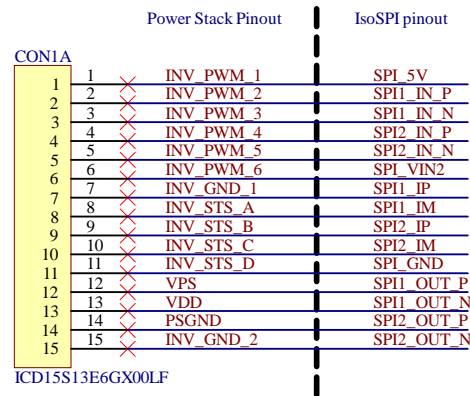
Time: 1:39:11 AM

Date: 7/1/2020

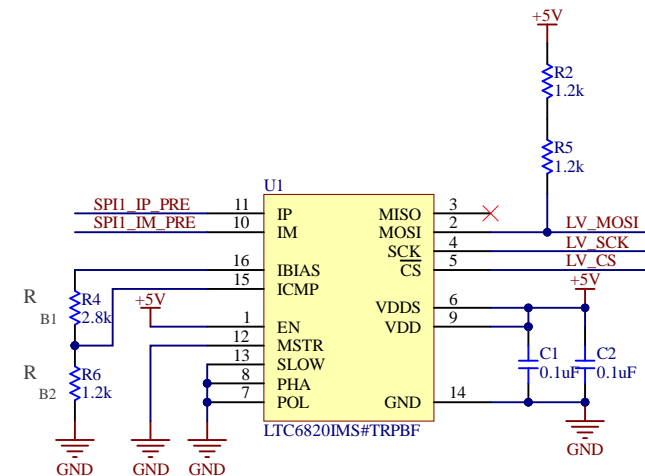
Engineer: Brandt Buchda

Severson Group
WEMPEC
UW-Madison

CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.



RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.



Title: **Input Connector**

File: InputConnector.SchDoc

Revision: B

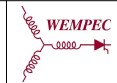
Time: 1:39:11 AM

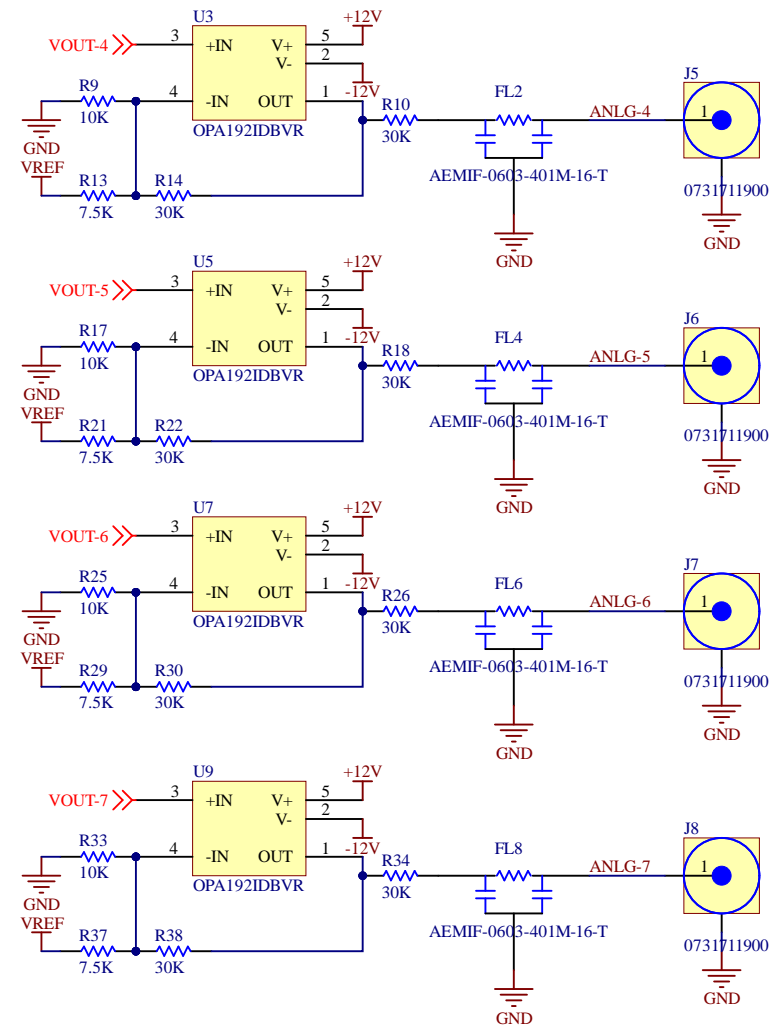
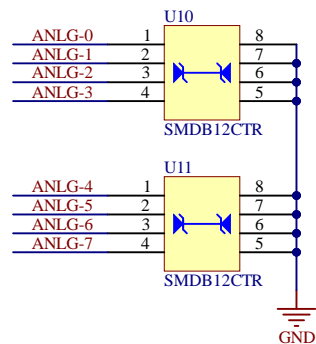
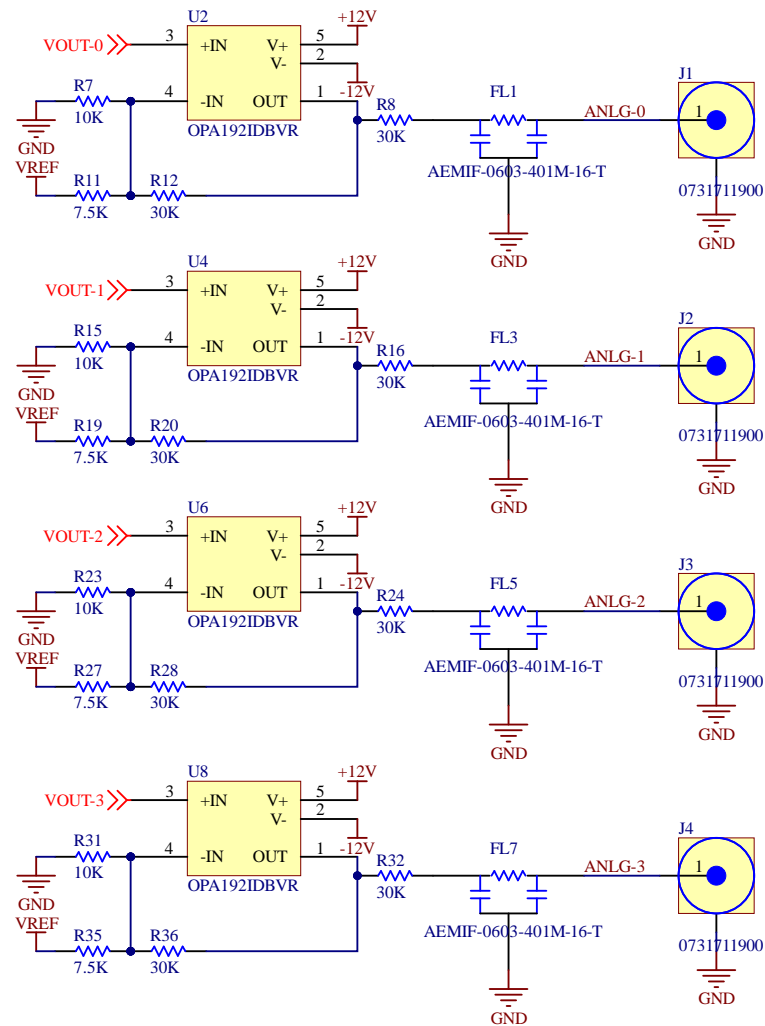
Sheet: 1 of 4

Date: 7/1/2020

Severson Group
WEMPEC
UW-Madison

Engineer: Brandt Buchda



Title: **Output Connectors**

File: OutputConnectors.SchDoc

Revision: B

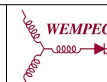
Time: 1:39:11 AM

Sheet: 3 of 4

Date: 7/1/2020

Severson Group
WEMPEC
UW-Madison

Engineer: Brandt Buchda



1

2

3

4

A

A

B

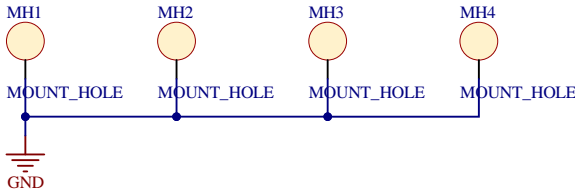
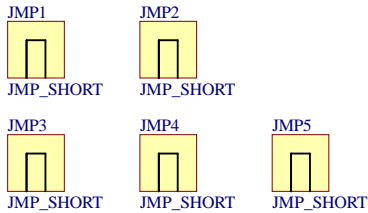
B

C

C

D

D



1

2

3

4