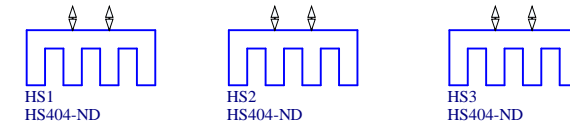
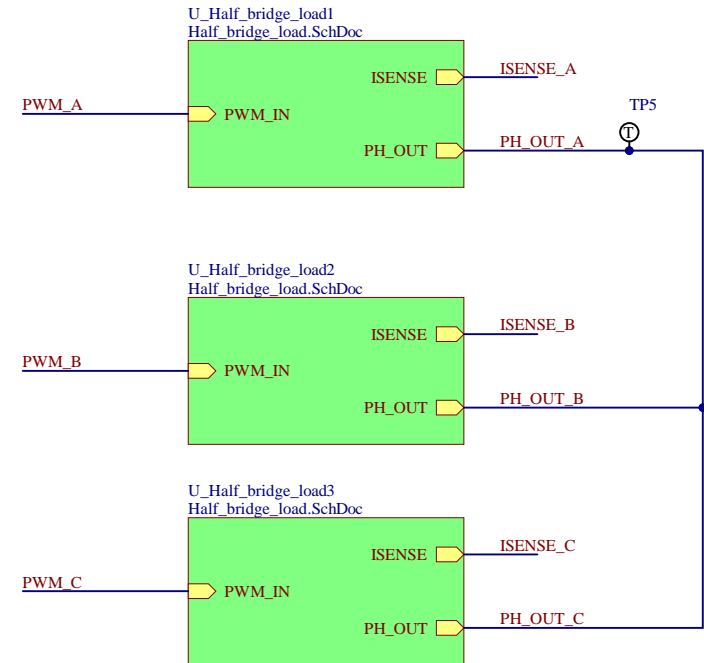
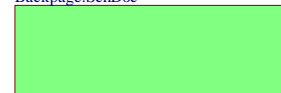



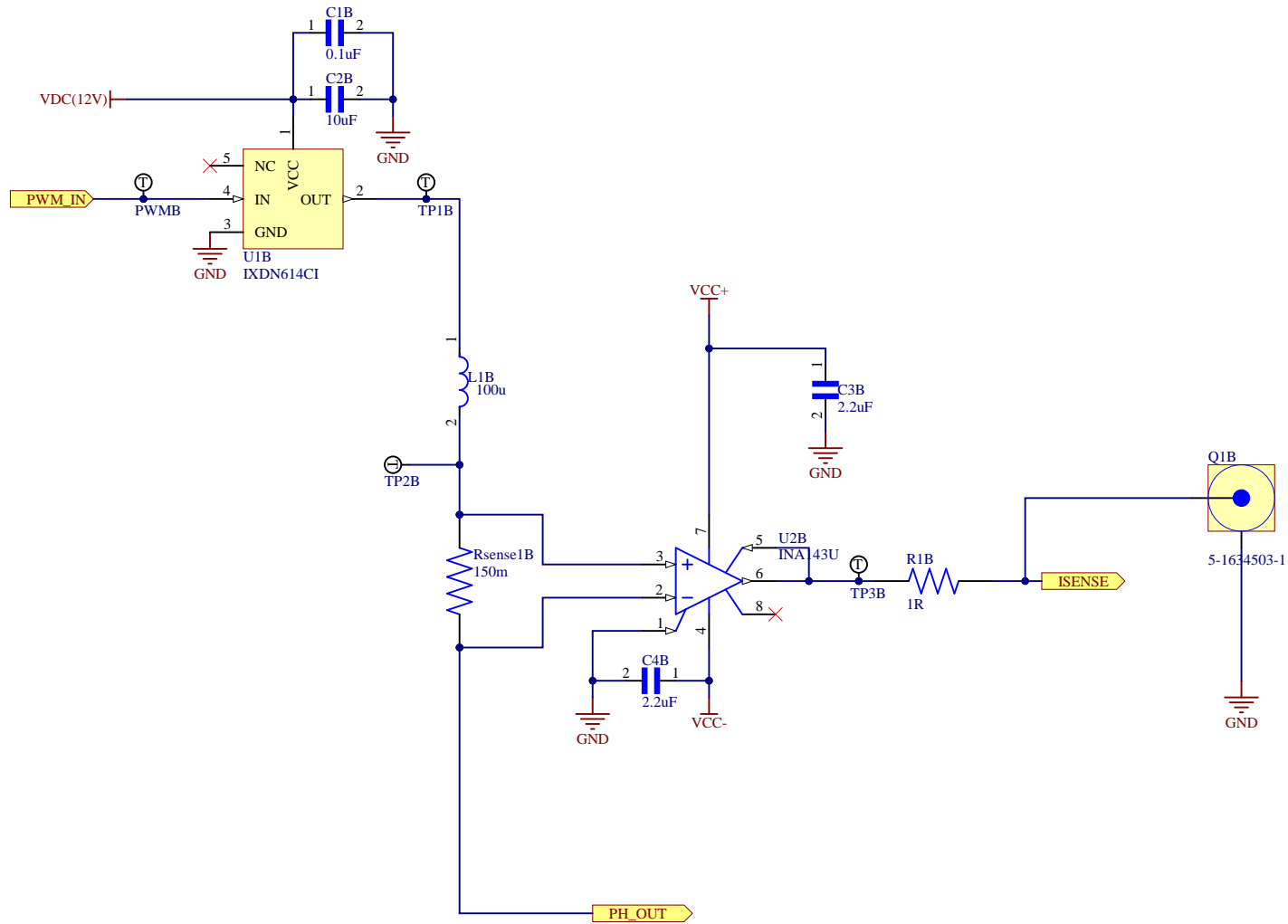
Three Phase Circuit



U_Backpage
Backpage.SchDoc



Title: <i>uInverter</i>			Severson Group WEMPEC UW-Madison	
File: uInverter.SchDoc		Sheet: 1 of 3		
Revision: B	Time: 19:29:19	Date: 21-09-2022	Engineer: Parth Purohit	



Title: **Half Bridge and Load**

File: Half_bridge_load.SchDoc

Revision: B

Time: 19:29:20

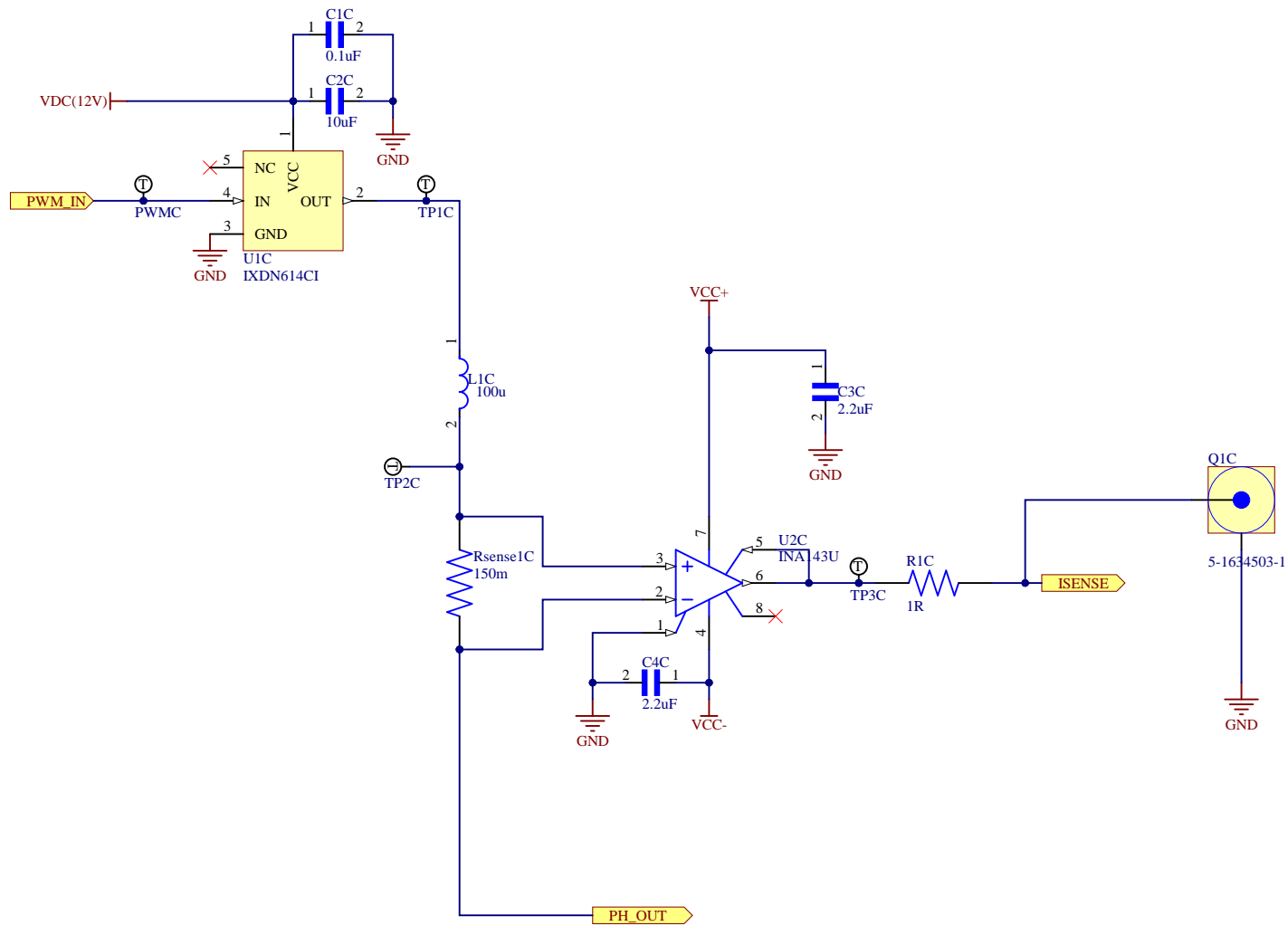
Sheet: 2 of 3

Date: 21-09-2022

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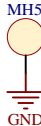
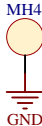
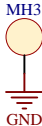
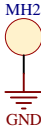
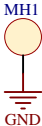
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Title: ***uInverter Backpage***

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