Title: **DAC**

File: DAC.SchDoc

Sheet: 1 of 4

Revision: B

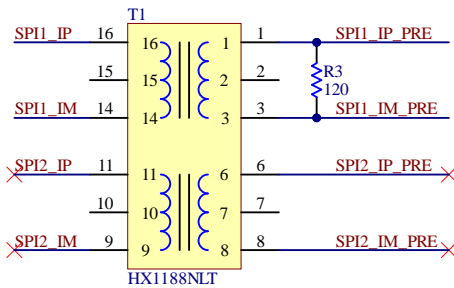
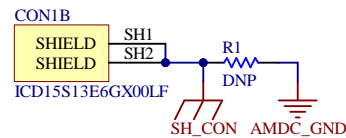
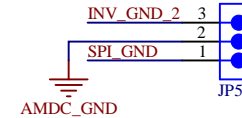
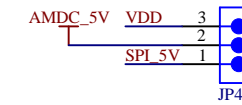
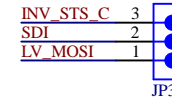
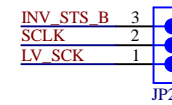
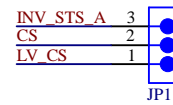
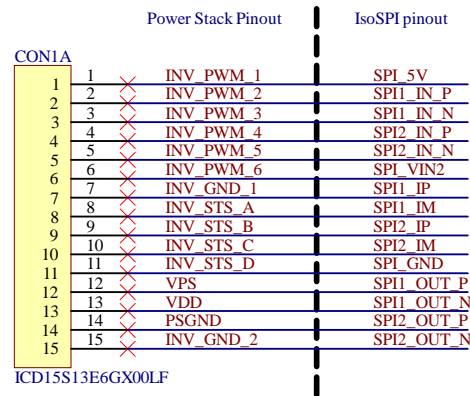
Time: 1:22:49 AM

Date: 7/1/2020

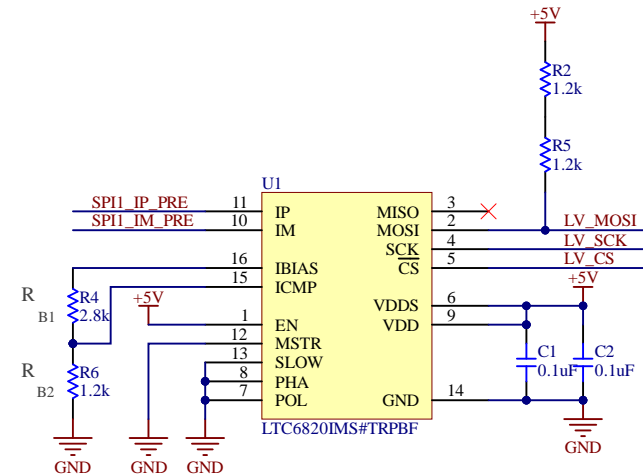
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△ CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.



△ RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.



Title: **Input Connector**

File: InputConnector.SchDoc

Revision: B

Time: 1:22:49 AM

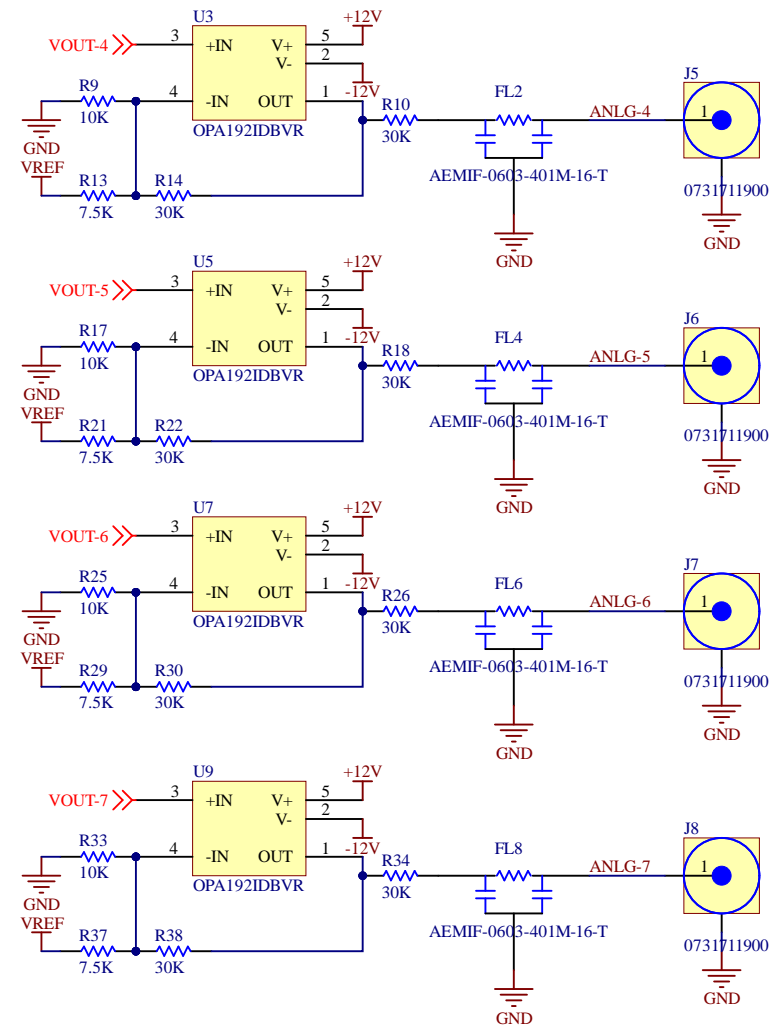
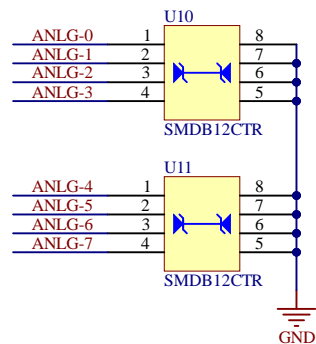
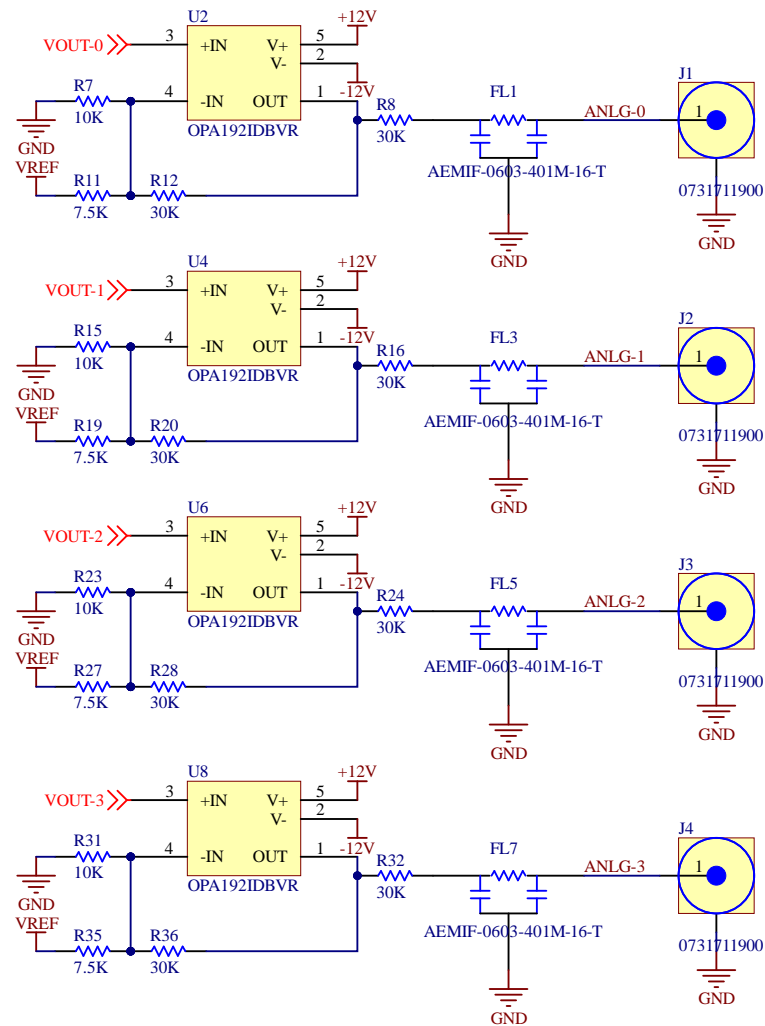
Sheet: 1 of 4

Date: 7/1/2020

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Title: **Output Connectors**

File: OutputConnectors.SchDoc

Revision: B

Time: 1:22:49 AM

Sheet: 3 of 4

Date: 7/1/2020

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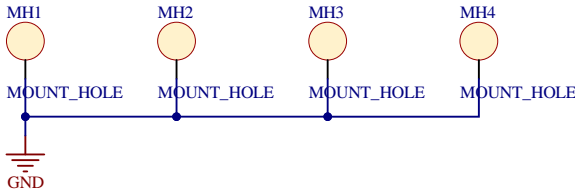
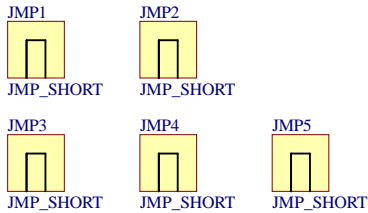
B

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C

D

D



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