

Title: **Input Connector**

File: InputConnector.SchDoc

Revision: B

Time: 8:38:07 PM

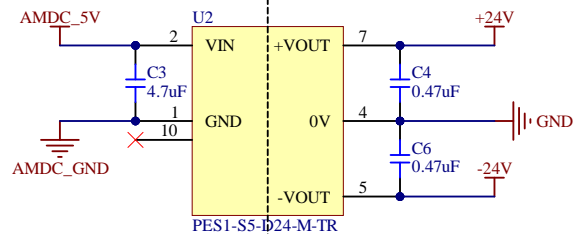
Sheet: 1 of 4

Date: 6/28/2020

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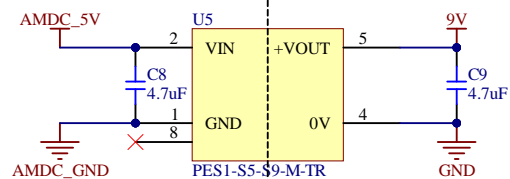
Engineer: Brandt Buchda



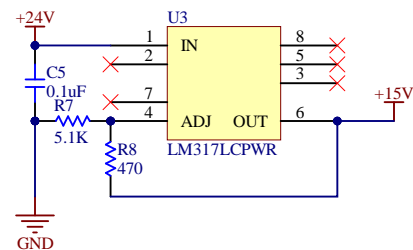


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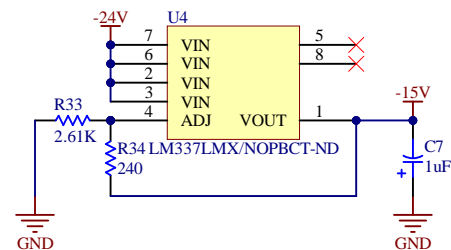
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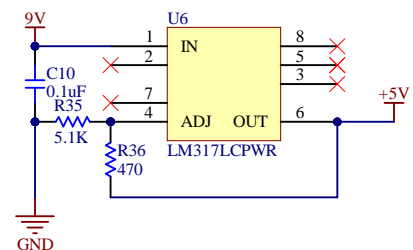
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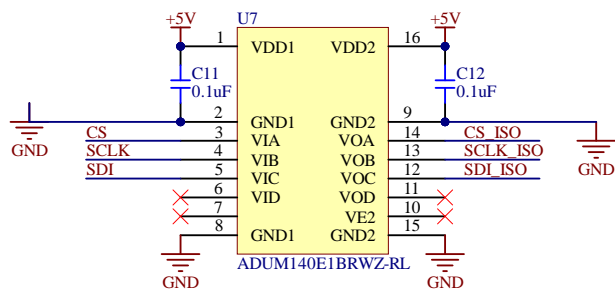
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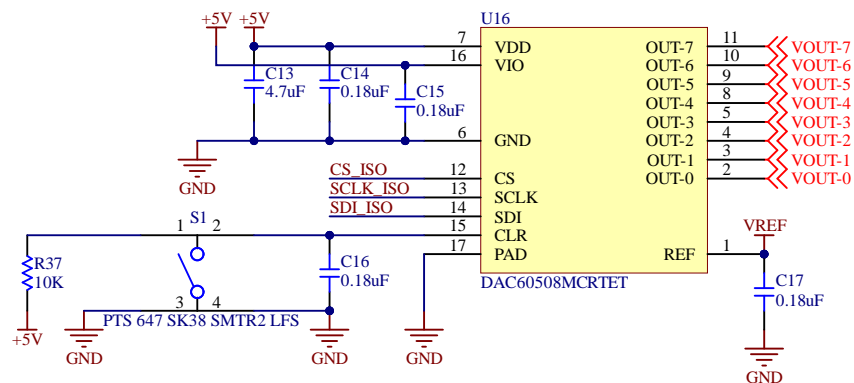
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LM317LCPWR



ADUM140E1BRWZ-RL



DAC60508MCR2TET

Title: **DAC**

File: DAC.SchDoc

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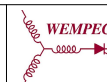
Revision: B

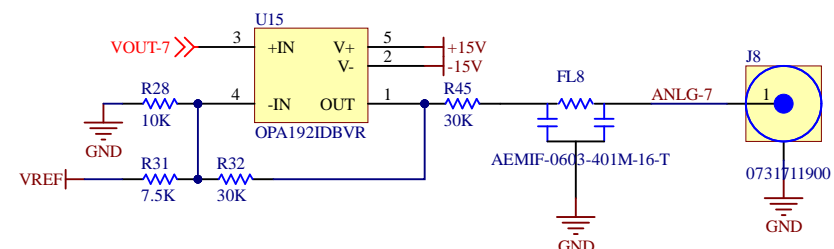
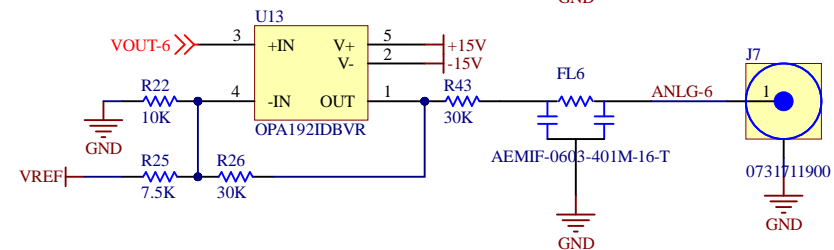
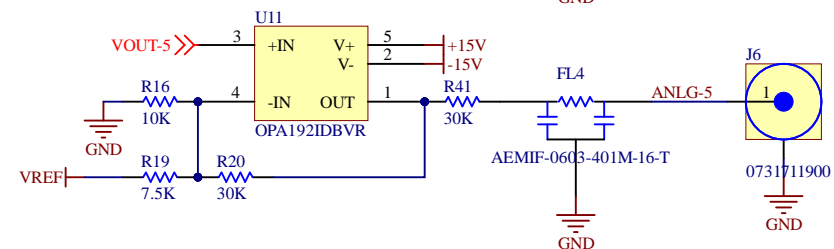
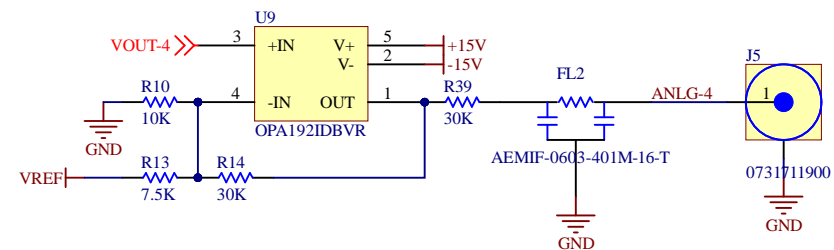
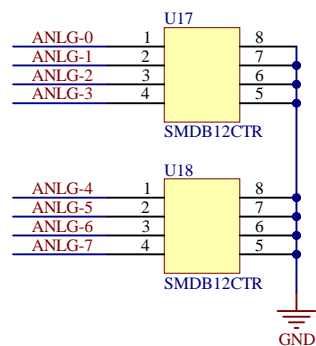
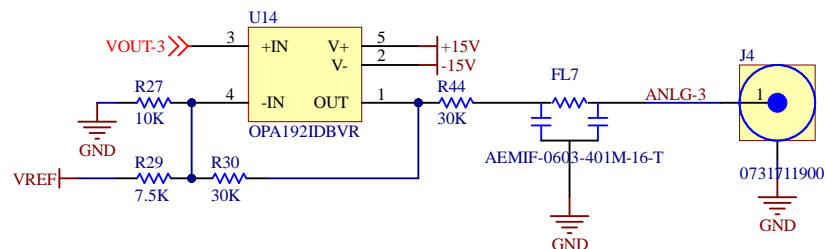
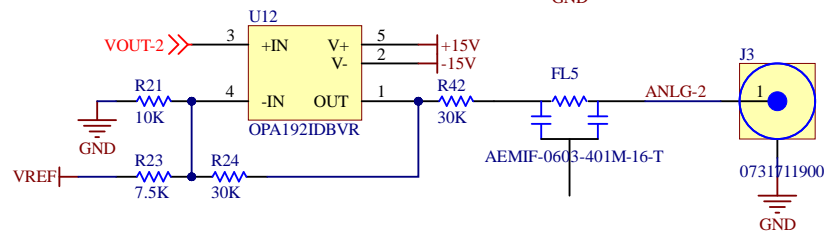
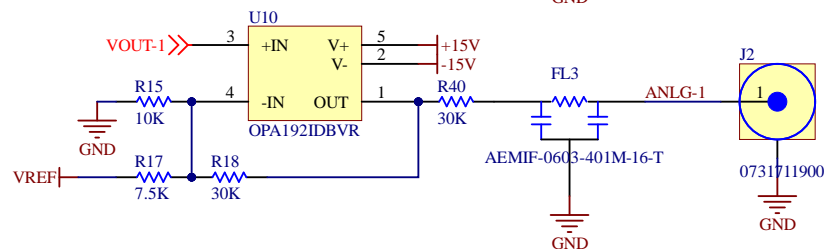
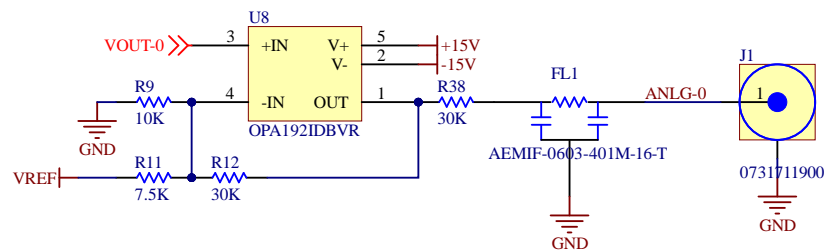
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Title: **Output Connectors**

File: OutputConnectors.SchDoc

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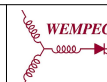
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Date: 6/28/2020

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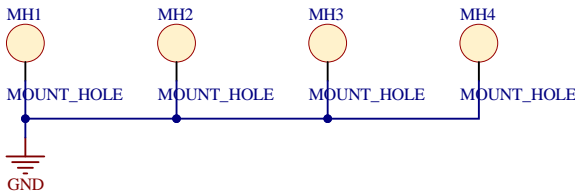
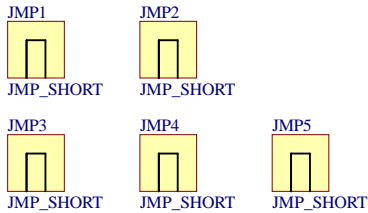
B

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C

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