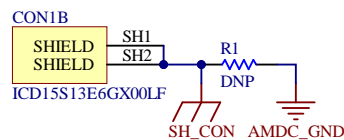


CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.

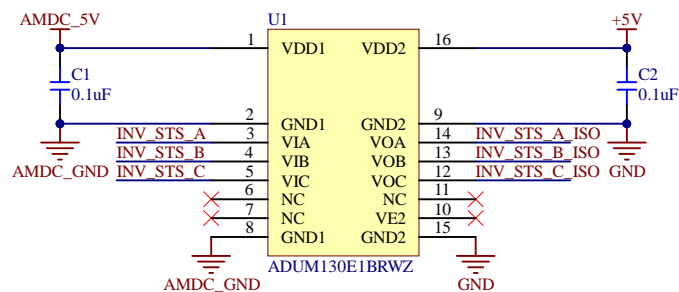
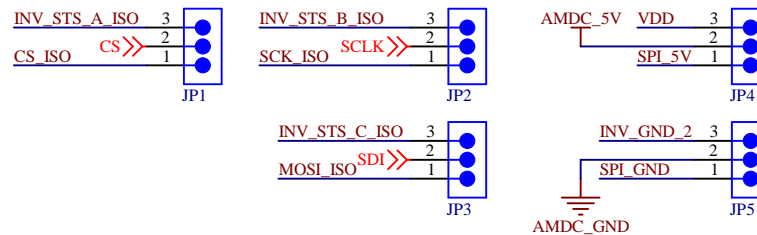
Legend:
 ✓ Used
 ✗ Unused

Power Stack Pinout		IsoSPI pinout	
CON1A			
1	1	INV_PWM_1	✗
2	2	INV_PWM_2	✗
3	3	INV_PWM_3	✗
4	4	INV_PWM_4	✗
5	5	INV_PWM_5	✗
6	6	INV_PWM_6	✗
7	7	INV_GND_1	✗
8	8	INV_STS_A	✓
9	9	INV_STS_B	✓
10	10	INV_STS_C	✓
11	11	INV_STS_D	✓
12	12	VPS	✗
13	13	VDD	✓
14	14	PSGND	✗
15	15	INV_GND_2	✓
		SPI_5V	✓
		SPI1_IN_P	✗
		SPI1_IN_N	✗
		SPI2_IN_P	✗
		SPI2_IN_N	✗
		SPI_VIN2	✗
		SPI1_IP	✓
		SPI1_IM	✓
		SPI2_IP	✗
		SPI2_IM	✗
		SPI_GND	✓
		SPI1_OUT_P	✗
		SPI1_OUT_N	✗
		SPI2_OUT_P	✗
		SPI2_OUT_N	✗

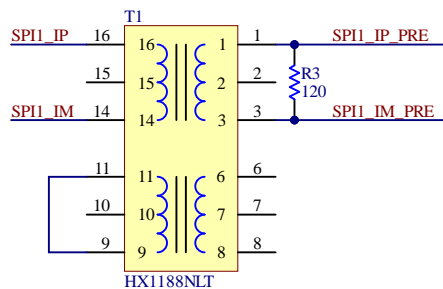
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ICD15S13E6GX00LF

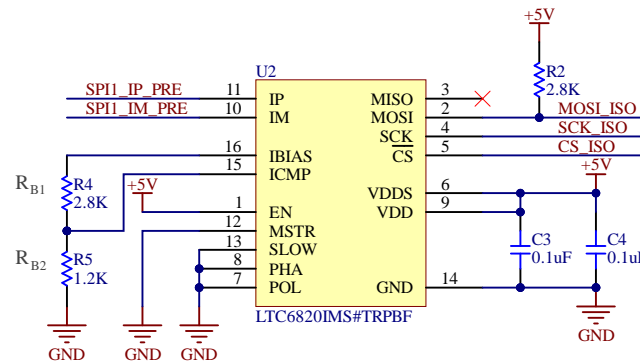


ADUM130E1BRWZ



HX1188NLT

RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.



LTC6820IMS#TRPBF

Title: **Input Connector**

File: InputConnector.SchDoc

Revision: B

Time: 12:35:00 PM

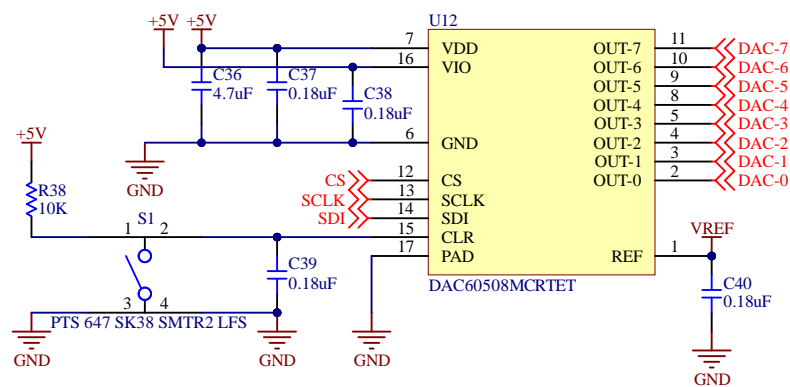
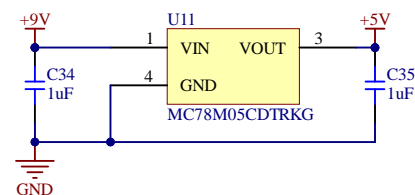
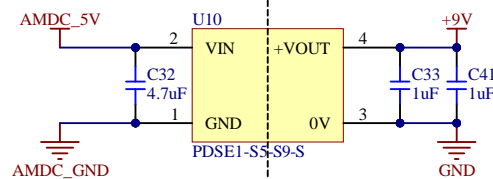
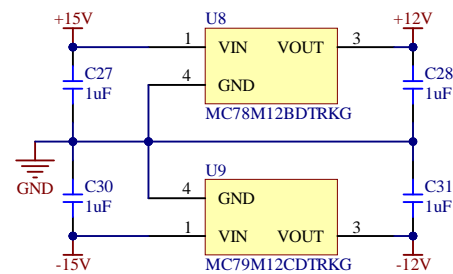
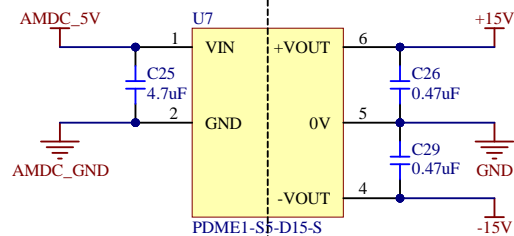
Sheet: 1 of 4

Date: 7/8/2020

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Engineer: Brandt Buchda



Title: **DAC**

File: DAC.SchDoc

Revision: B

Time: 12:35:01 PM

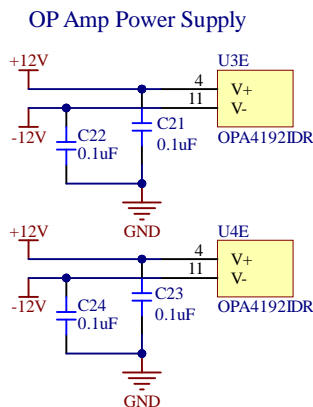
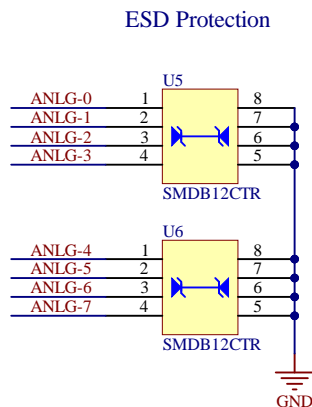
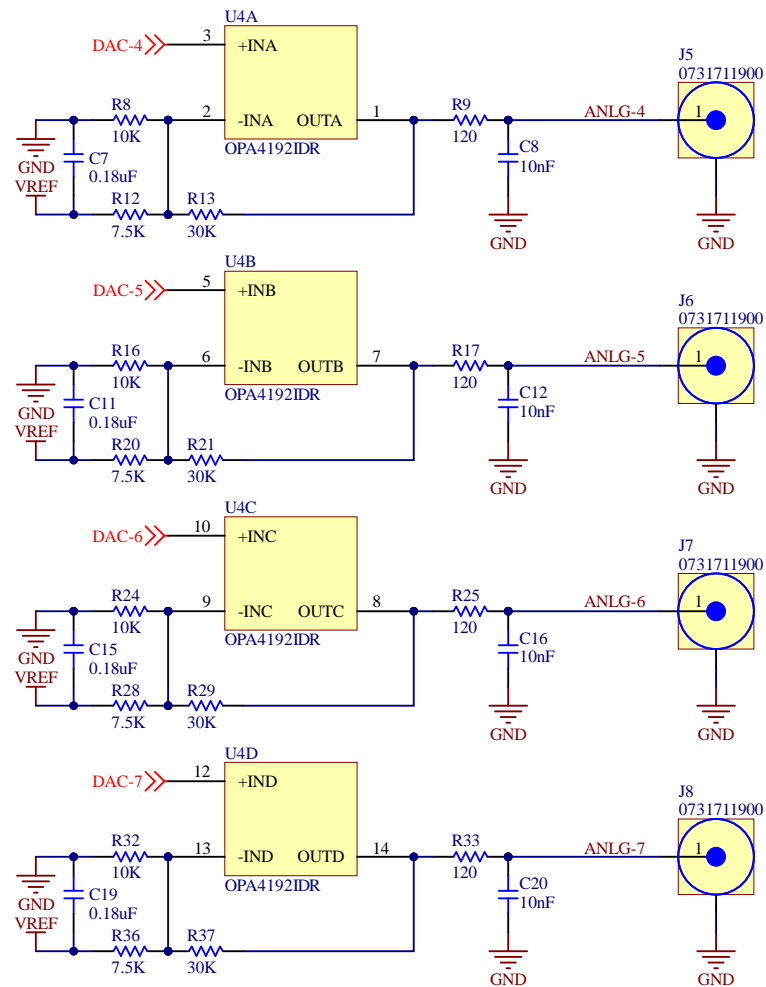
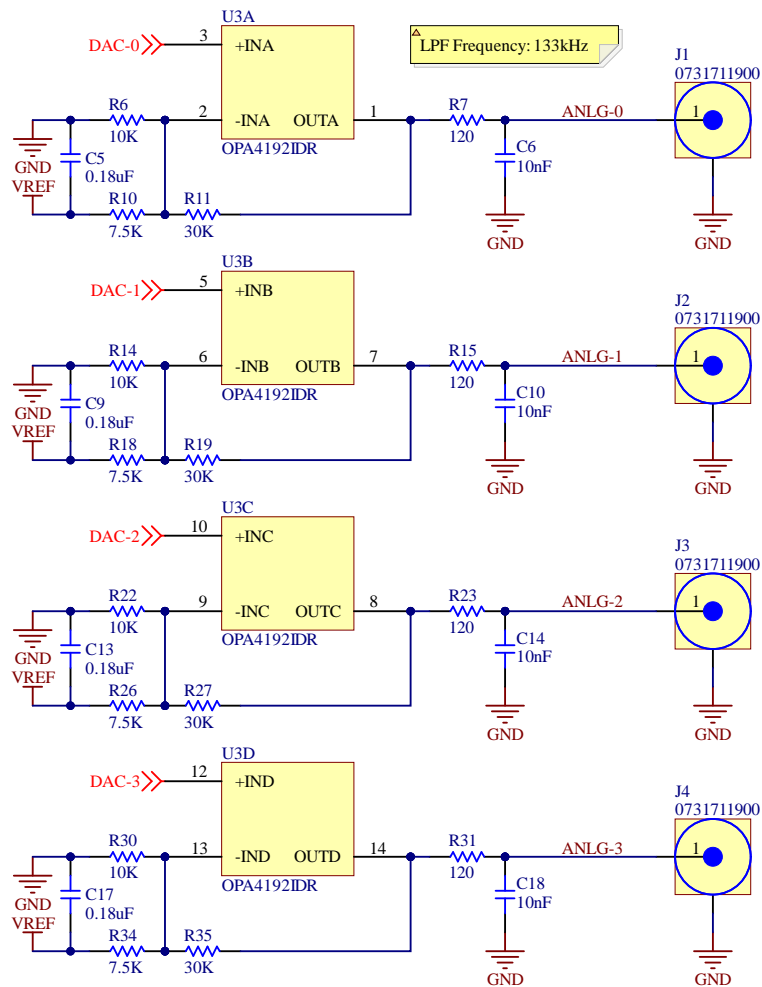
Sheet: 2 of 4

Date: 7/8/2020

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WEMPEC
UW-Madison

Engineer: Brandt Buchda



Title: **Output Connectors**

File: OutputConnectors.SchDoc

Sheet: 3 of 4

Revision: B

Time: 12:35:01 PM

Date: 7/8/2020

Engineer: Brandt Buchda

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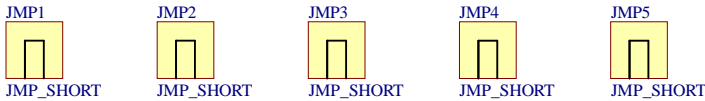
4-40 Screws:



4-40 Standoffs:



Pinout Jumpers:
Silkscreen marked with default locations



Mounting Holes:

