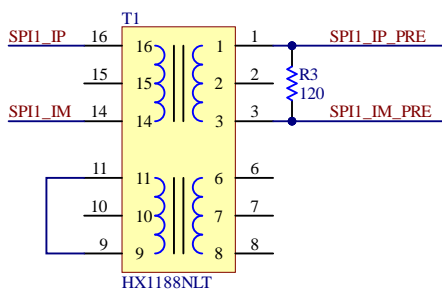
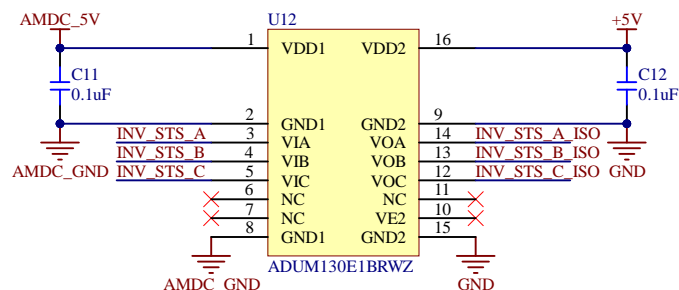
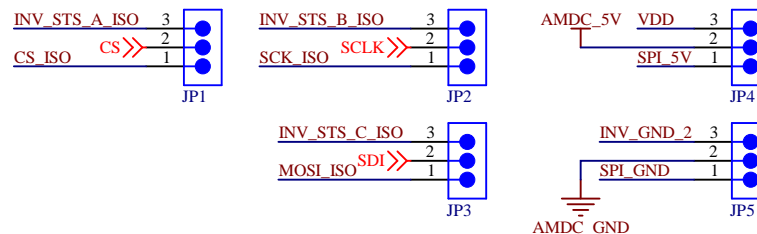
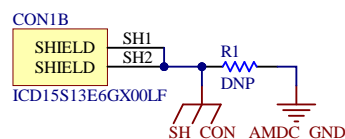
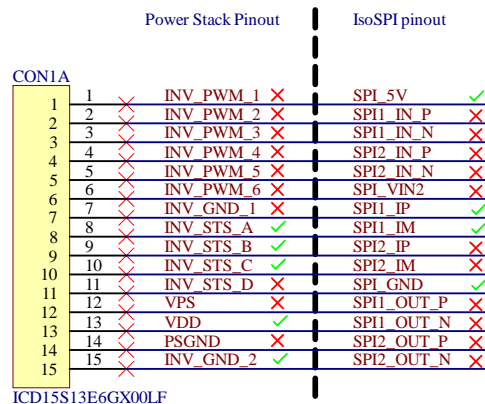
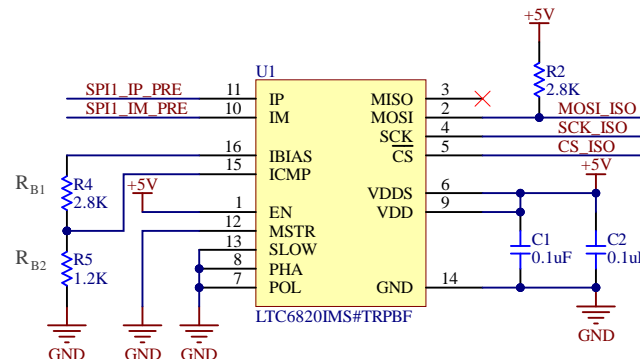


CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.

Legend:
 ✓ Used
 ✗ Unused



RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.



Title: **Input Connector**

File: InputConnector.SchDoc

Revision: B

Time: 3:32:57 PM

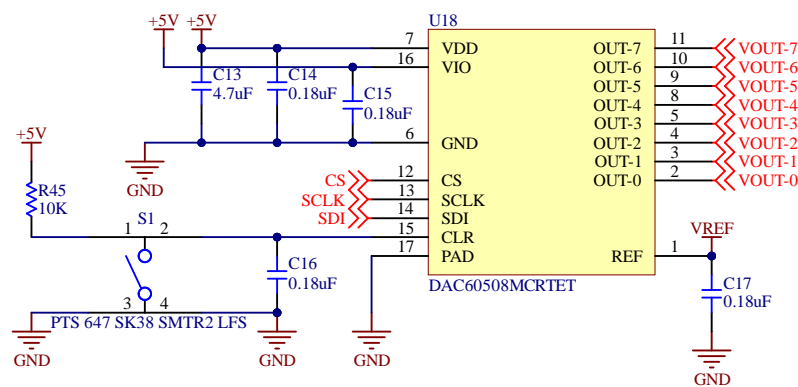
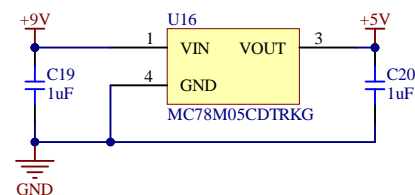
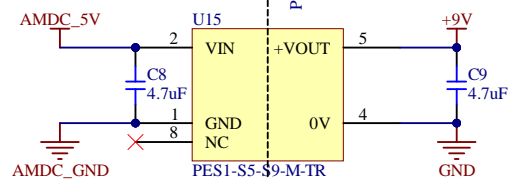
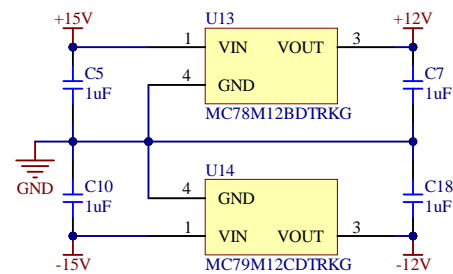
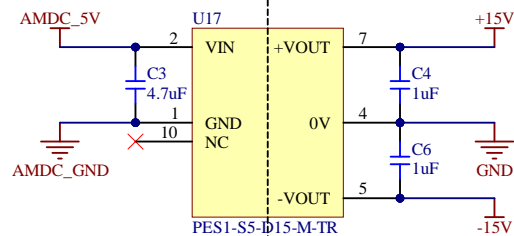
Sheet: 1 of 4

Date: 7/6/2020

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Title: **DAC**

File: DAC.SchDoc

Sheet: 2 of 4

Revision: B

Time: 3:32:57 PM

Date: 7/6/2020

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WEMPEC
UW-Madison

Engineer: Brandt Buchda



A

B

C

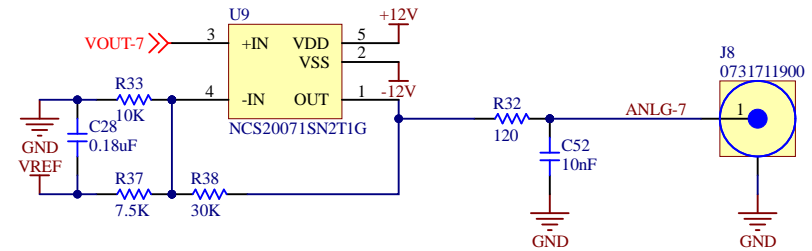
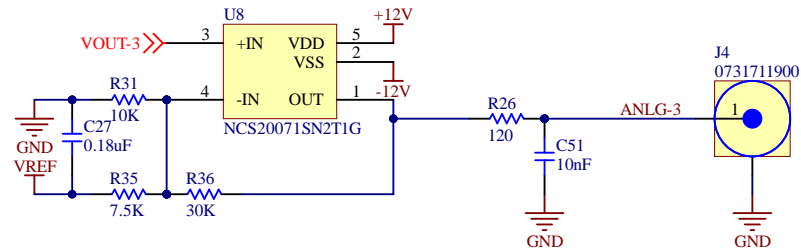
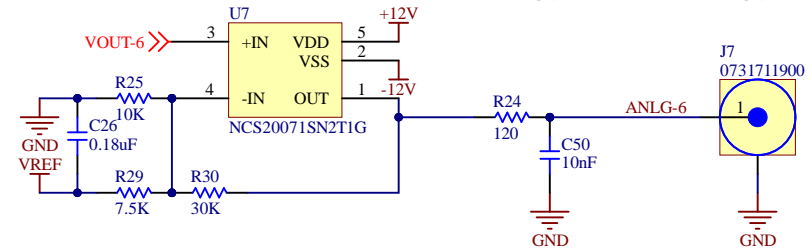
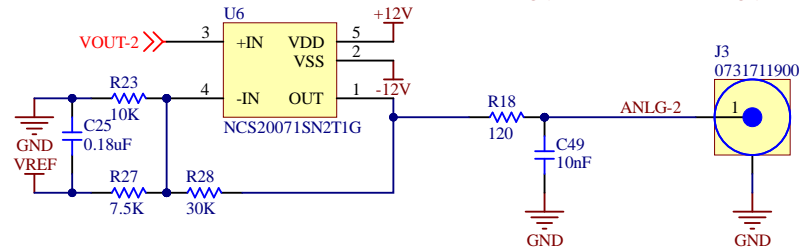
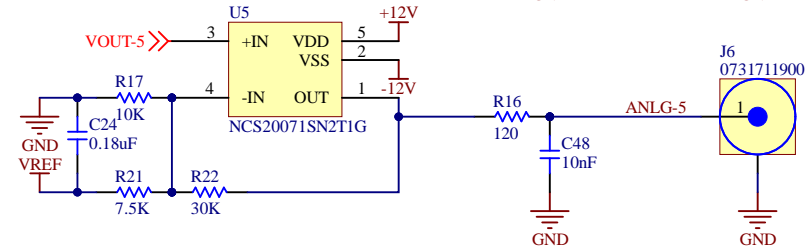
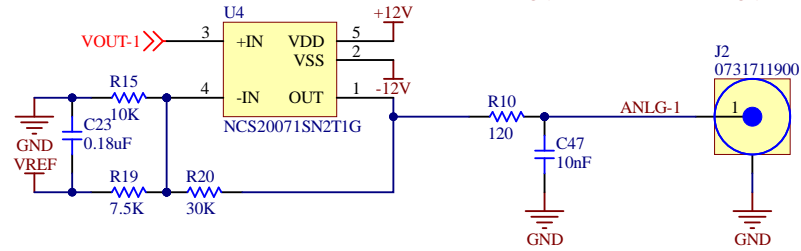
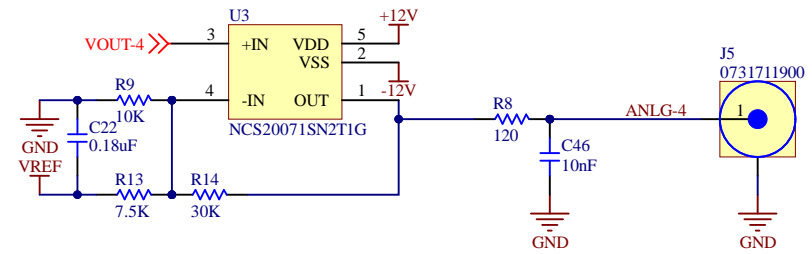
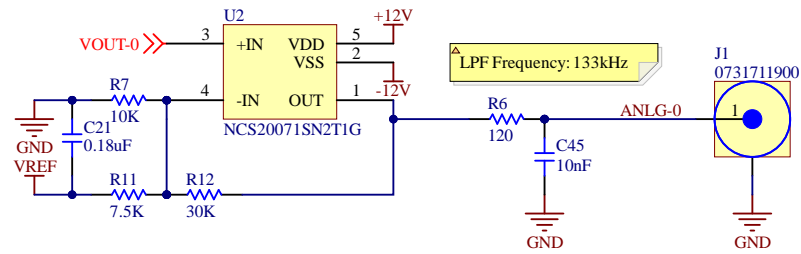
D

A

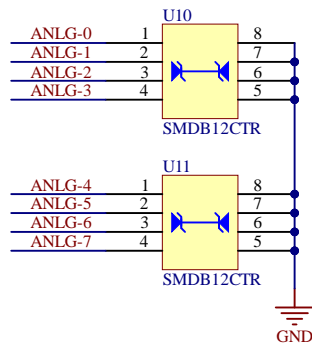
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C

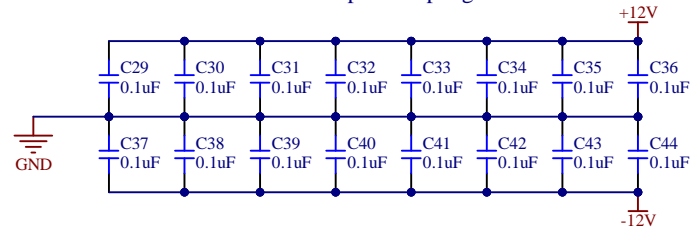
D



ESD Protection



OP Amp Decoupling



Title: **Output Connectors**

File: OutputConnectors.SchDoc

Revision: B

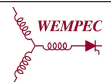
Time: 3:32:57 PM

Sheet: 3 of 4

Date: 7/6/2020

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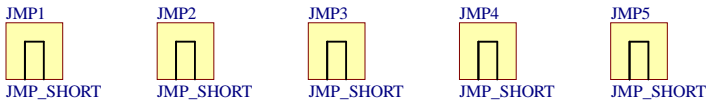
4-40 Screws:



4-40 Standoffs:



Pinout Jumpers:
Silkscreen marked with default locations



Mounting Holes:

