

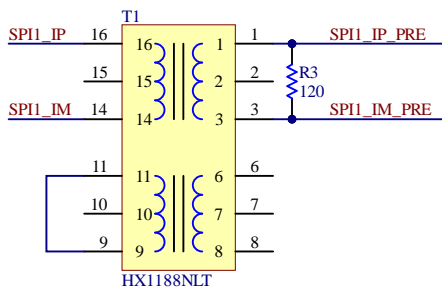
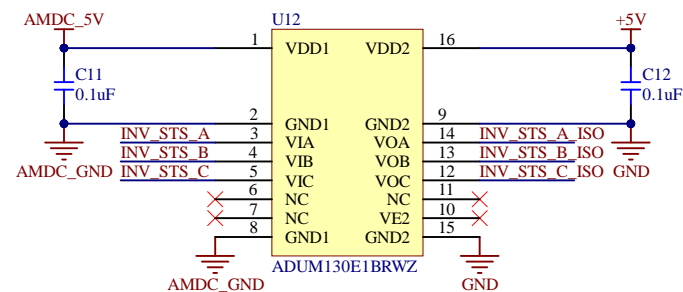
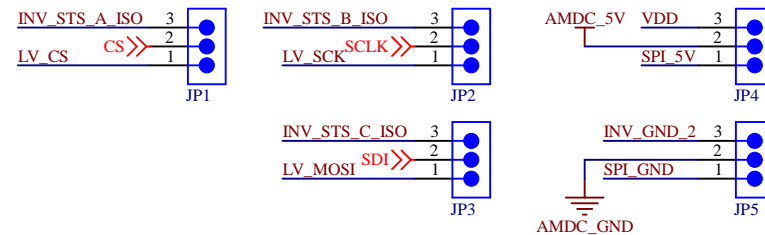
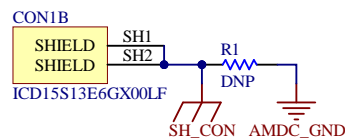
CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.

Legend:
 ✓ Used
 ✗ Unused

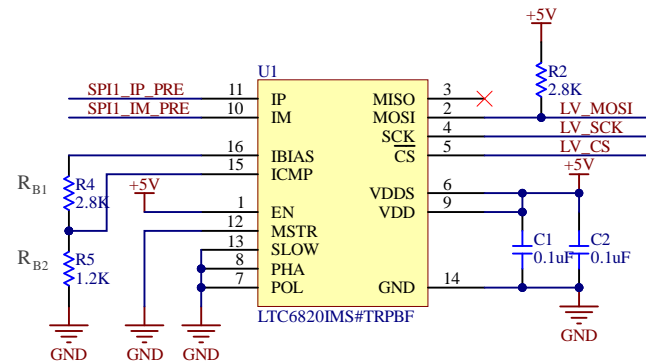
Power Stack Pinout			IsoSPI pinout		
CON1A					
1	✗	INV_PWM 1	✗	SPI_5V	✓
2	✗	INV_PWM 2	✗	SPI1_IN_P	✗
3	✗	INV_PWM 3	✗	SPI1_IN_N	✗
4	✗	INV_PWM 4	✗	SPI2_IN_P	✗
5	✗	INV_PWM 5	✗	SPI2_IN_N	✗
6	✗	INV_PWM 6	✗	SPI_VIN2	✗
7	✗	INV_GND 1	✗	SPI1_IP	✓
8	✓	INV_STS_A	✓	SPI1_IM	✓
9	✓	INV_STS_B	✓	SPI2_IP	✗
10	✓	INV_STS_C	✓	SPI2_IM	✗
11	✗	INV_STS_D	✗	SPI_GND	✓
12	✗	VPS	✗	SPI1_OUT_P	✗
13	✓	VDD	✓	SPI1_OUT_N	✗
14	✗	PSGND	✗	SPI2_OUT_P	✗
15	✗	INV_GND 2	✗	SPI2_OUT_N	✗

ICD15S13E6GX00LF

ICD15S13E6GX00LF



RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.



Title: **Input Connector**

File: InputConnector.SchDoc

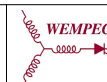
Revision: B

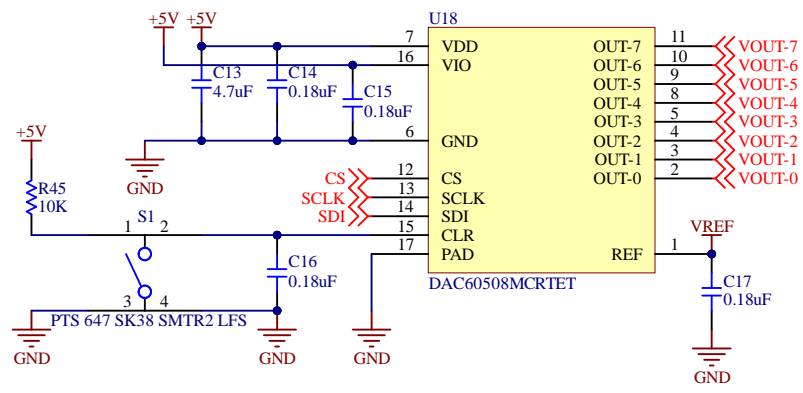
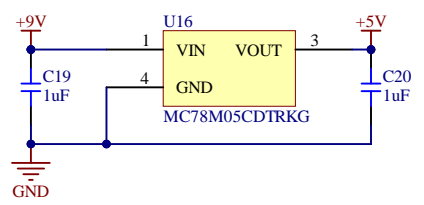
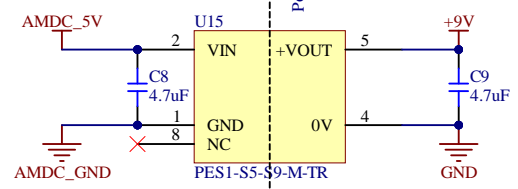
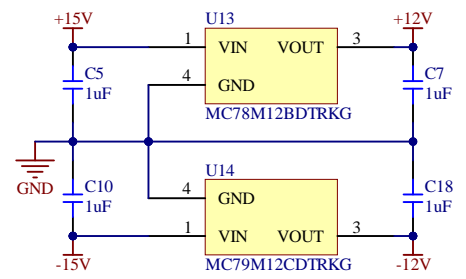
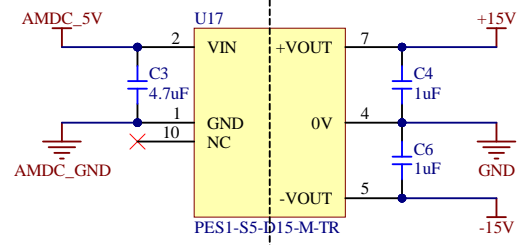
Date: 7/3/2020

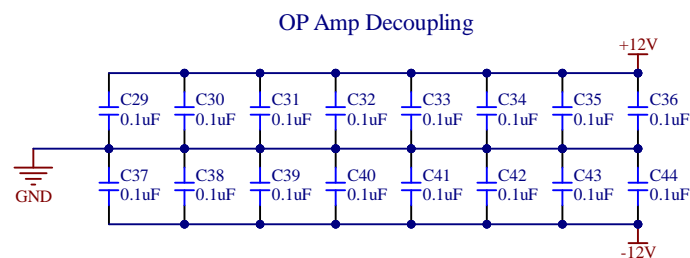
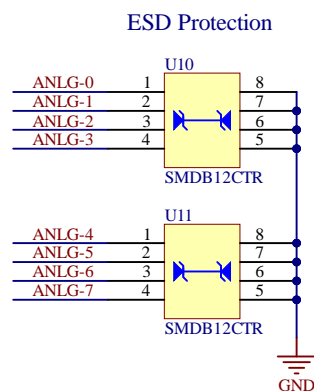
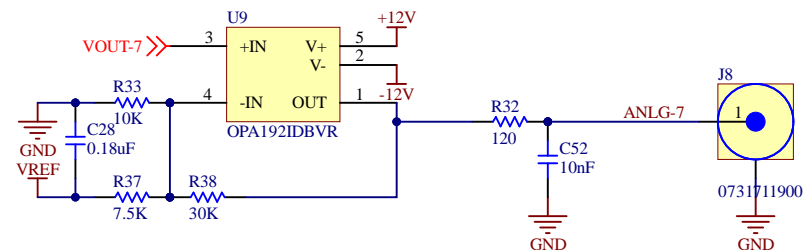
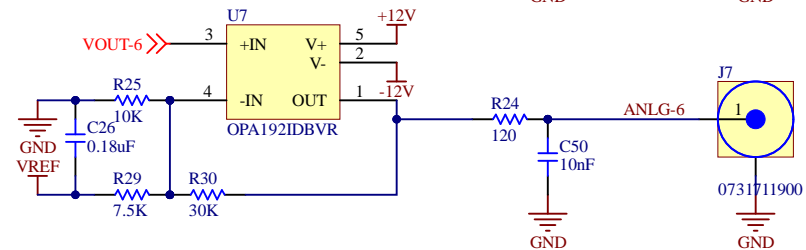
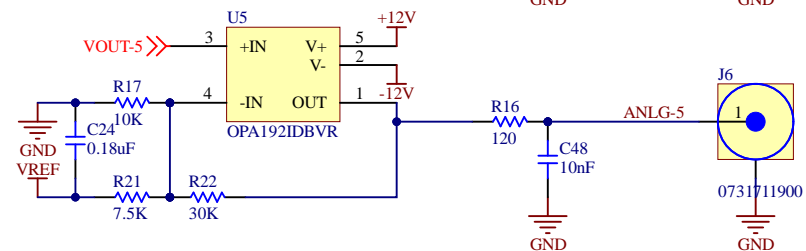
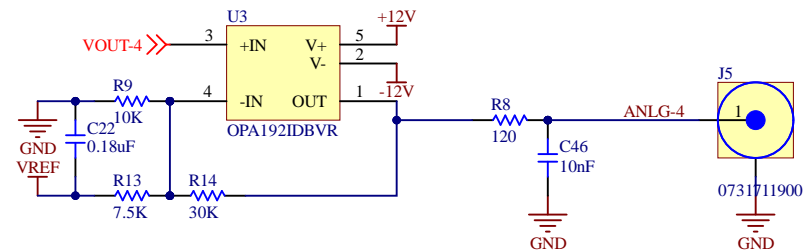
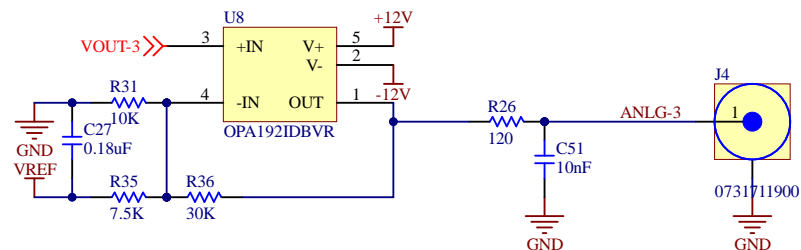
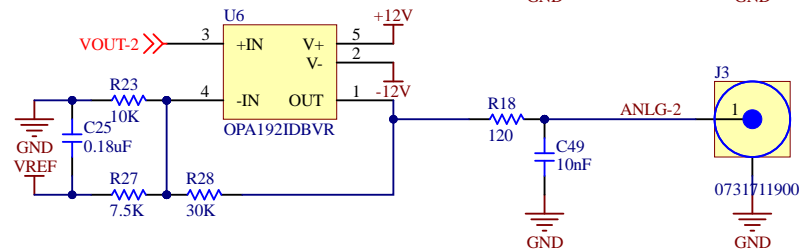
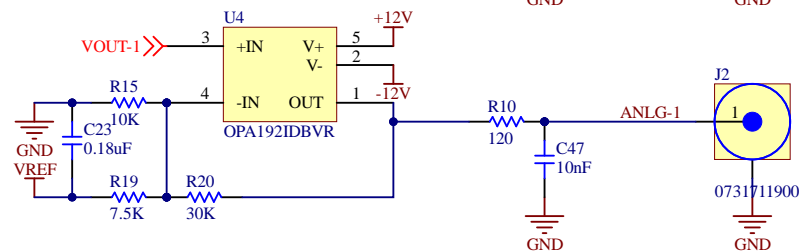
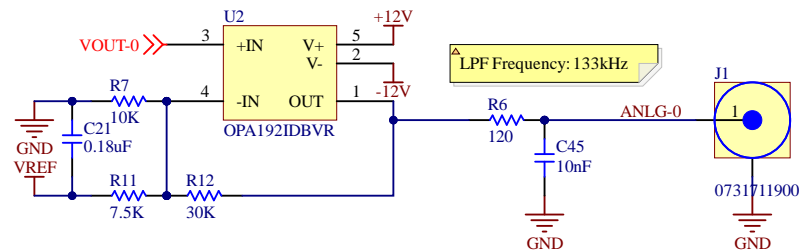
Sheet: 1 of 4


Severson Group
WEMPEC
UW-Madison

Engineer: Brandt Buchda







Title: <i>Output Connectors</i>			Severson Group WEMPEC UW-Madison	
File: OutputConnectors.SchDoc		Sheet: 3 of 4		
Revision: B	Time: 12:56:52 PM	Date: 7/3/2020		

Pinout Jumpers

Silkscreen marked with default locations

JMP1



JMP_SHORT

JMP2



JMP_SHORT

JMP3



JMP_SHORT

JMP4



JMP_SHORT

JMP5



JMP_SHORT

Board Mounting Holes

MH1



MH2



MH3



MH4

