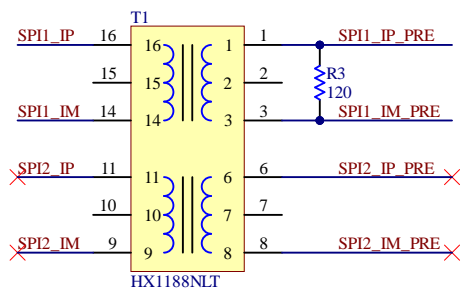
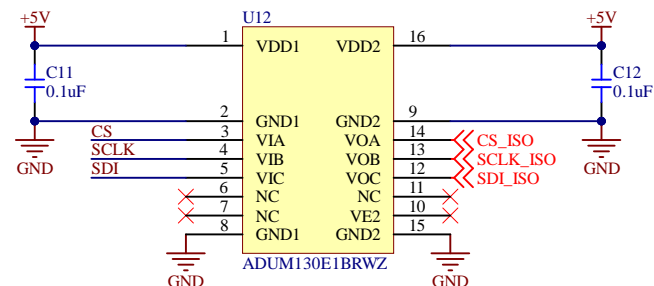
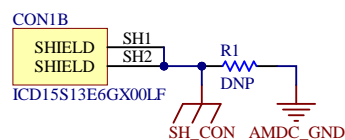
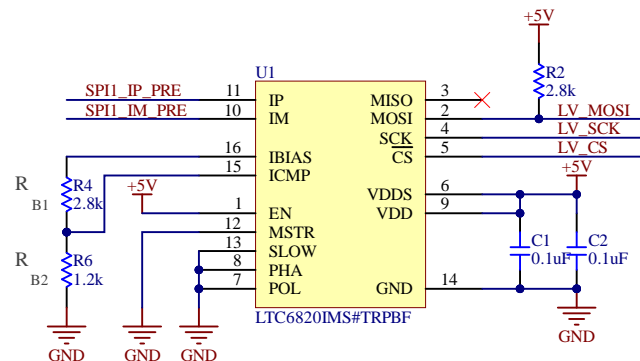


CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.



RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.



Title: **Input Connector**

File: InputConnector.SchDoc

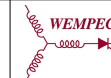
Revision: B

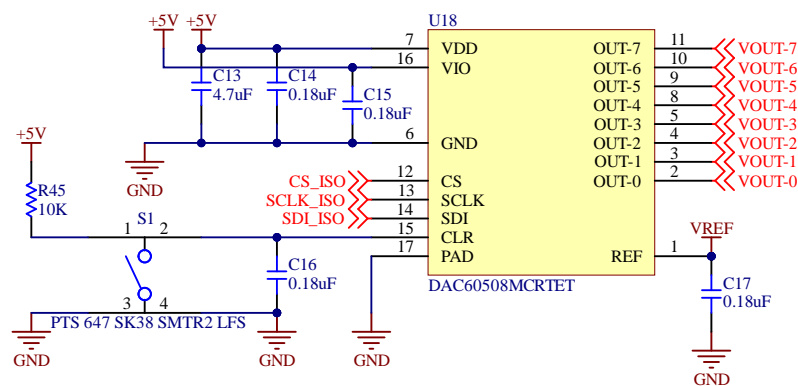
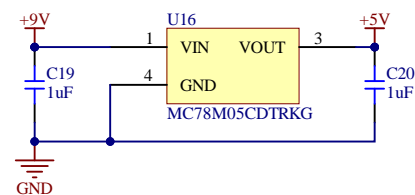
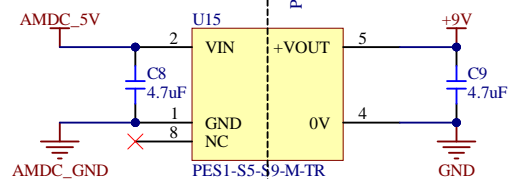
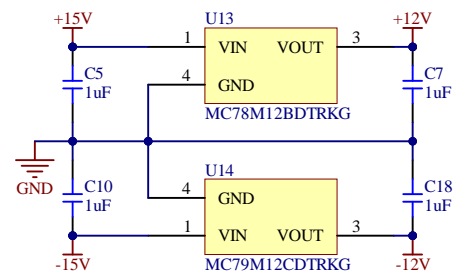
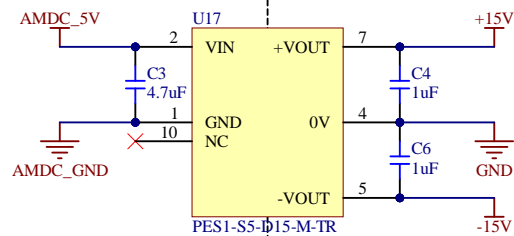
Date: 7/1/2020

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Title: **DAC**

File: DAC.SchDoc

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Date: 7/1/2020

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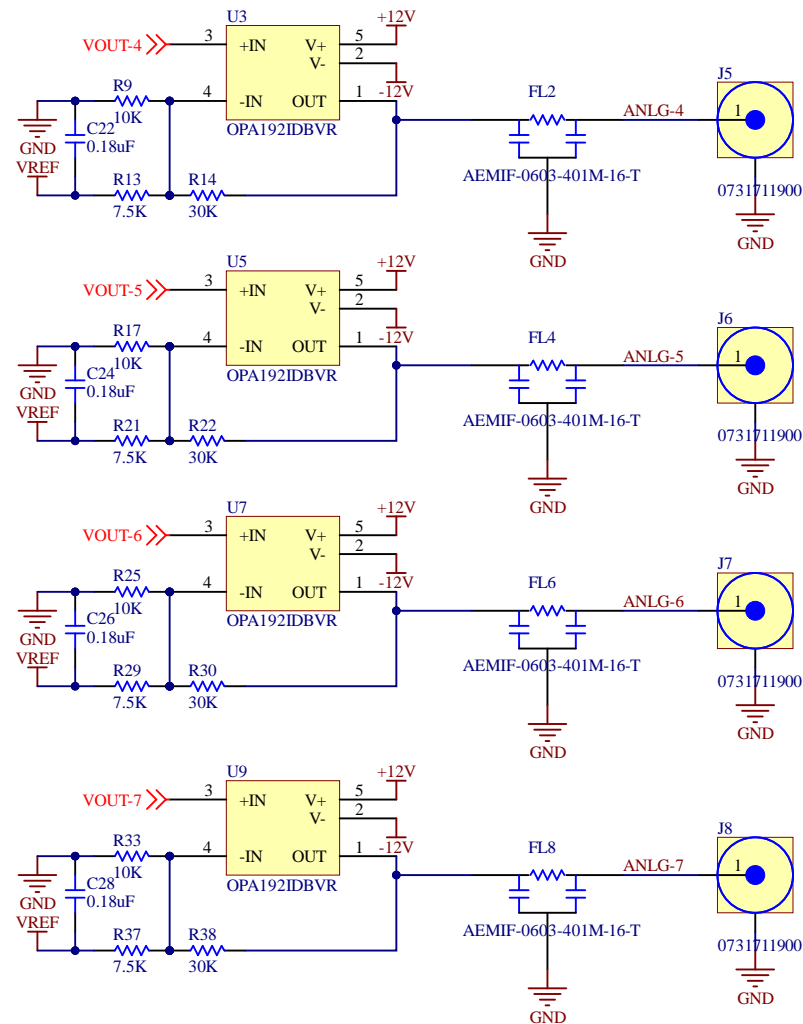
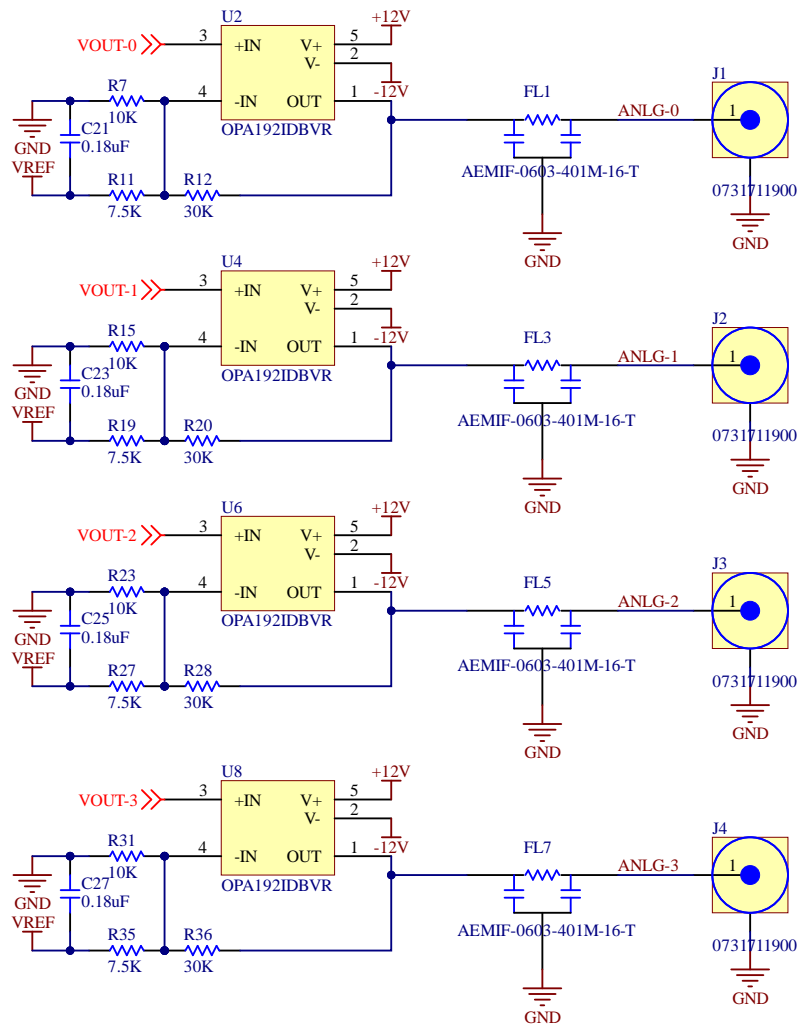


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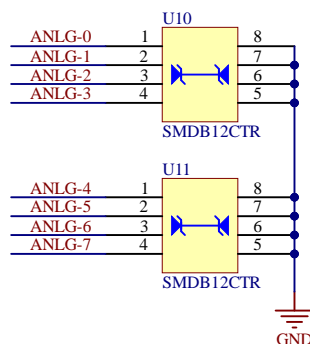
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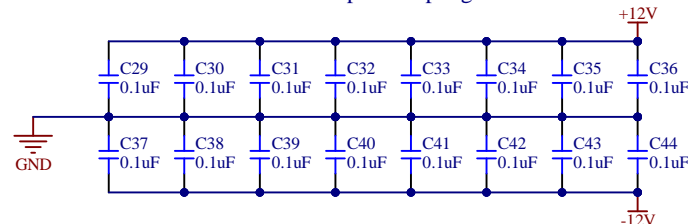
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### ESD Protection



### OP Amp Decoupling



Title: **Output Connectors**

File: OutputConnectors.SchDoc

Revision: B

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Date: 7/1/2020

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A

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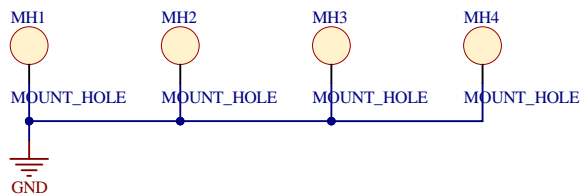
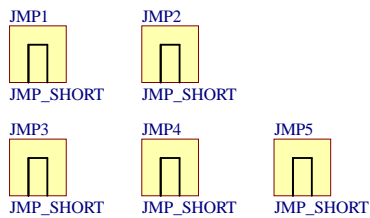
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D

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Title: <i>Back Page</i>			Severson Group WEMPEC UW-Madison		
File: BackPage.SchDoc		Sheet: 4 of 4			
Revision: B	Time: 12:20:53 PM	Date: 7/1/2020	Engineer: Brandt Buchda		

1

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3

4