

Power Stack Pinout IsoSPI pinout

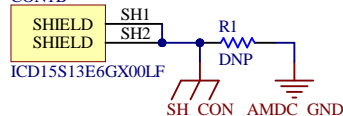
CON1 can be connected to both the power stack and isoSPI connectors. Jumpers J1-J5 are used to configure the DAC between the powerstack and isoSPI pinouts.

Legend:
 ✓ Used
 ✗ Unused

CON1A					
1	✗	INV_PWM_1	✗	SPI_5V	✓
2	✗	INV_PWM_2	✗	SPI1_IN_P	✗
3	✗	INV_PWM_3	✗	SPI1_IN_N	✗
4	✗	INV_PWM_4	✗	SPI2_IN_P	✗
5	✗	INV_PWM_5	✗	SPI2_IN_N	✗
6	✗	INV_PWM_6	✗	SPI_VIN2	✗
7	✗	INV_GND_1	✗	SPI1_IP	✓
8	✗	INV_STS_A	✓	SPI1_IM	✓
9	✗	INV_STS_B	✓	SPI2_IP	✗
10	✗	INV_STS_C	✓	SPI2_IM	✗
11	✗	INV_STS_D	✗	SPI_GND	✓
12	✗	VPS	✗	SPI1_OUT_P	✗
13	✗	VDD	✓	SPI1_OUT_N	✗
14	✗	PSGND	✓	SPI2_OUT_P	✗
15	✗	INV_GND_2	✓	SPI2_OUT_N	✗

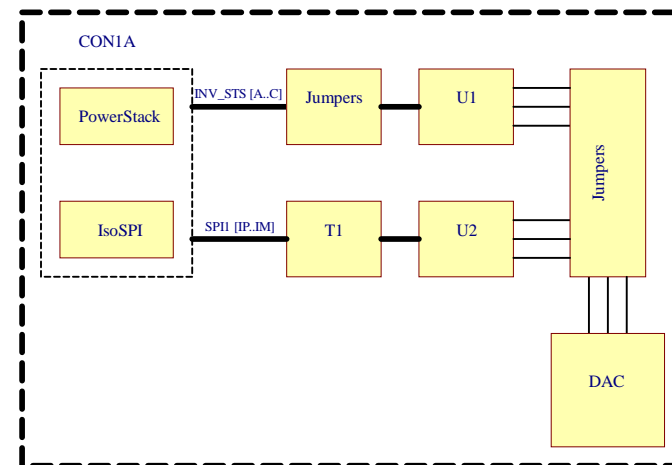
ICD15S13E6GX00LF

CON1B

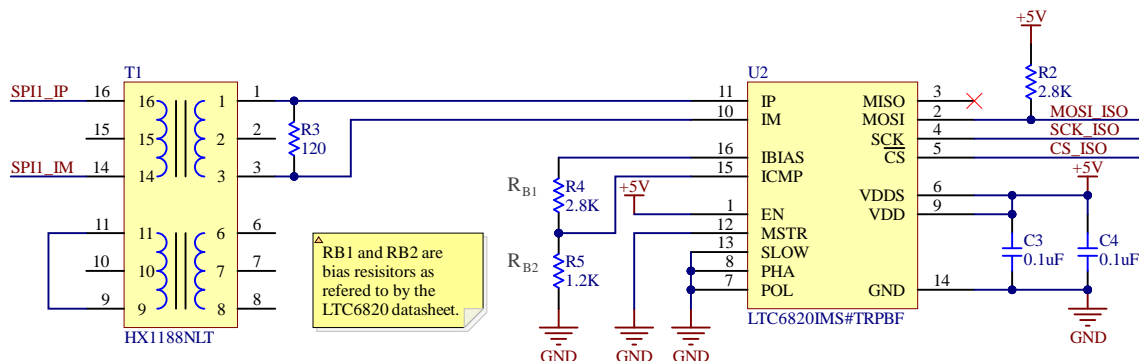
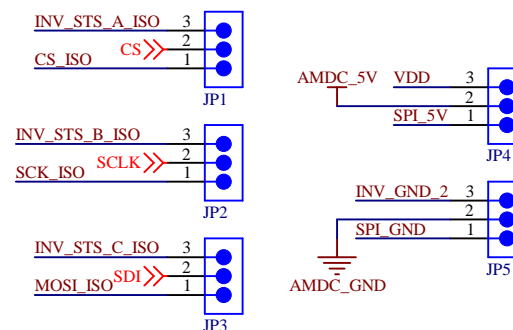
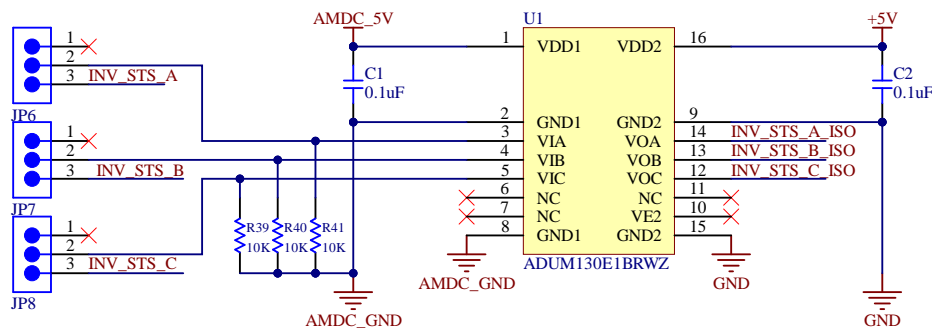


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Connector Signal Diagram

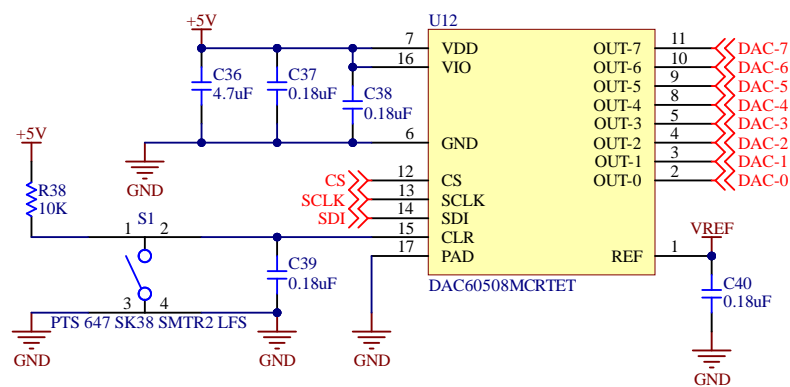
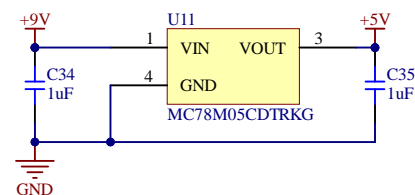
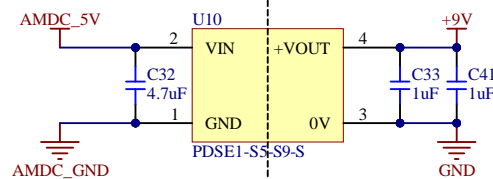
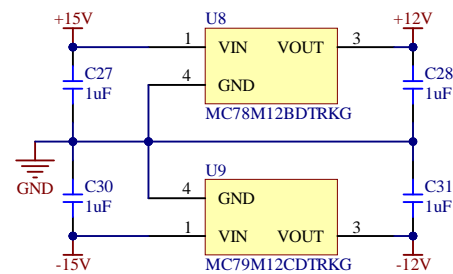
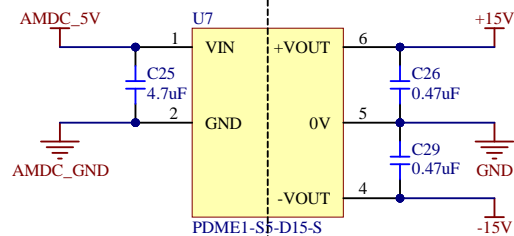


Jumpers J6-J8 are used to disable U1 when the power stack is not in use



RB1 and RB2 are bias resistors as referred to by the LTC6820 datasheet.

Title: Input Connector		Severson Group WEMPEC UW-Madison	
File: InputConnector.SchDoc	Sheet: 1 of 4		
Revision: B	Time: 6:17:03 PM		
Date: 7/8/2020	Engineer: Brandt Buchda		

Title: **DAC**

File: DAC.SchDoc

Revision: B

Time: 6:17:03 PM

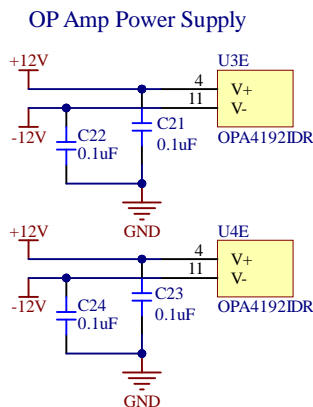
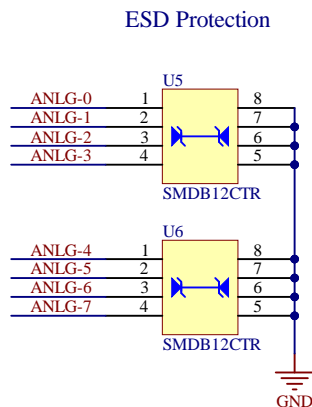
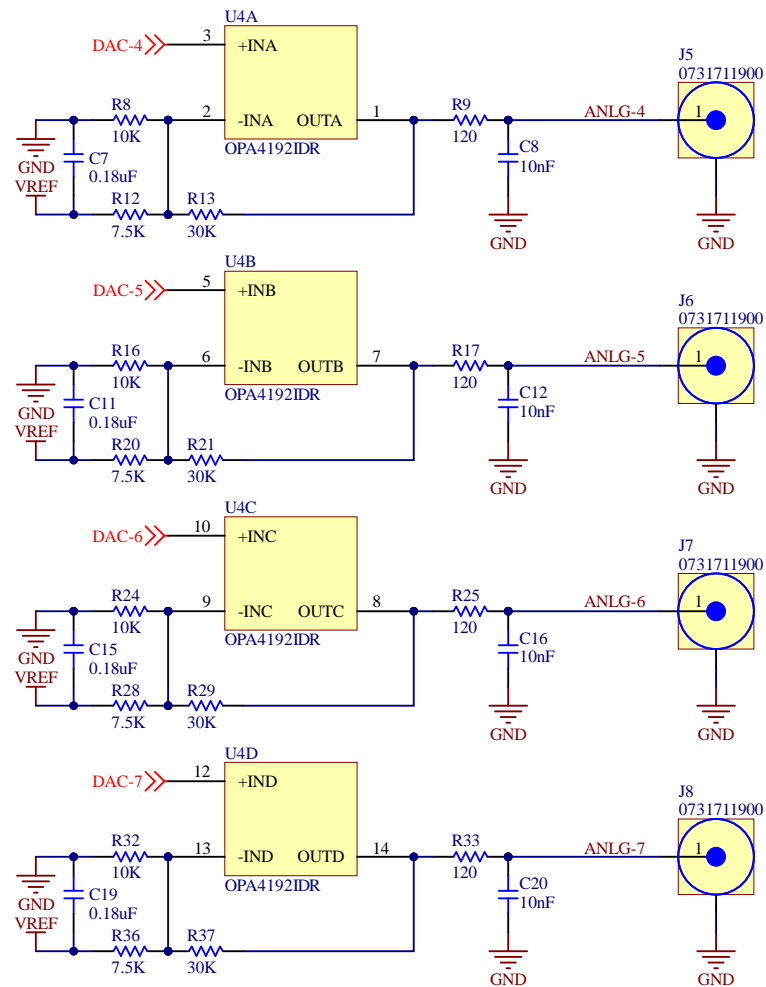
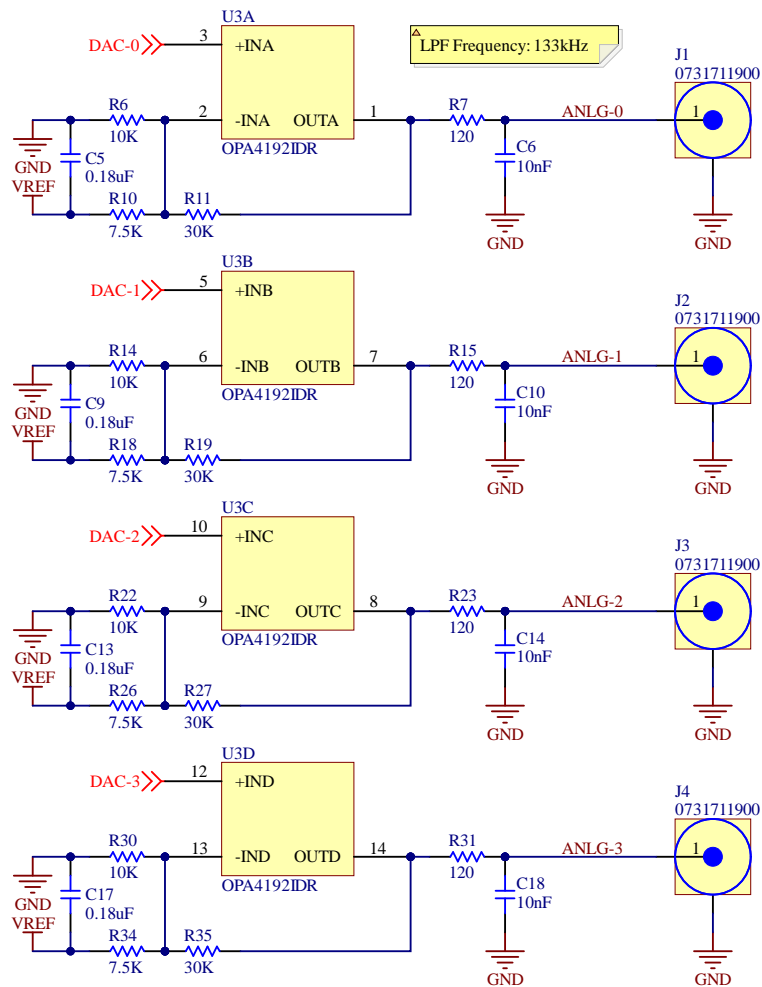
Sheet: 2 of 4

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Title: **Output Connectors**

File: OutputConnectors.SchDoc

Sheet: 3 of 4

Revision: B

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Engineer: Brandt Buchda

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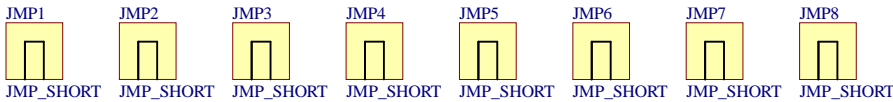
4-40 Screws:



4-40 Standoffs:



Pinout Jumpers:
Silkscreen marked with default locations



Mounting Holes:

