- Output mode: Active driving
- Pull-up/down control: the pull-up and pull-down resistance is 90KΩ with ±30% variation over PVT condition
- External Interrupt IO with 5 trigger modes: high-level, low-level, rising edge, falling edge, double edge

The digital IO AGPIO function is equivalent to GPIO as shown above. A dedicated internal control signal is used to select between the digital and analog functions. These IOs are multiplexed with 8 channels ADC.

GPIO	FUNC1	FUNC2	FUNC3	FUNC4	FUNC5
PA00	SPI1_MOSI	SD_CMD	UART0_TX	CSI_D0	EINTA0
PA01	SPI1_MISO	SD_DATA0	UART0_RX	CSI_D1	EINTA1
PA02	SPI1_CLK	SD_CLK	TWI1_SCL	CSI_D2	EINTA2
PA03	SPI1_CS0	SD_DATA1	TWI1_SDA	CSI_D3	EINTA3
PA04	UART1_CTS	SD_DATA2	TWI0_SCL	CSI_D4	EINTA4
PA05	UART1_RTS	SD_DATA3	TWI0_SDA	CSI_D5	EINTA5
PA06	UART1_TX	SPI1_CS1	TWI0_SCL	CSI_D6	EINTA6
PA07	UART1_RX	SPI1_CS2	TWI0_SDA	CSI_D7	EINTA7
PA08	ADC_CH0	PWM0/ECT0	TWI1_SCL	CSI_PCLK	EINTA8
PA09	ADC_CH1	PWM1/ECT1	TWI1_SDA	CSI_MCLK	EINTA9
PA10	ADC_CH2	PWM2/ECT2	DMIC_CLK	CSI_HSYNC	EINTA10
PA11	ADC_CH3	PWM3/ECT3	DMIC_DATA	CSI_VSYNC	EINTA11
PA12	ADC_CH4	PWM4/ECT4	I2S_MCLK	IR_TX	EINTA12
PA13	ADC_CH5	PWM5/ECT5	I2S_BCLK	32KOSCO	EINTA13
PA14	ADC_CH6	PWM6/ECT6	I2S_DI	IR_RX	EINTA14
PA15	ADC_CH7	PWM7/ECT7	I2S_DO	UART1_CTS	EINTA15
PA16	IR_TX	IR_RX	I2S_LRCLK	UART1_RTS	EINTA16
PA17	TWI0_SCL	IR_RX	TWI1_SCL	UART1_TX	EINTA17
PA18	TWI0_SDA	IR_TX	TWI1_SDA	UART1_RX	EINTA18
PA19	NUART_CTS		PWM0/ECT0	SPI1_MOSI	EINTA19
PA20	NUART_RTS		PWM1/ECT1	SPI1_MISO	EINTA20
PA21	NUART_TX	DMIC_CLK	PWM2/ECT2	SPI1_CLK	EINTA21
PA22	NUART_RX	DMIC_DATA	PWM3/ECT3	SPI1_CS0	EINTA22
PB00	UART0_TX		PWM4/ECT4		EINTB0
PB01	UART0_RX		PWM5/ECT5		EINTB1
PB02	SWD_TMS		PWM6/ECT6		EINTB2
PB03	SWD_TCK		PWM7/ECT7		EINTB3
PB04	SPI0_MOSI				EINTB4
PB05	SPI0_MISO				EINTB5
PB06	SPI0_CS0				EINTB6
PB07	SPI0_CLK				EINTB7

Table 2-1 GPIO Multiplexing

Figure 4-1 XR871GT Pin Layout

4.1.2 XR871ET Pin Layout

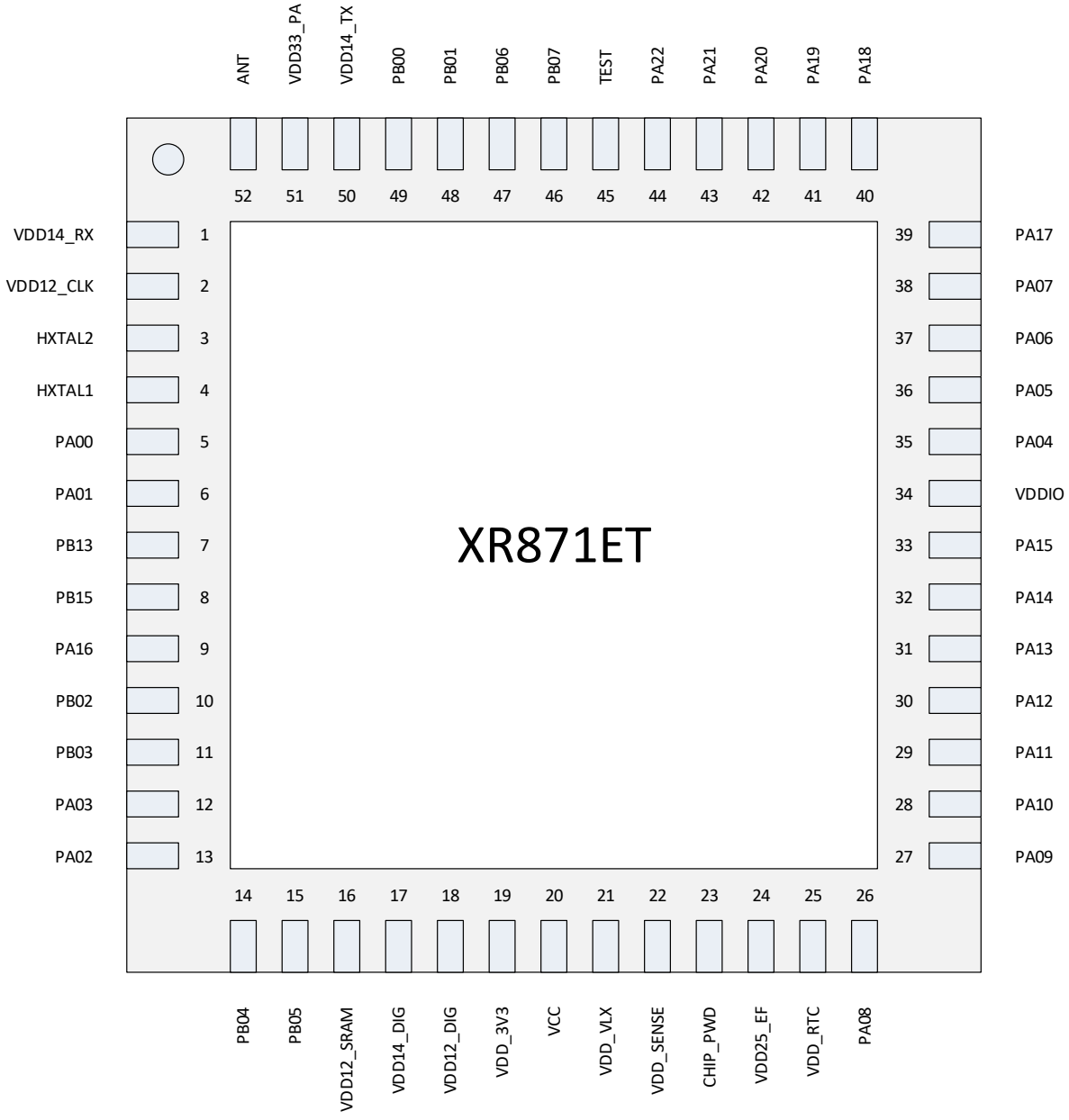

Figure 4-2 XR871ET Pin Layout

