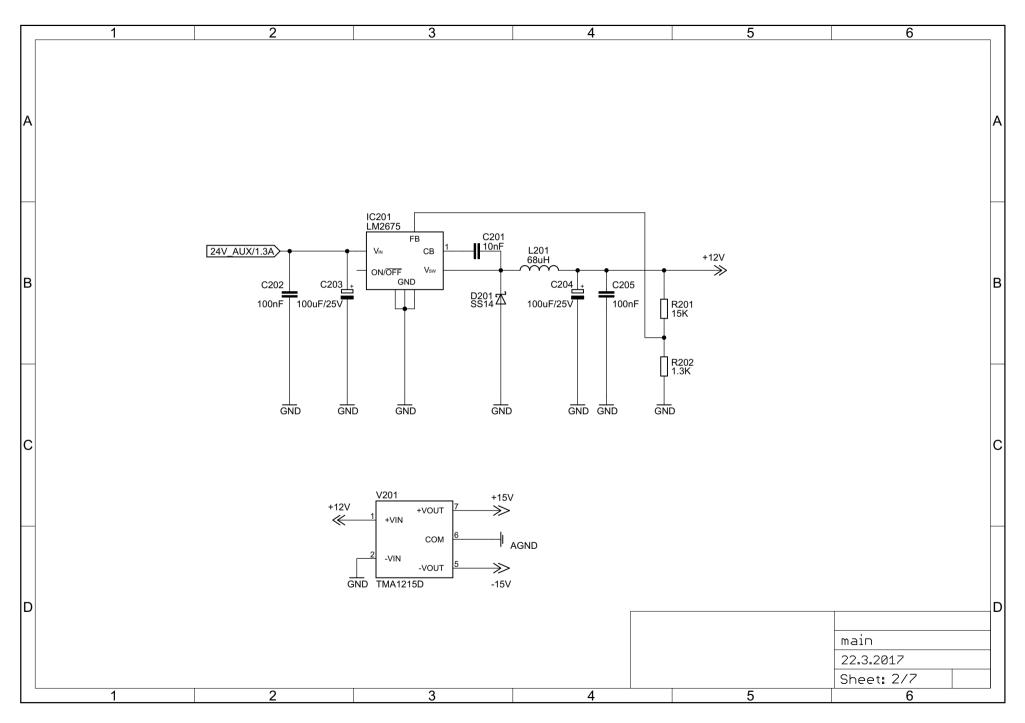
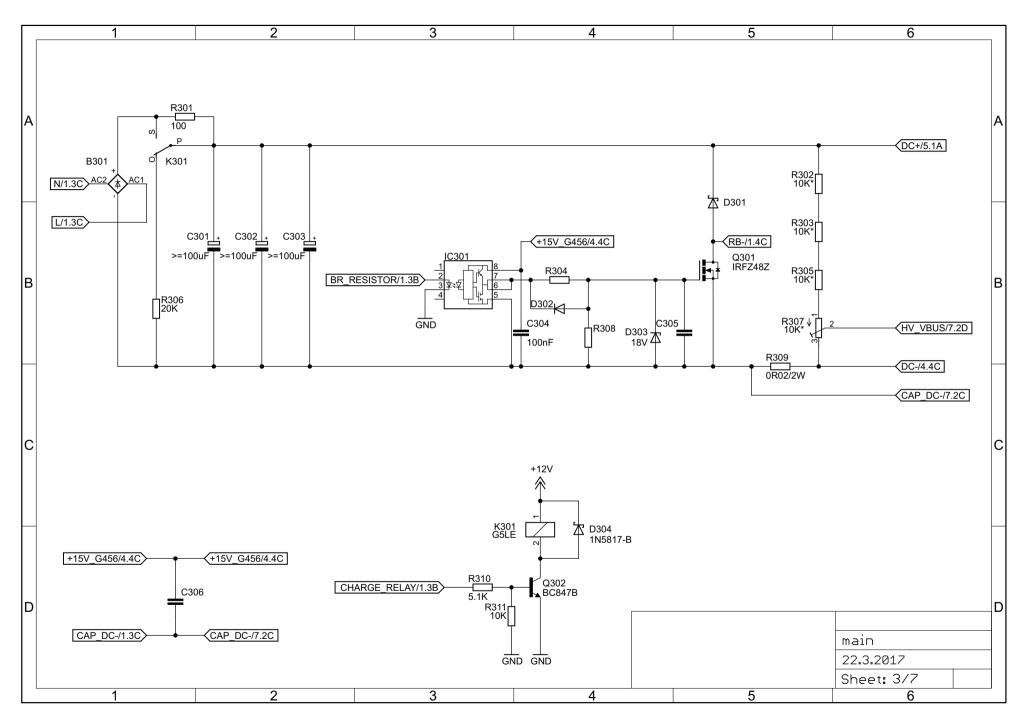


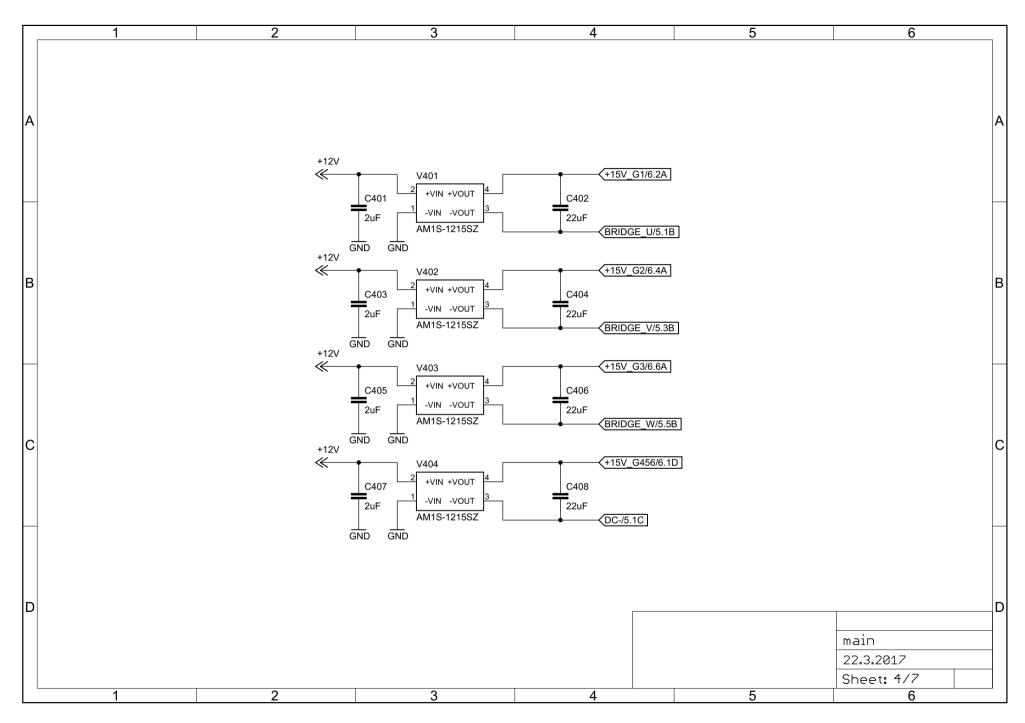
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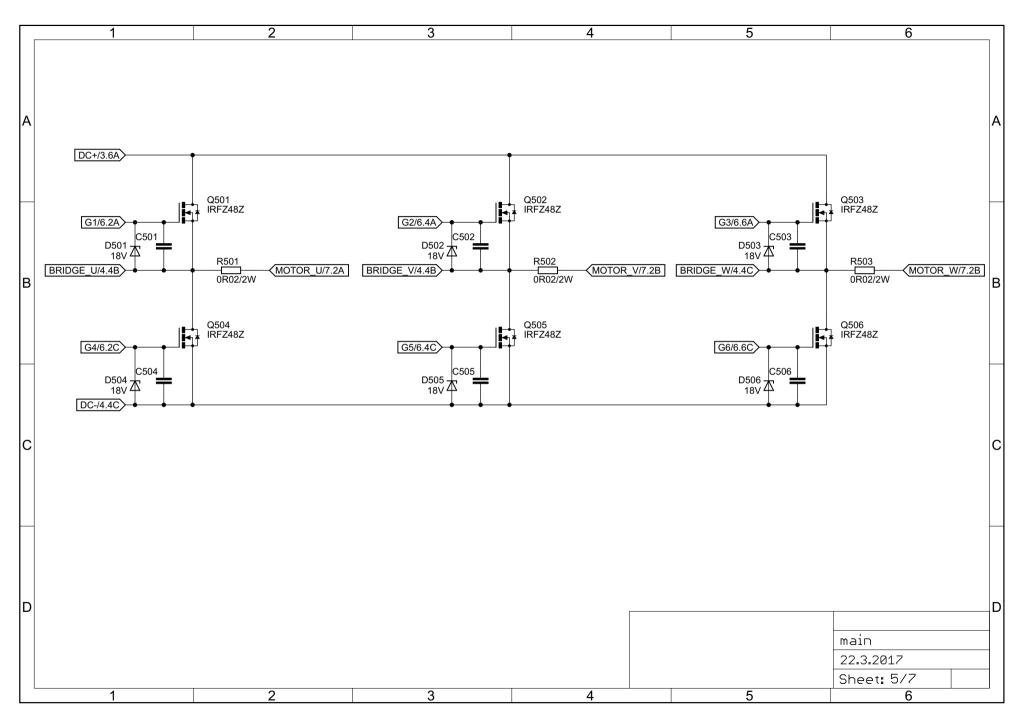
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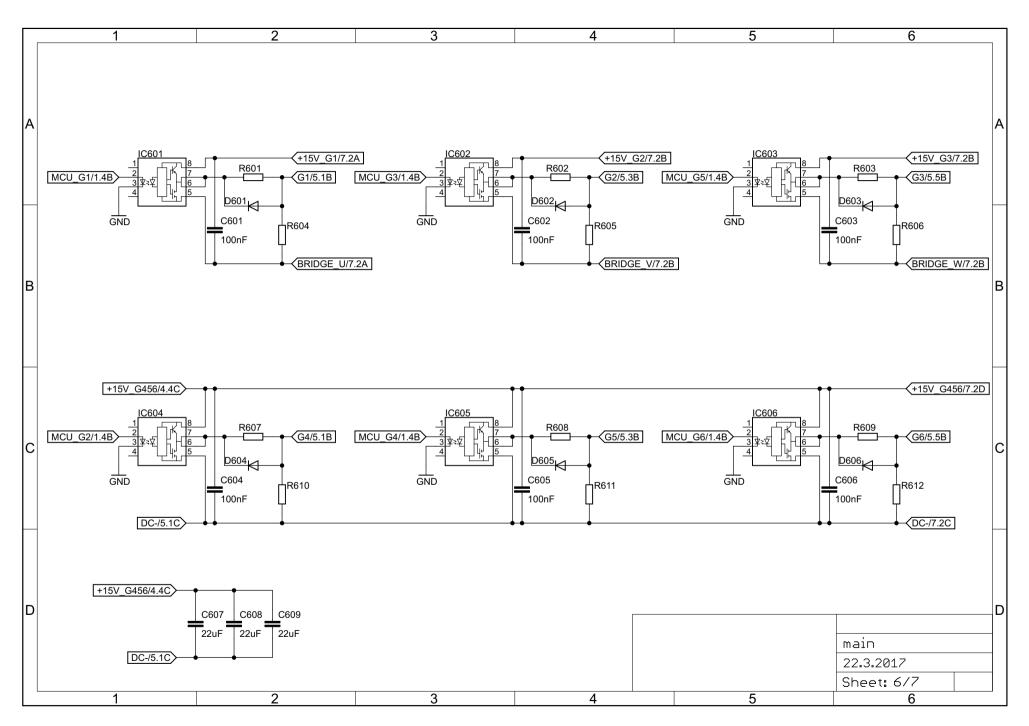
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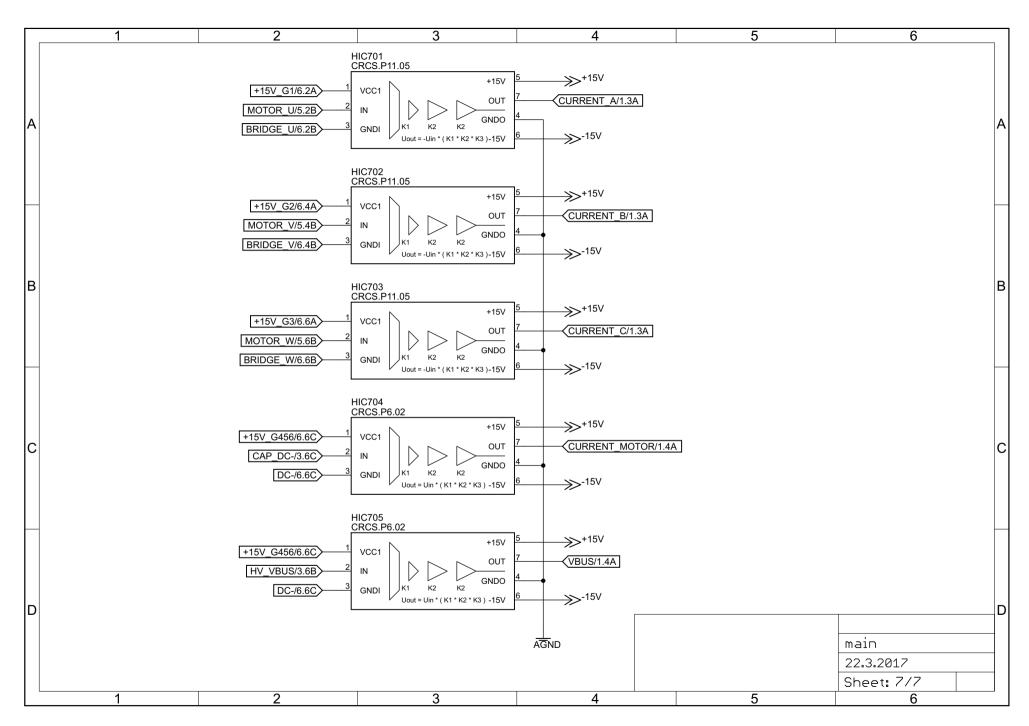
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22.3.2017 г. 22:14:49 ч. D:\Developments\Elektronika\Motor Control\Projects\BLDC Servo\Scheme and Board\v.0.0.0\Power Stage\v.0.0.0\main.sch (Sheet: 7/7)