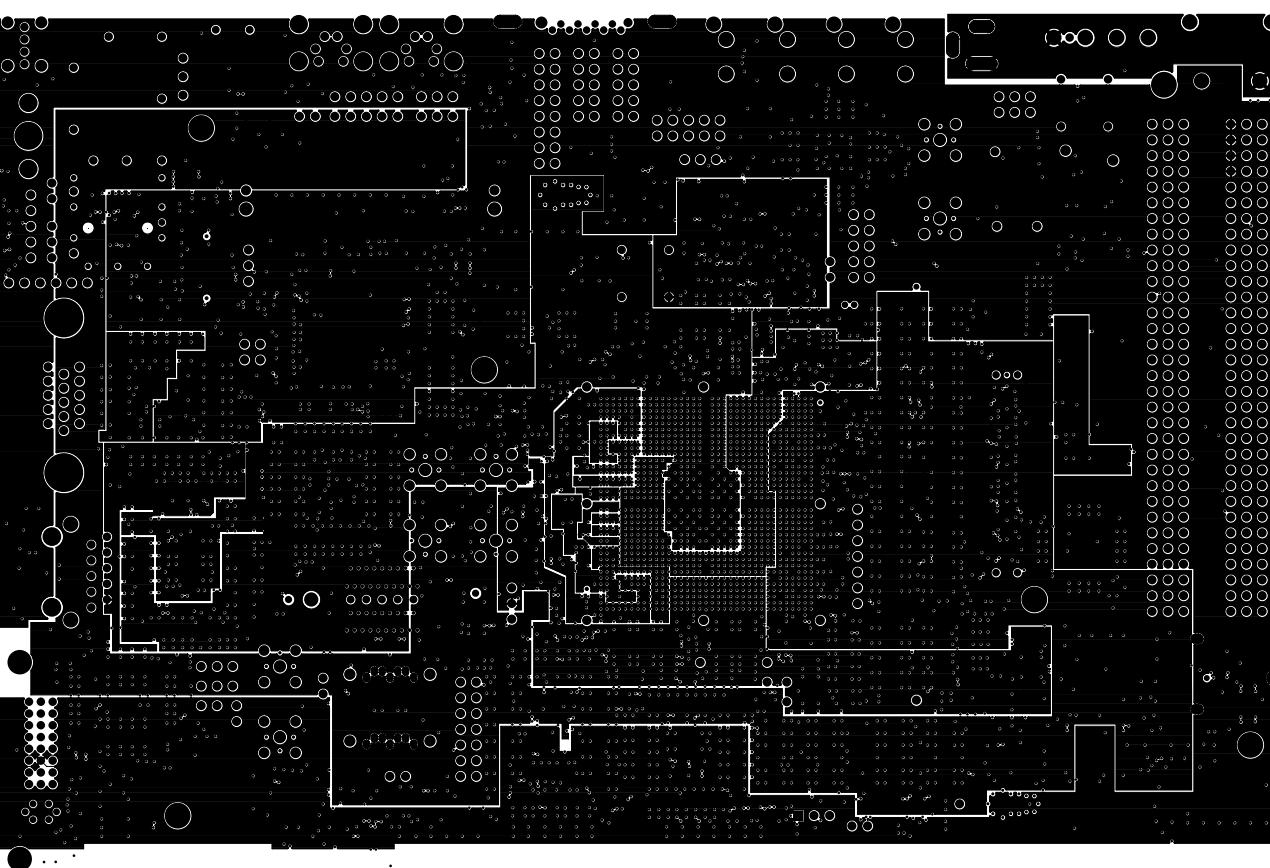
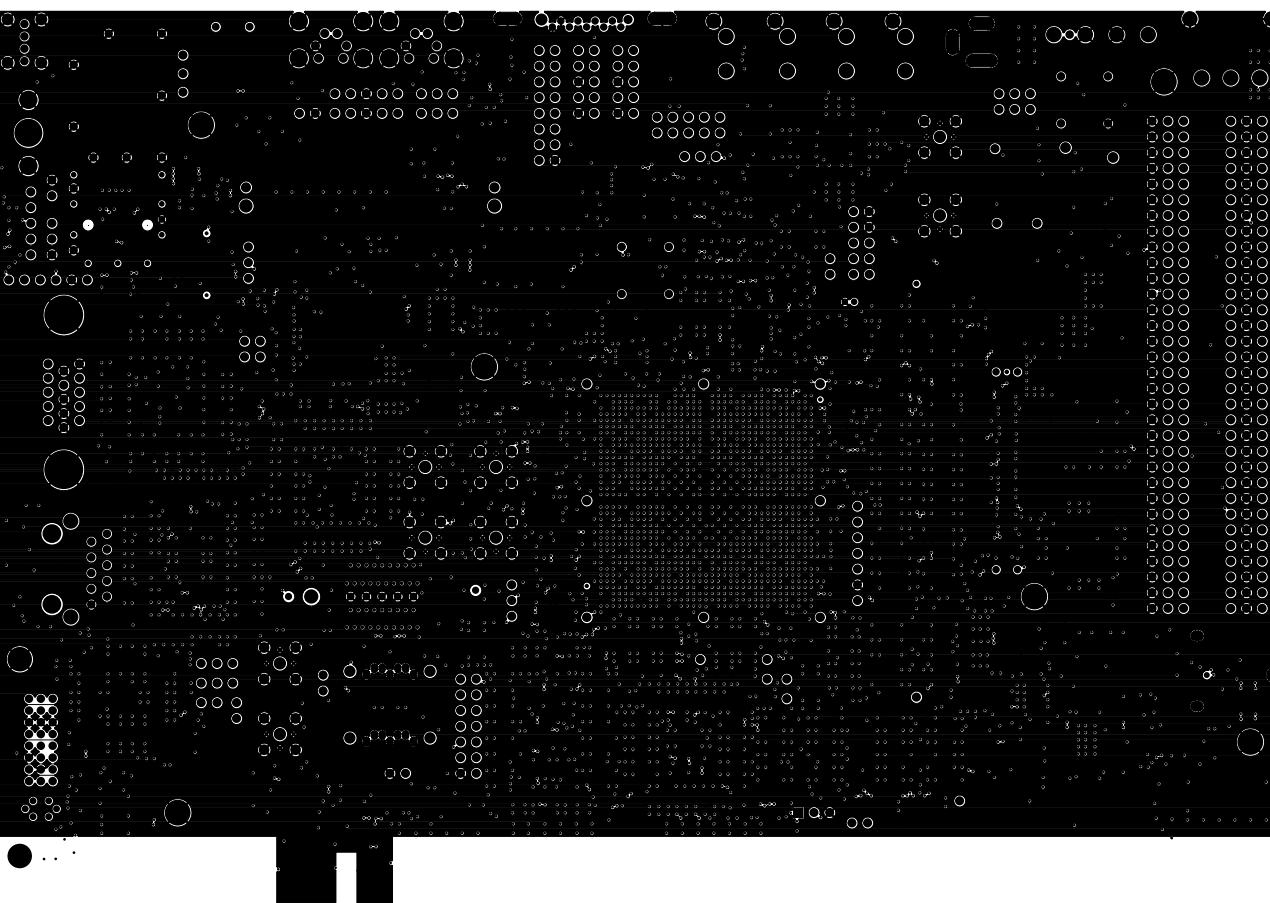


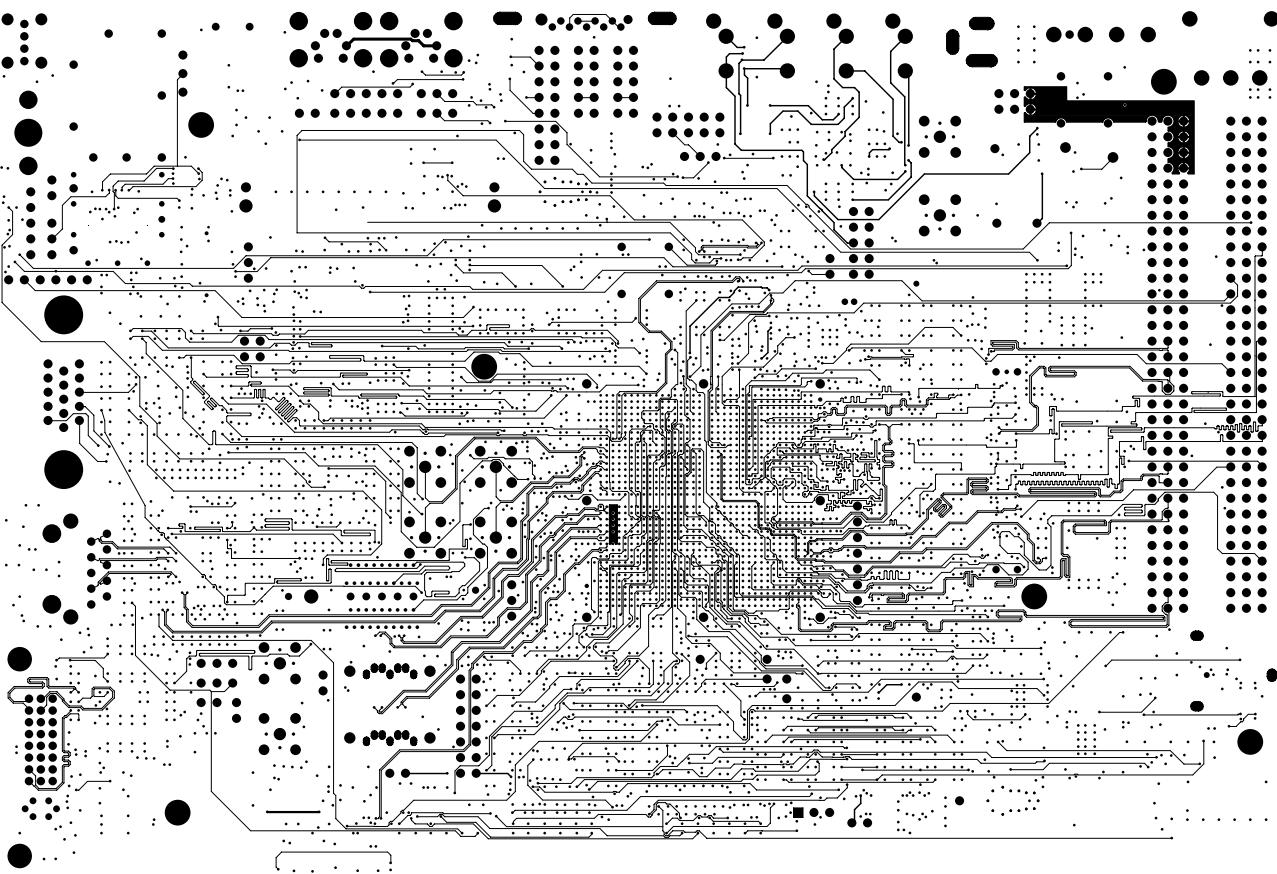
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 01 OF 25 L1_TOP



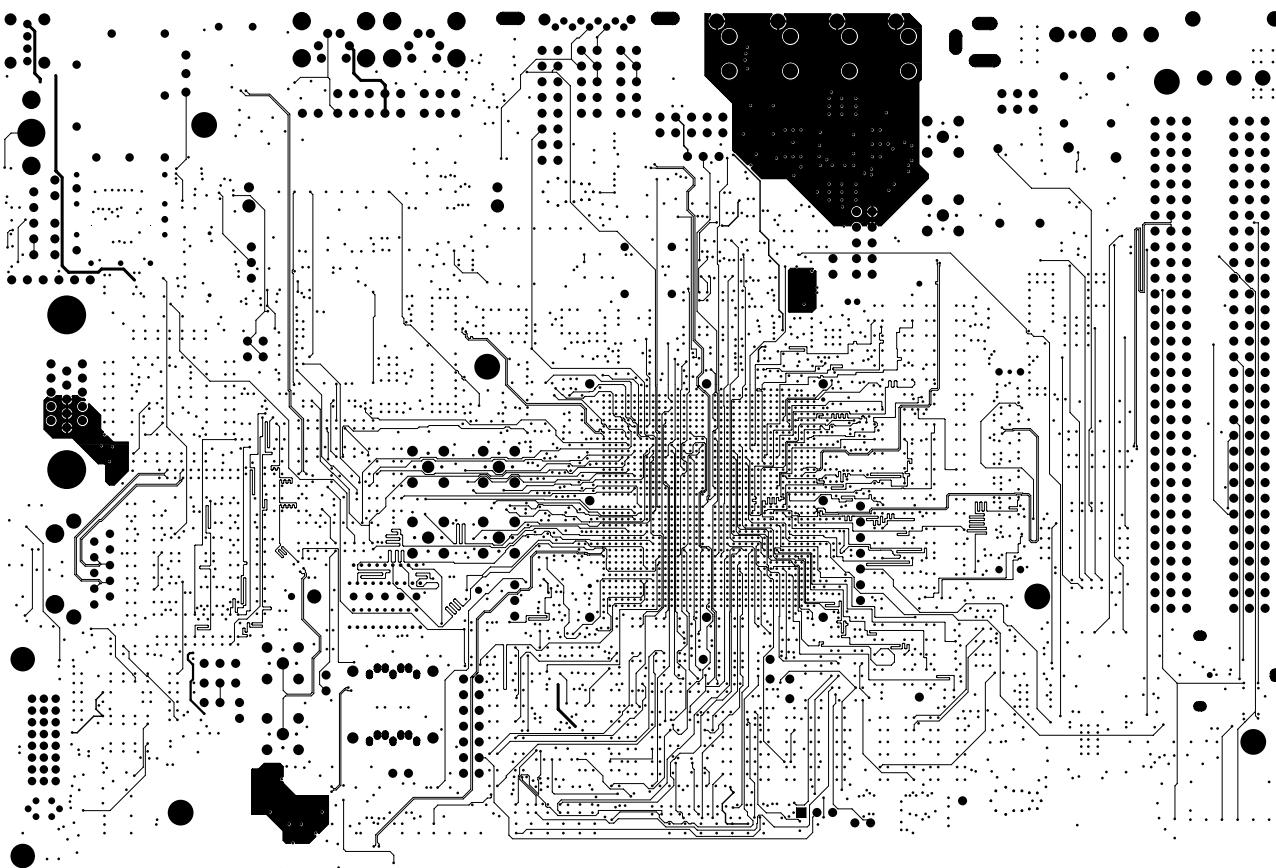
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 02 OF 25 L2_PWR



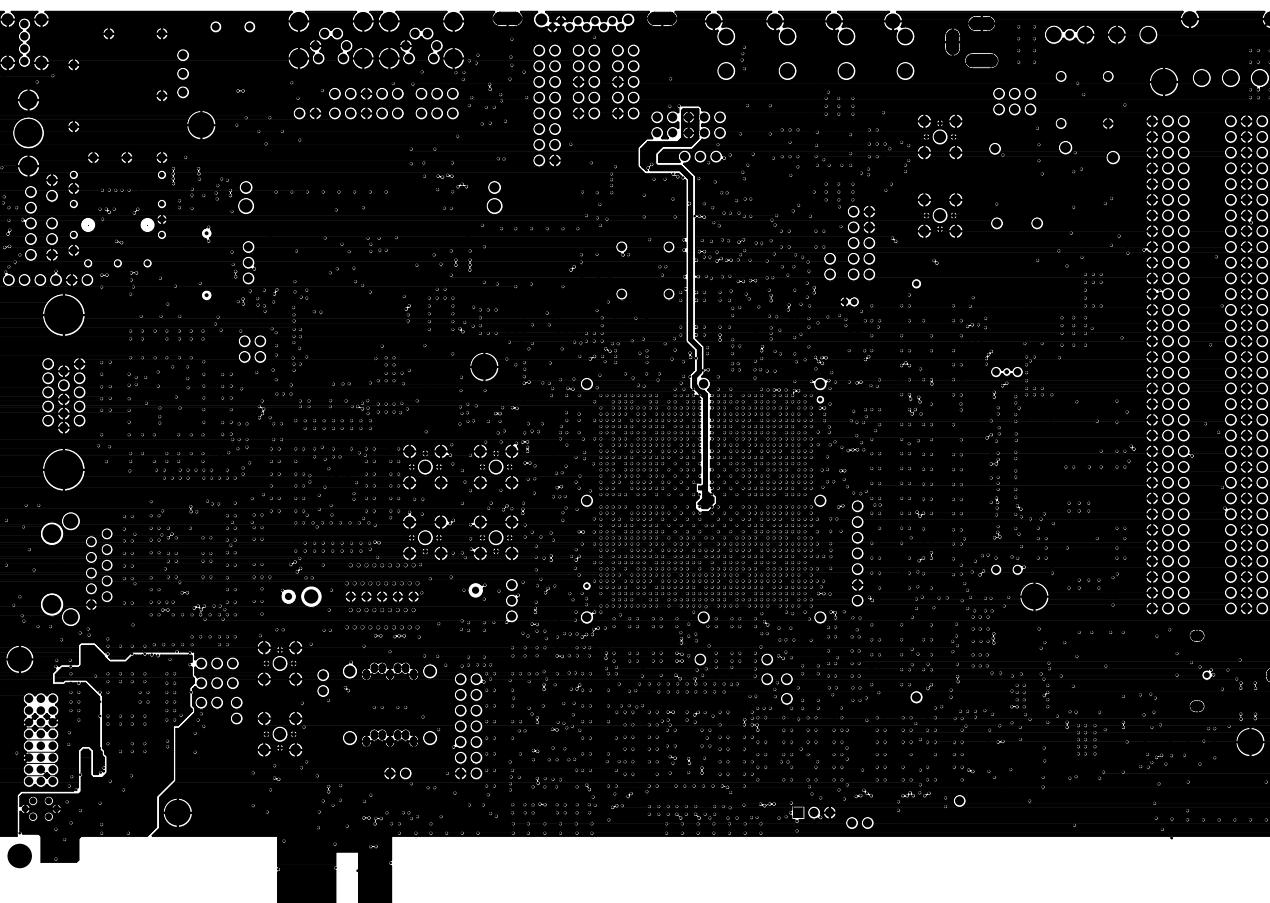
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 03 OF 25 L3_GND



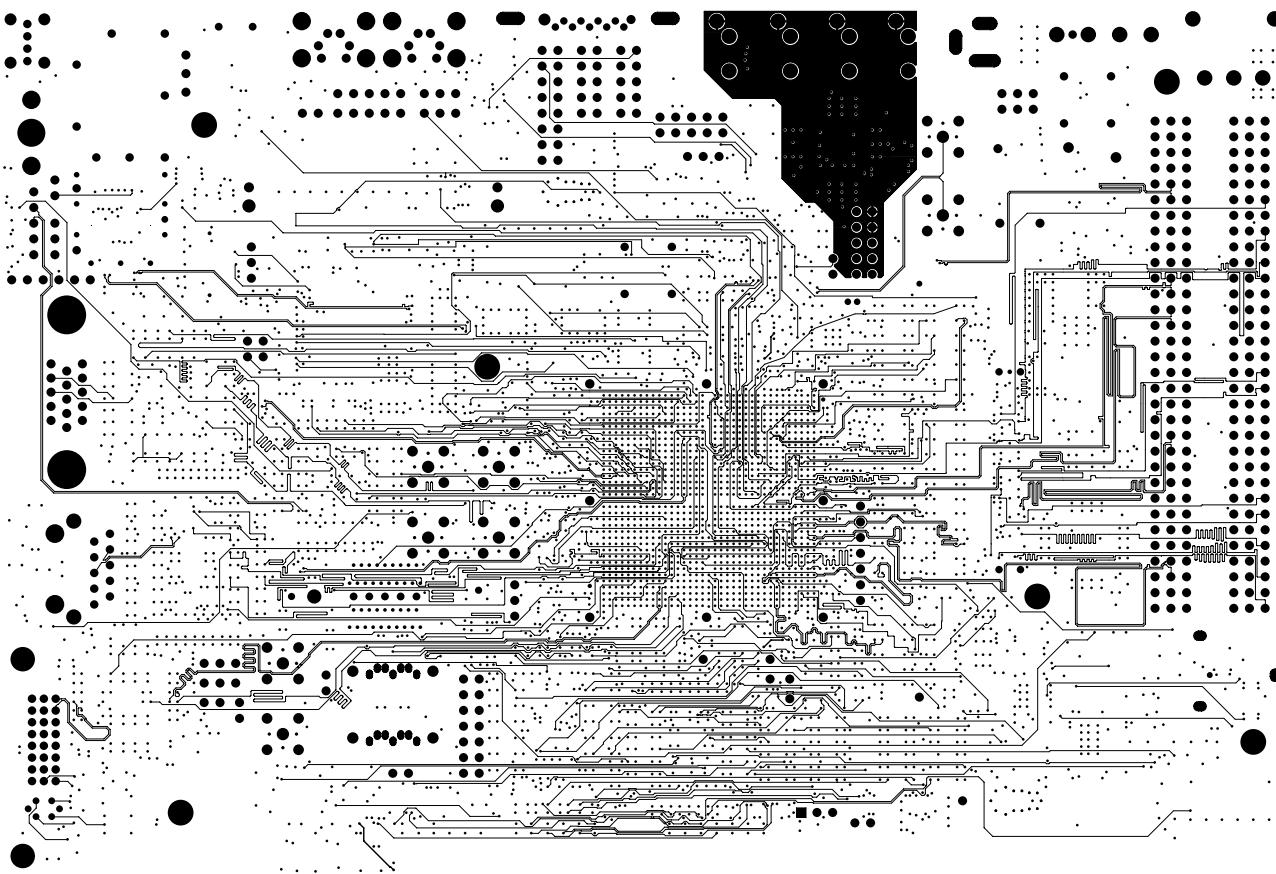
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 04 OF 25 L4_SIG



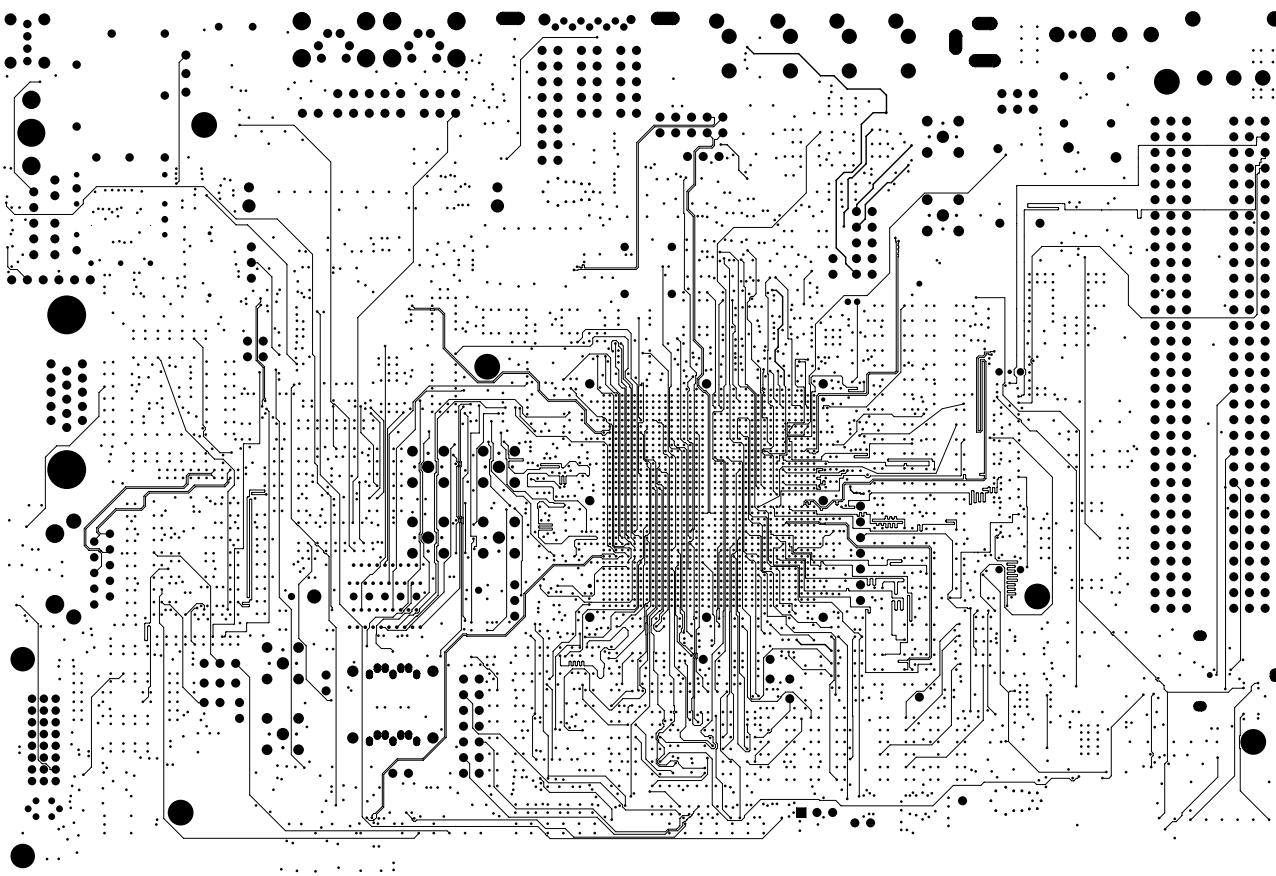
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 05 OF 25 L5_SIG



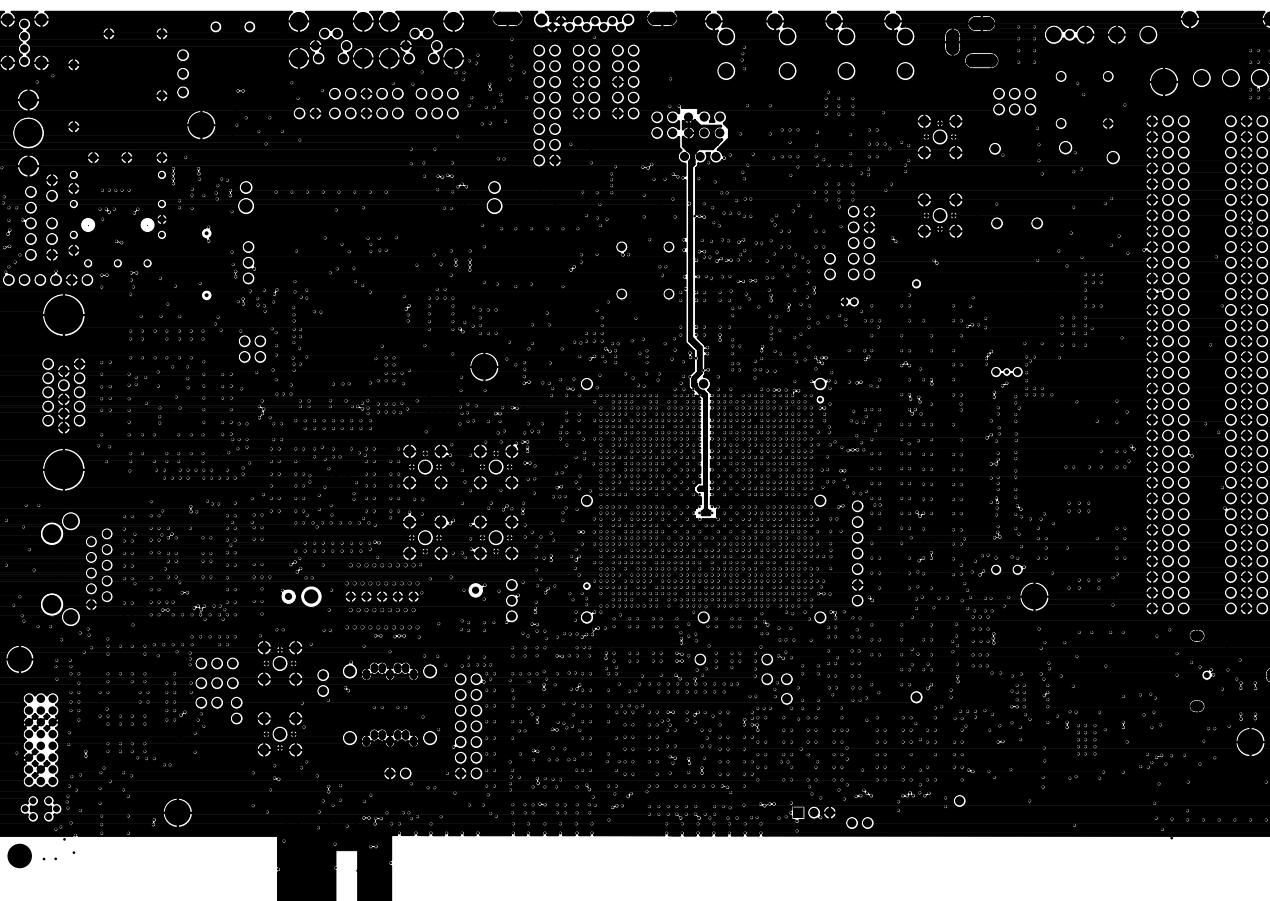
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 06 OF 25 L6_GND



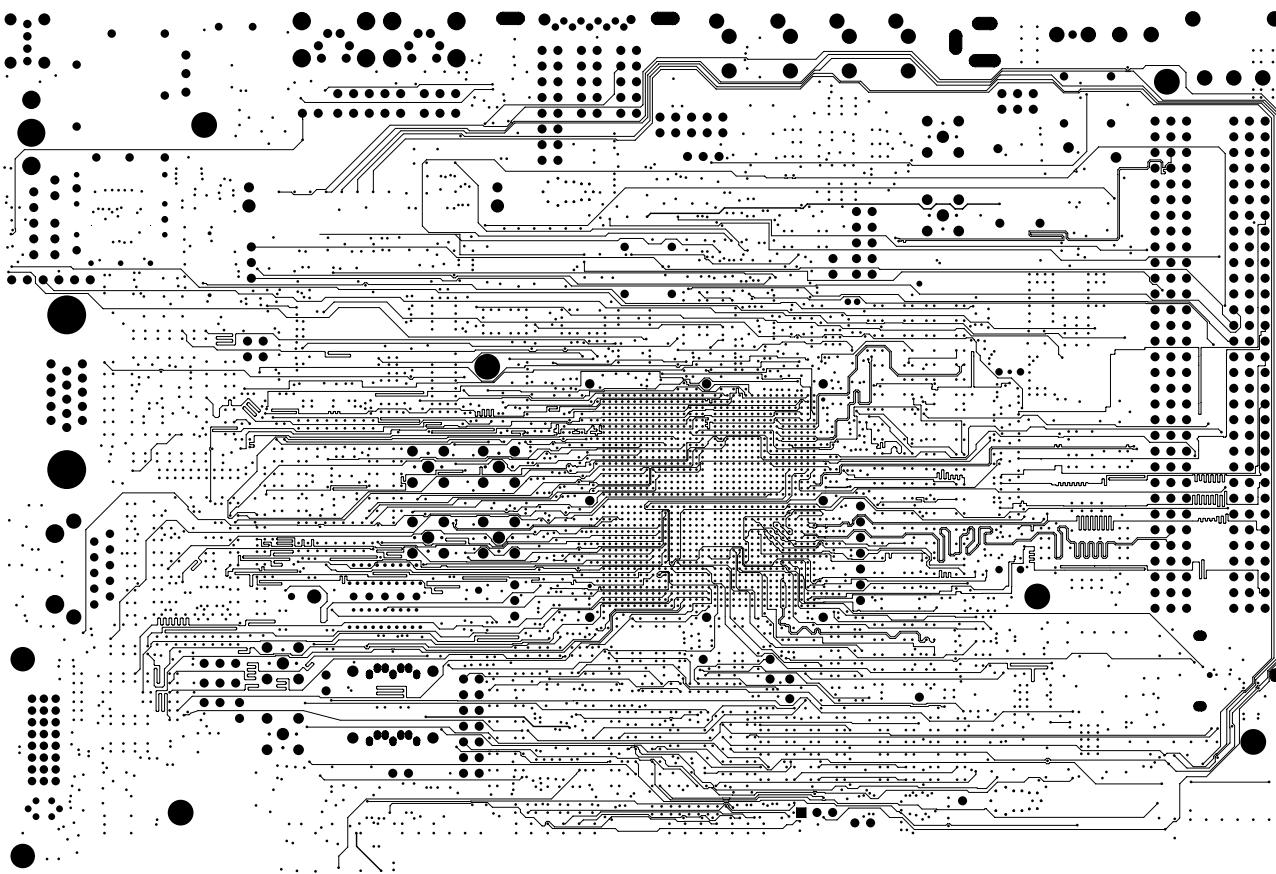
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 07 OF 25 L7_SIG



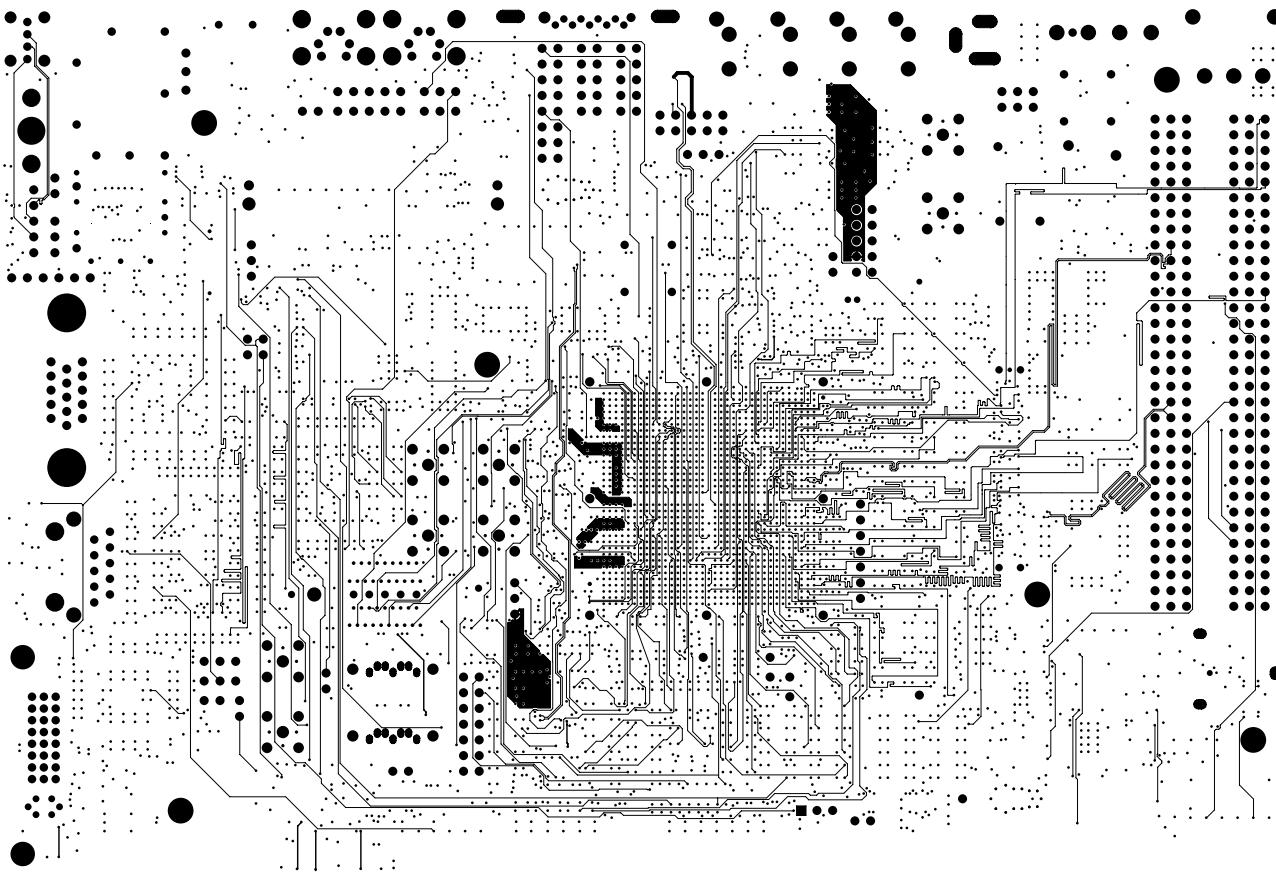
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 08 OF 25 L8_SIG



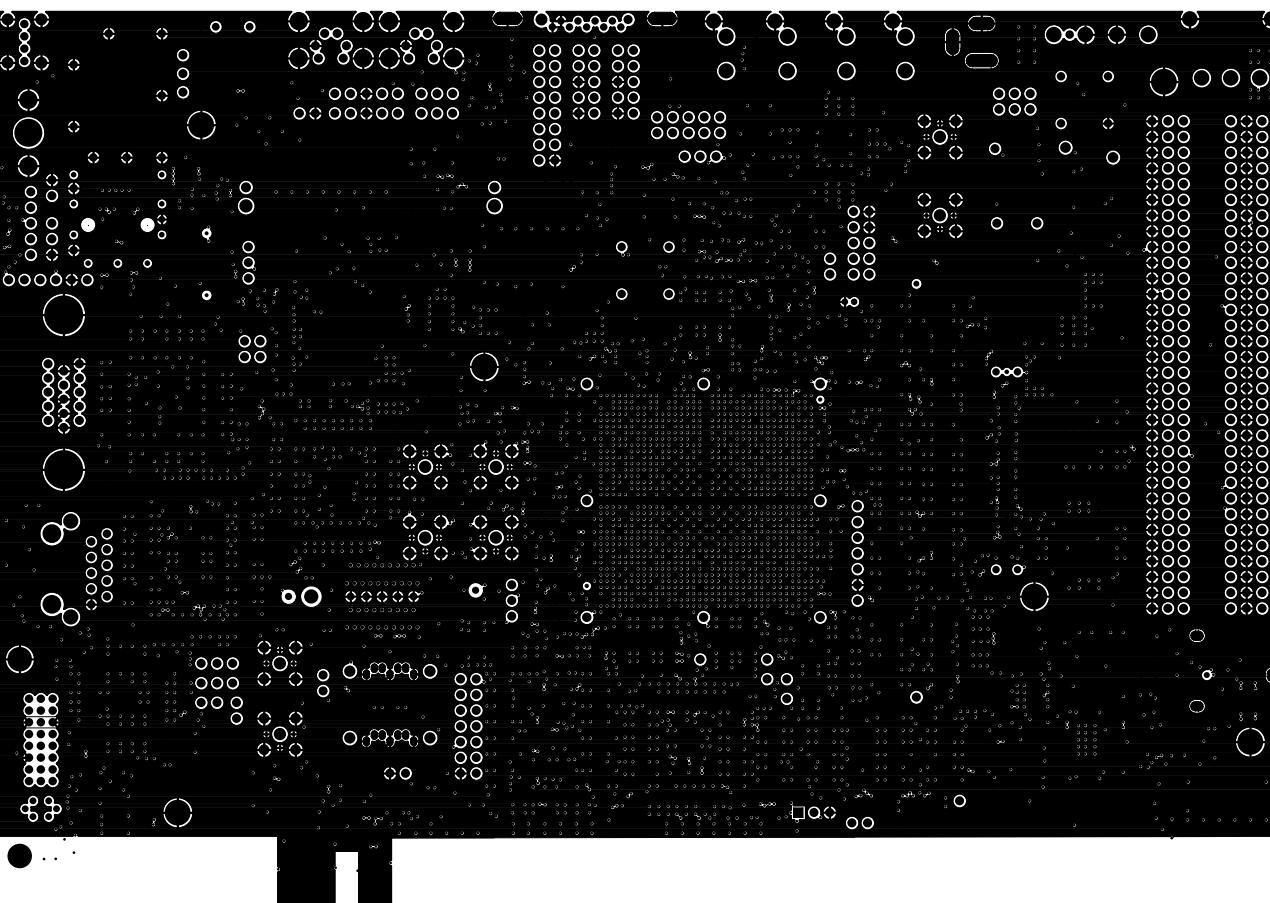
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 09 OF 25 L9_GND



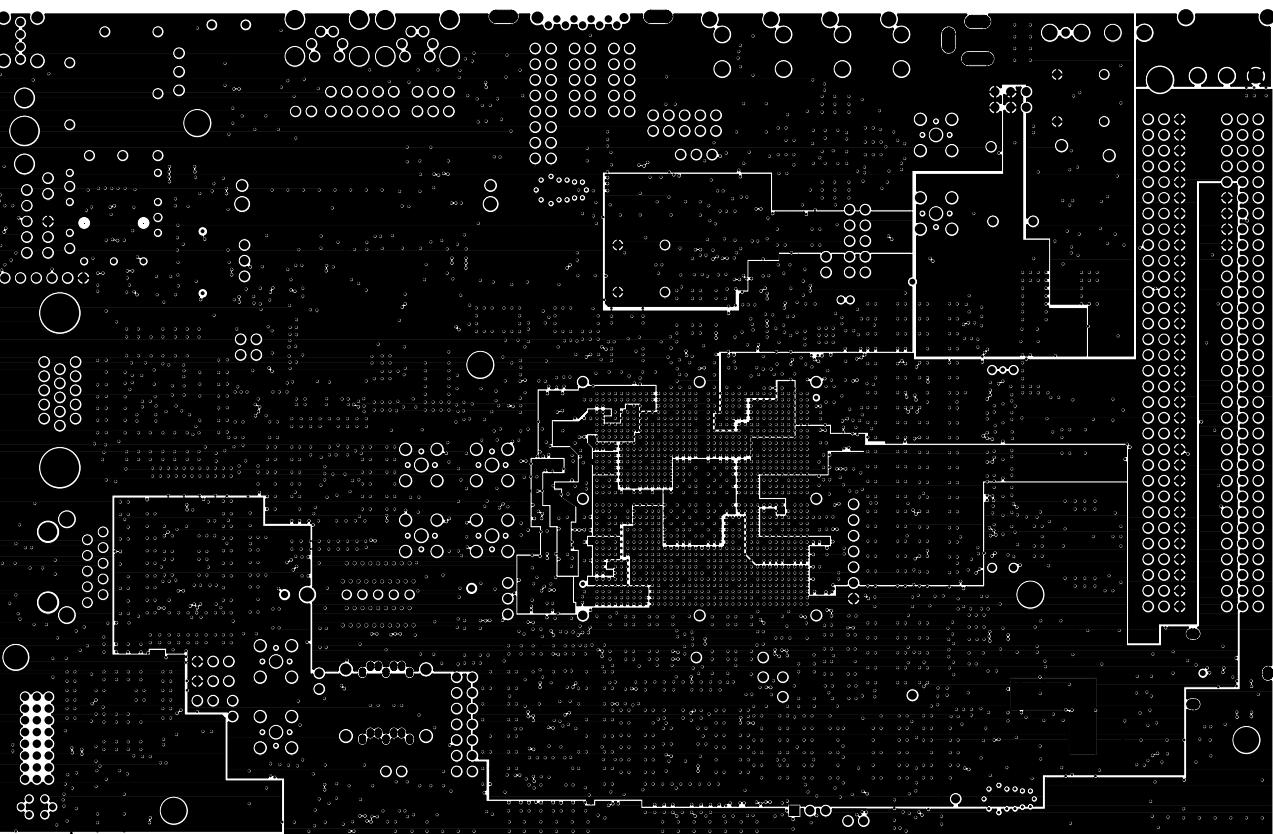
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 10 OF 25 L10_SIG



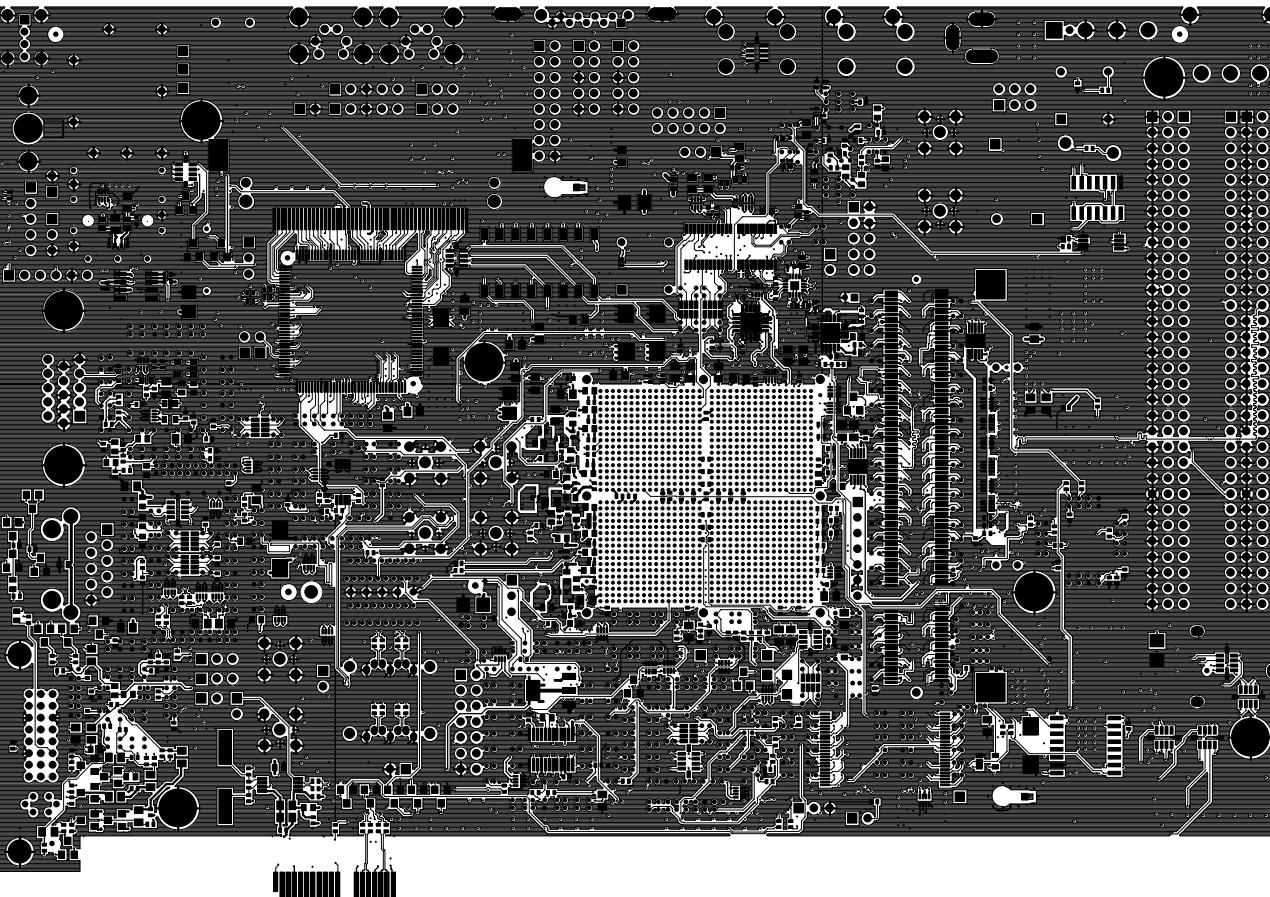
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 11 OF 25 L11_PWR



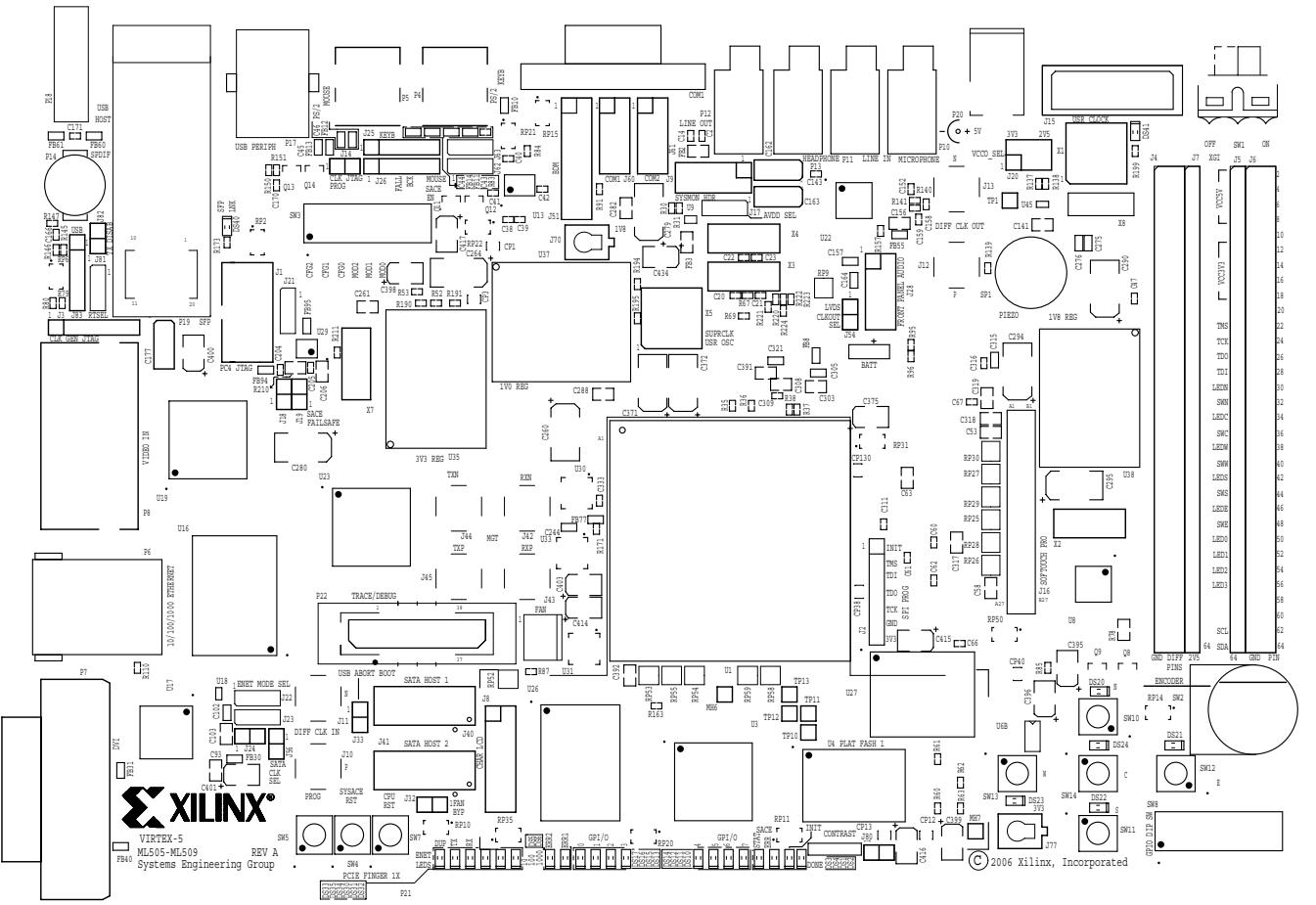
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 12 OF 25 L12_GND



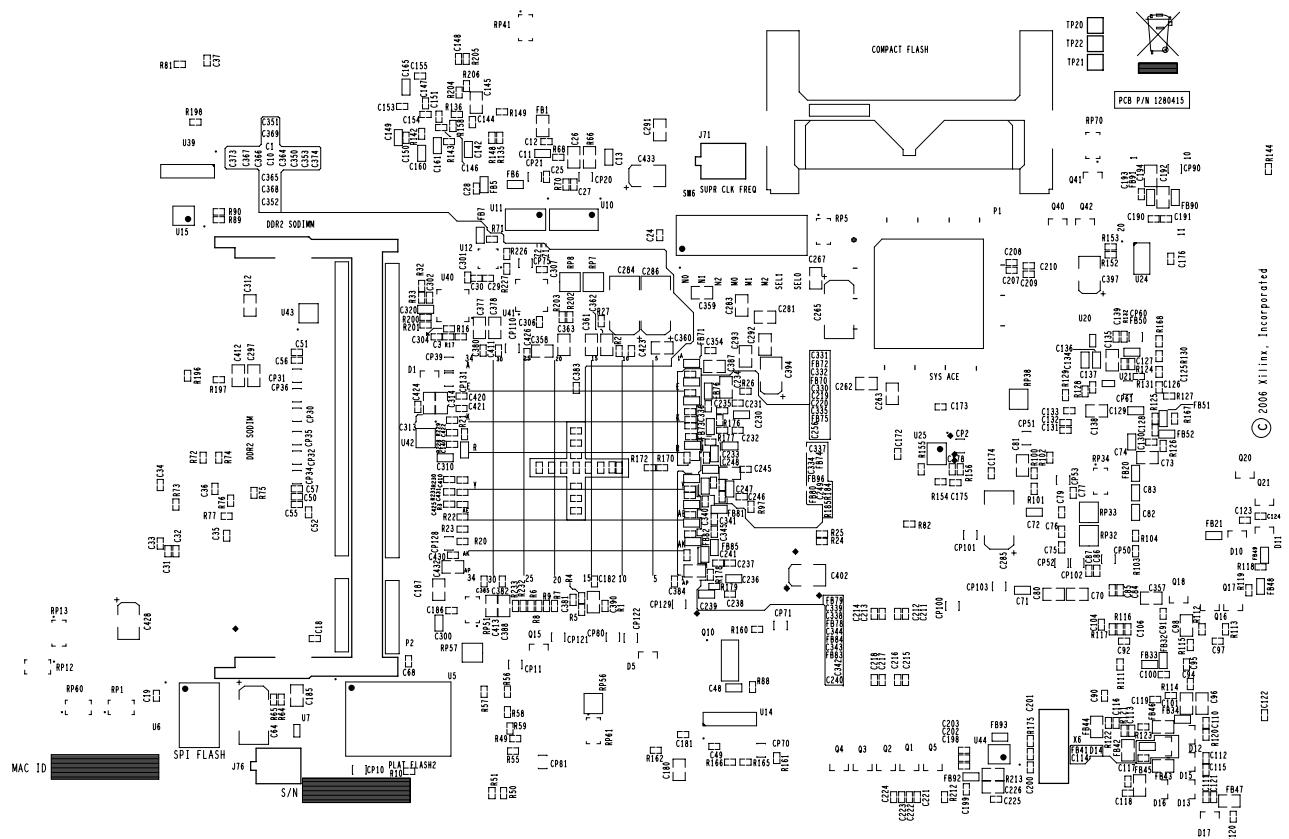
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 13 OF 25 L13_GND



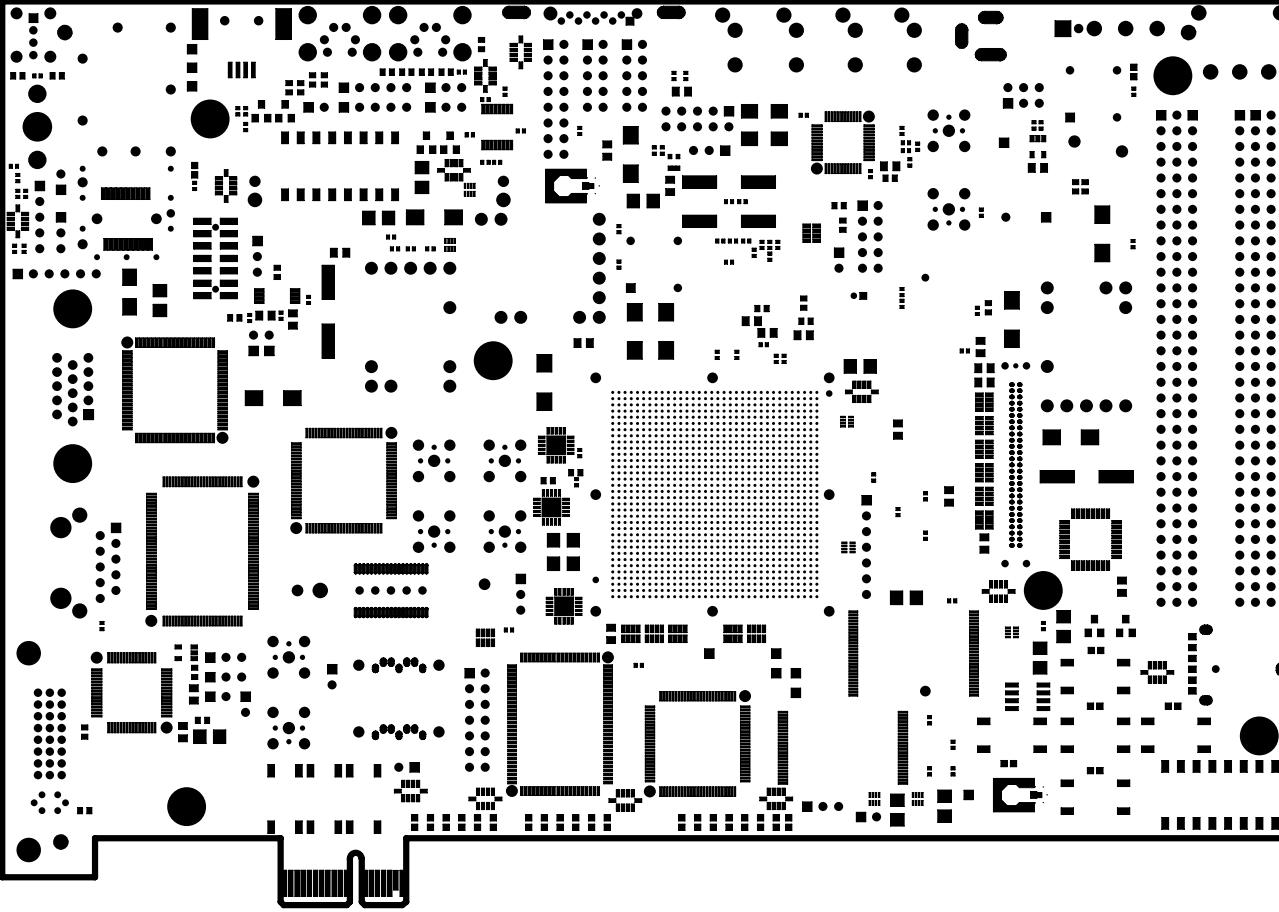
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 14 OF 25 L14_BOTTOM



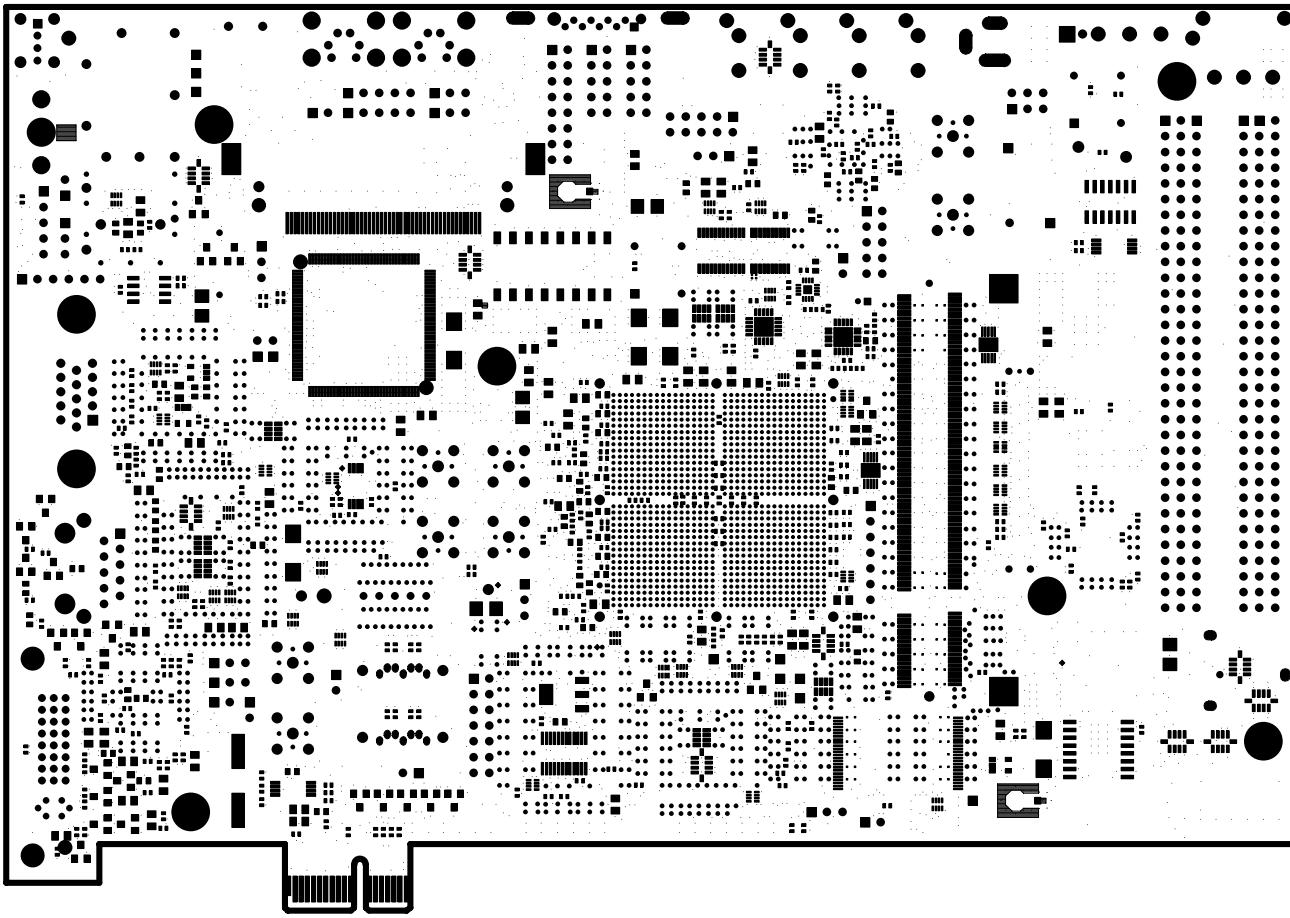
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
 ARTMASTER # 0531630
 Designed by Xilinx Oct 04 2006
 LAYER:
 19 OF 25 SILK-SCREEN TOP



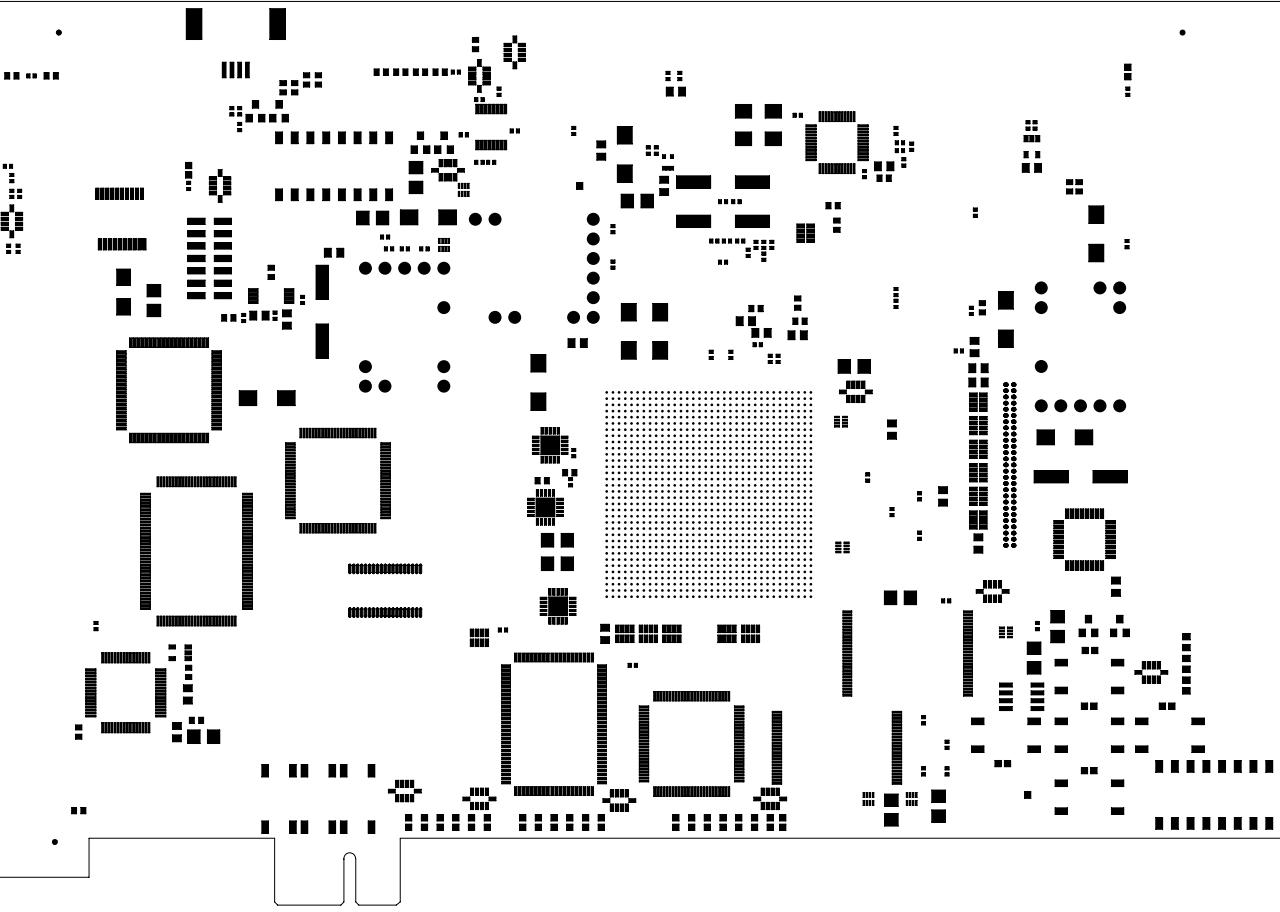
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 20 OF 25 SILK_SCREEN_BOTTOM



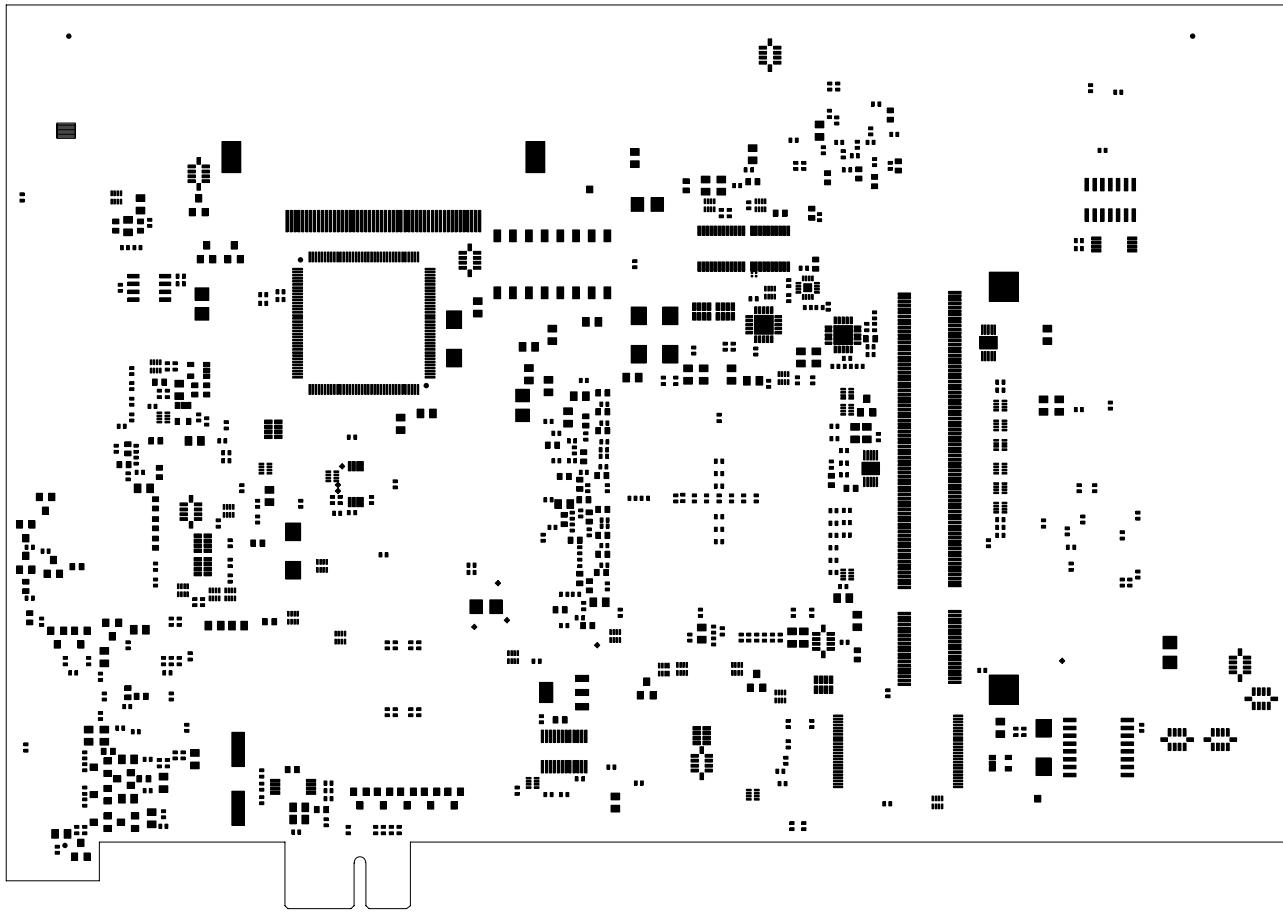
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 15 OF 25 MASK TOP



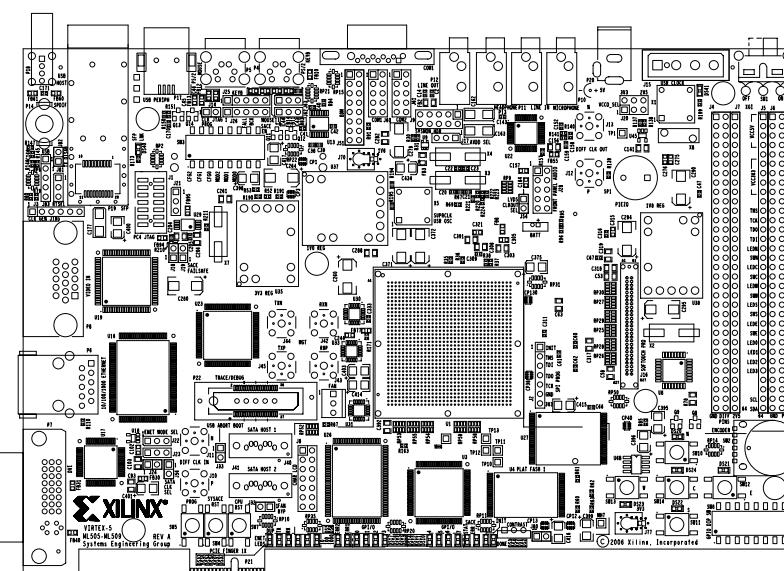
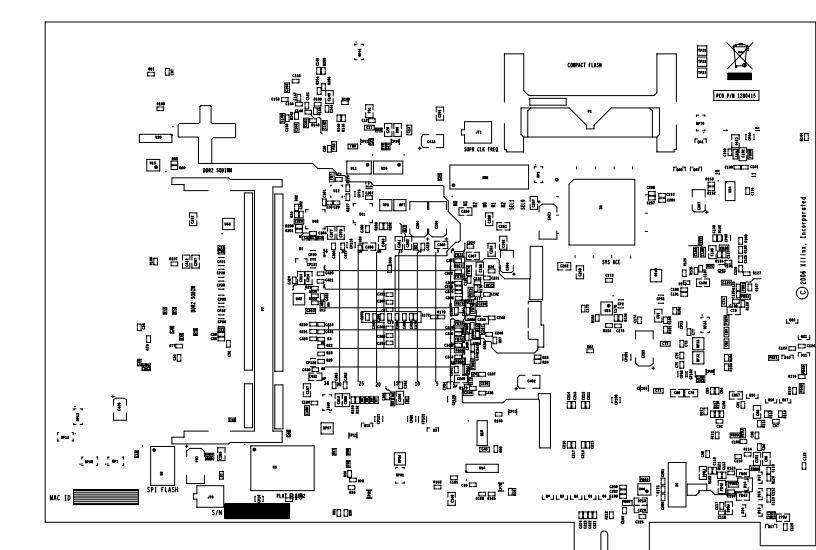
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 16 OF 25 MASK BOTTOM

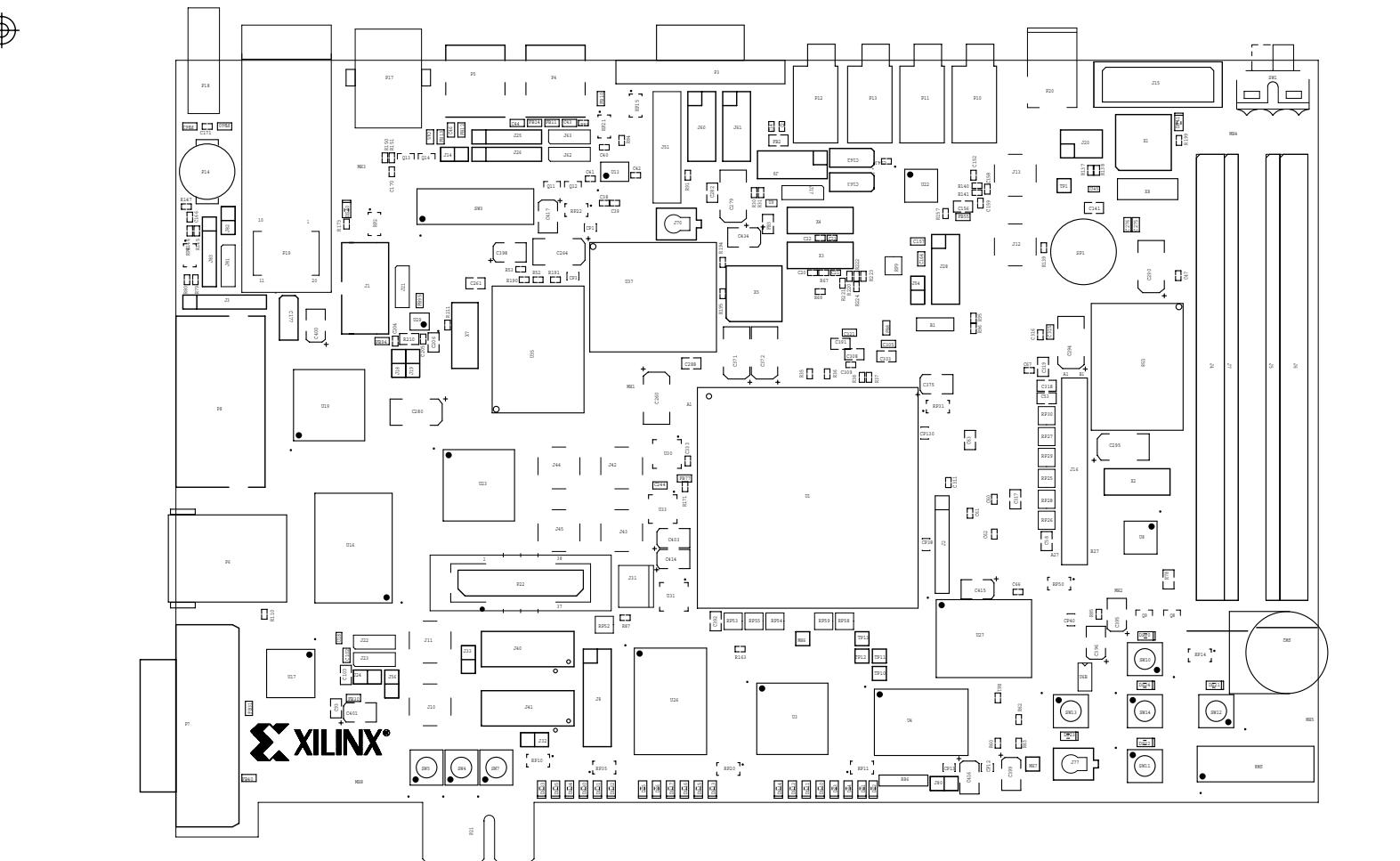


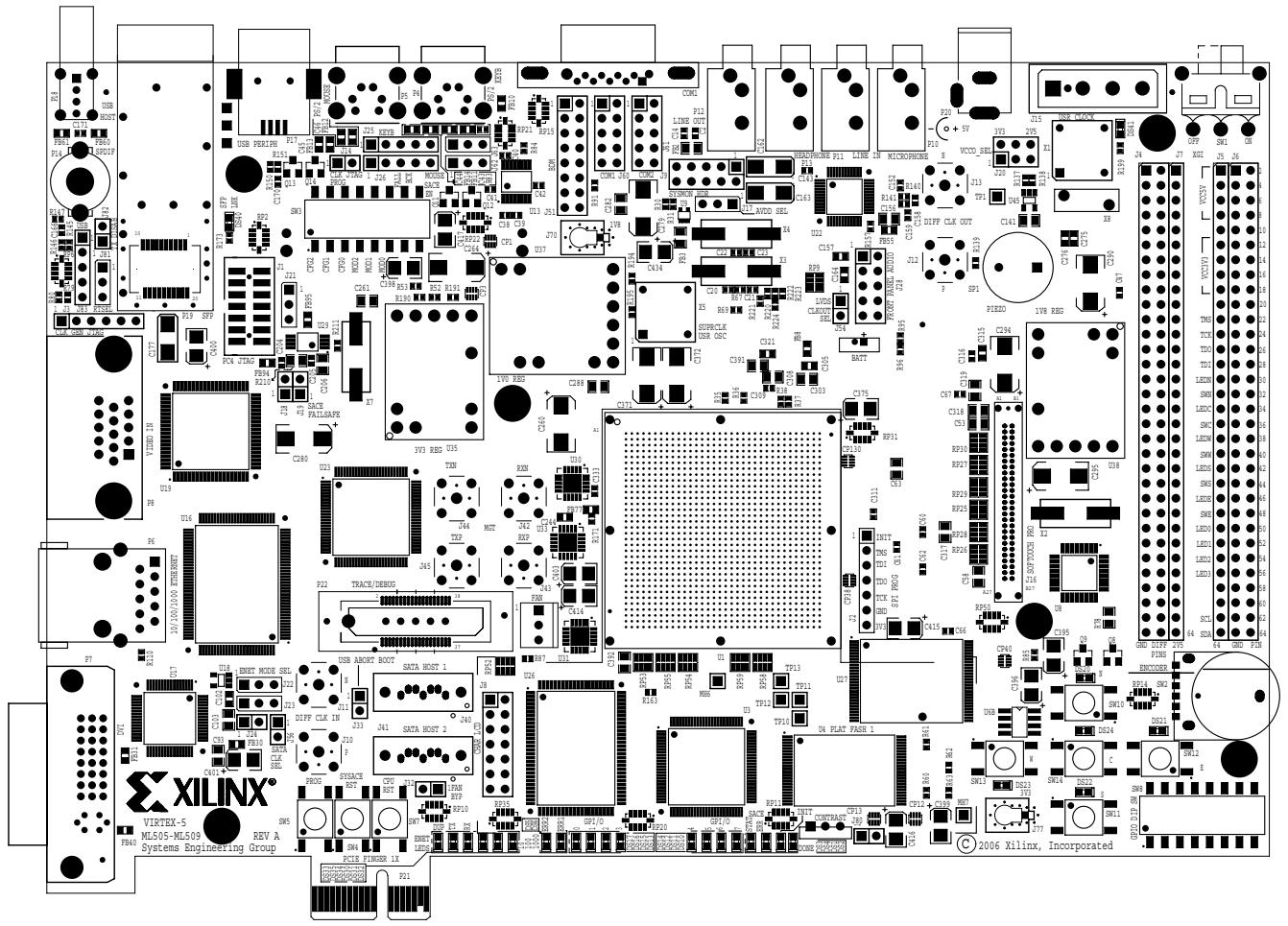
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 17 OF 25 PASTE TOP



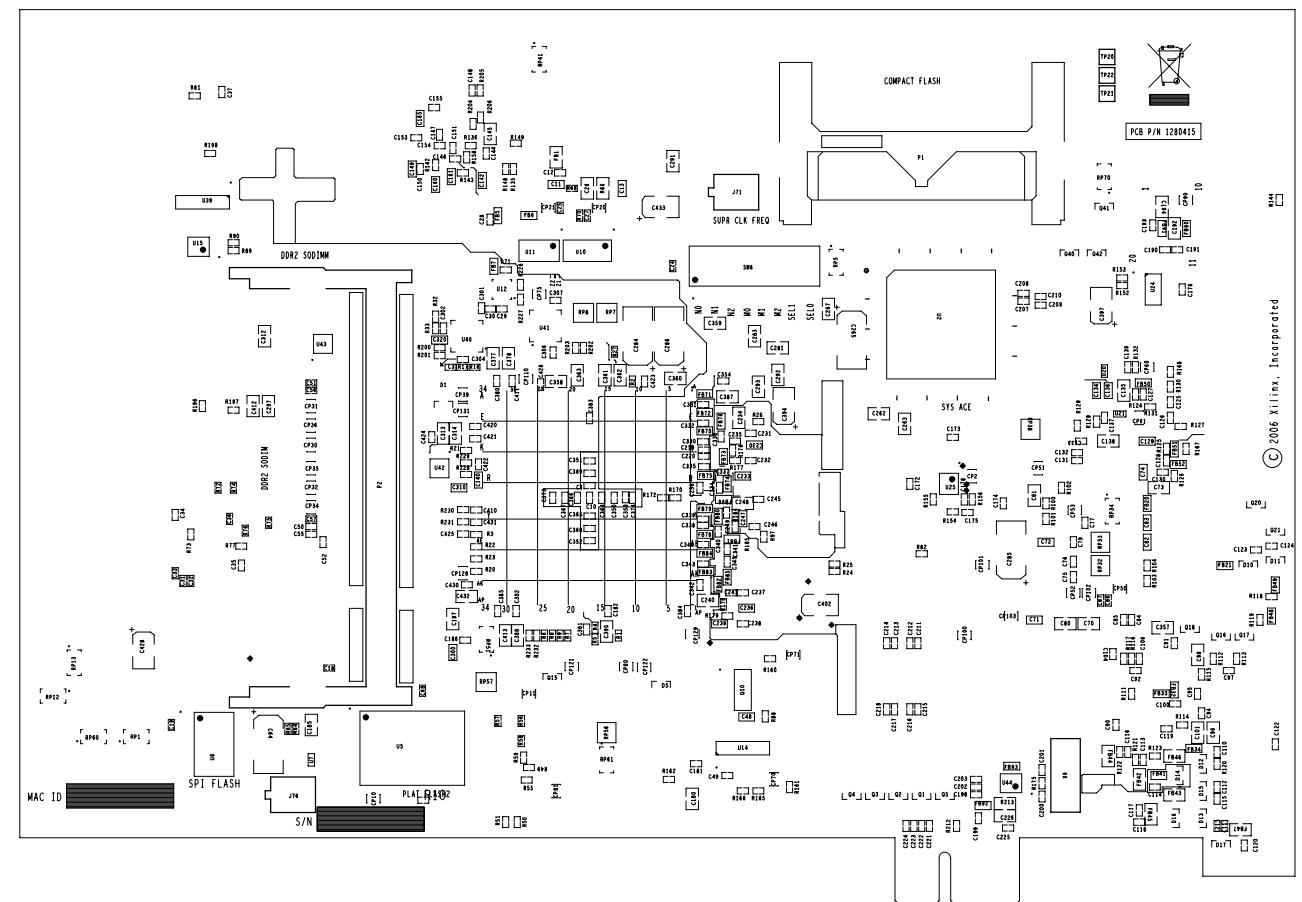
ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 18 OF 25 PASTE BOTTOM

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<p>D</p> <table border="1" style="border-collapse: collapse; width: 100%;"> <tr> <td style="width: 10%; text-align: center;">REV 01</td> <td style="width: 90%; text-align: left; padding-left: 5px;"> THE INFORMATION IN THIS DRAWING IS THE SOLE PROPERTY OF THE XILINX CORPORATION. ANY REPRODUCTION IN PART OR WHOLE WITHOUT THE WRITTEN PERMISSION OF XILINX CORPORATION. IS PROHIBITED </td> </tr> <tr> <td colspan="2" style="text-align: right; padding-right: 5px;"> DESCRIPTION </td> </tr> <tr> <td colspan="2" style="text-align: right; padding-right: 5px;"> REV </td> </tr> <tr> <td colspan="2" style="text-align: right; padding-right: 5px;"> DESCRIPTION </td> </tr> <tr> <td colspan="2" style="text-align: right; padding-right: 5px;"> DATE </td> </tr> <tr> <td colspan="2" style="text-align: right; padding-right: 5px;"> APPROVED </td> </tr> </table>	REV 01	THE INFORMATION IN THIS DRAWING IS THE SOLE PROPERTY OF THE XILINX CORPORATION. ANY REPRODUCTION IN PART OR WHOLE WITHOUT THE WRITTEN PERMISSION OF XILINX CORPORATION. IS PROHIBITED	DESCRIPTION		REV		DESCRIPTION		DATE		APPROVED			<p>D</p>  <p>ASSEMBLY NOTES:</p> <ol style="list-style-type: none"> 1. INSTALL SHUNT WHERE SHOWN. 2. ASSEMBLE SCREWS, BUMPERS AND STANDOFFS ON SIDE CLOSEST TO THE POWER JACK. <p>VIEWED FROM TOP SIDE</p>	 <p>VIEWED FROM BOTTOM SIDE</p>	<p>C</p> <table border="1" style="border-collapse: collapse; width: 100%;"> <tr> <td style="width: 10%; text-align: center;">REV 01</td> <td style="width: 90%; text-align: left; padding-left: 5px;"> DWG NO. 0431402 </td> </tr> </table> <p>B</p> <p>A</p> <p>ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415 ARTMASTER # 0531630 Designed by Xilinx Oct 04 2006 LAYER: 25 of 25 ASSEMBLY</p>	REV 01	DWG NO. 0431402																																																										
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ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006
LAYER: 22 OF 25 ASSY TOP
19 OF 25 SILK-SCREEN TOP



ARTWORK, ROHS COMPLIANT, ML505 VIRTEX-5 LX EVALUATION PLATFORM, 1280415
ARTMASTER # 0531630
Designed by Xilinx Oct 04 2006

