	Fully Dedicated	Shared Axon	Shared Synapse	Shared Dendrite	Fully Shared
Synapse Circuits	N^2	N^2	N	N/A	1
RAM Words	N^2	N^2	N^2	N^2/A	N^2
Bits / spike	1	$\log(N)$	$N(\log(N)+B)$	$N(\log(N)+B)/A$	$(f_{\text{step}}/f_{\text{spk}})NB$