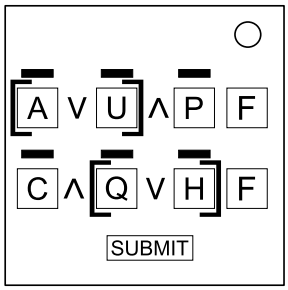
On the Subject of Limiting Logic



The logical conclusion of all the Logic manual pages.

Symbol	Gate	Symbol	Statement
∧	AND (both true)	↓	NOR (both false)
∨	OR (at least 1 is true)	↔	XNOR (both the same)
⊻	XOR (both different)	→	Implies (NOT left true + right false)
|	NAND (NOT both true)	←	Implies (NOT right true + left false)

Batteries (A G H J N U)

Condition	Letter(s)
0-1 batteries	H
2-4 batteries	G
5+ batteries	G J
1 holder	U
3+ holders	N
batteries = indicators	A

Ports (F M P Q R Y Z)

Condition	Letter(s)
0-1 ports	Z
2 ports	Q
6+ ports	Y
2+ port types	F
all unique ports	M
Parallel	P
PS/2	R

Indicators (C D E K L O T W X)

Condition	Letter(s)
0 total ind.	W
1 total ind.	X
3+ total ind.	L
1 lit ind.	K
1 unlit ind.	E
lit and unlit ind.	O
"IND"	C
"FRK"	D
"MSA"	T

Serial Number (B I S V)

Condition	Letter(s)
letters > numbers	B
last digit is odd	I
sum of digits > 10	S
vowel	V