2 nd proposal (integrator omitted) > to ADC / your Comparator Corporations. Nother Buffer needed wince we're wing the \$ LMV762/767 which we're wing the \$\text{1/2/767 which is cleighed to work without a buffer (pt imput curent \$\text{4}) 10 ppm relixbry (2020€ each =) 0, u0 (= ;) Rim 117 tigure ga 173379 dolashat (1) ADC input: 2-3 p. A expect current 4,5 V cliff for DGC supply for the ADC norded Make Pin/Pin, 2 such that we can accept the ± 12 V from the integrator directly! => Convert ± 12V to ± 4V (plus - Cle QSV offiet, but that's done by the ordered offut at the CT 6350, sel figure 90) From the LT6350 destarhed: Vout, diff = 4V = -2. V. Rinz (Rf)

Rin (R6)

Also

O, 16 = R = 3,01K

R = 18K

O, 16 = R = 18K