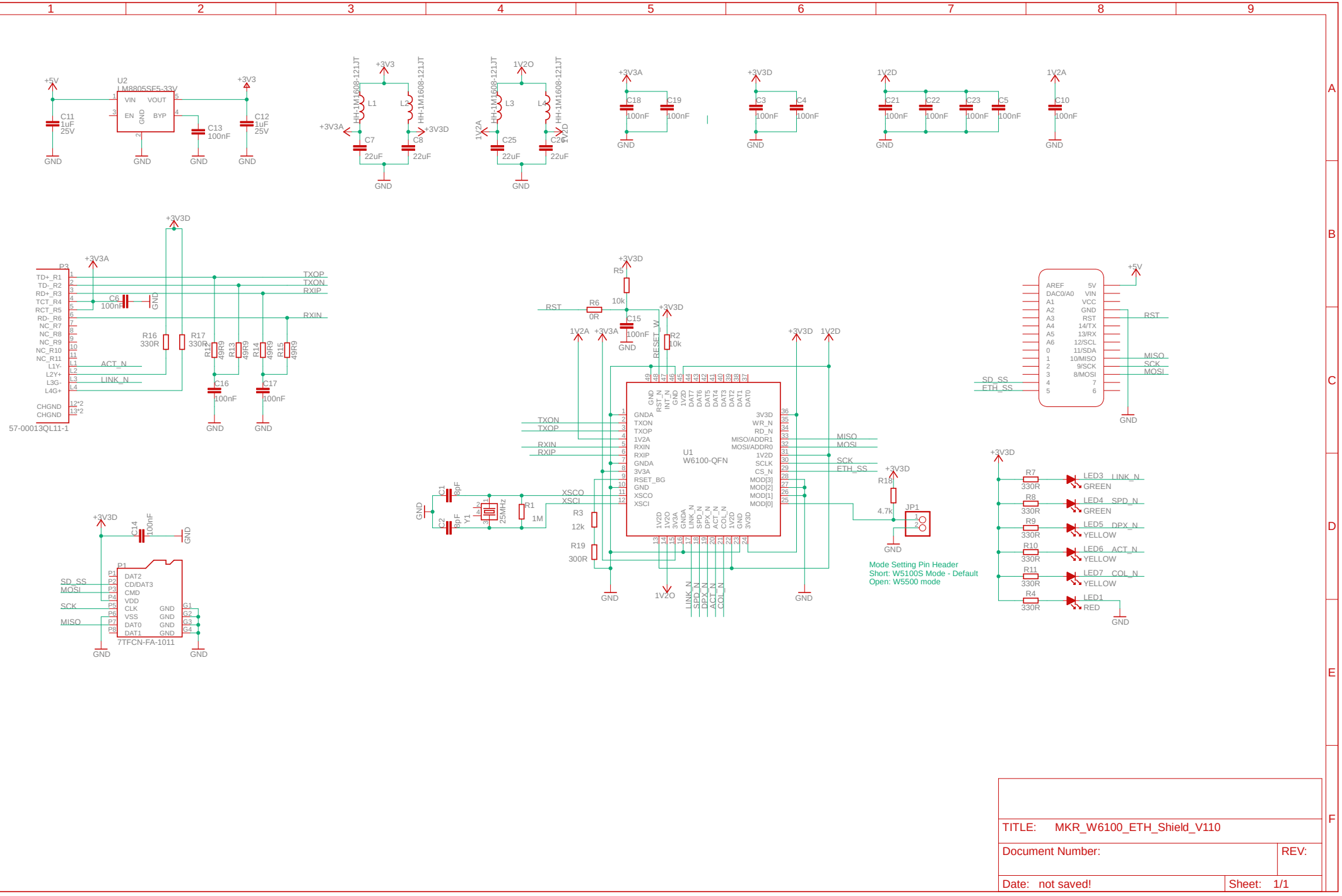
U2
LM8805SF5-33V
VIN
VOUT
EN
GND
BYP
C11 1uF 25V
C13 100nF
C12 1uF 25V
HH-1M1608-121JT
L1
L2
L3
L4
C7 22uF
C8 22uF
C25 22uF
C24 22uF
C18 100nF
C19 100nF
C3 100nF
C4 100nF
C21 100nF
C22 100nF
C23 100nF
C5 100nF
C10 100nF
P3
57-00013QL11-1
TD+_R1
TD-_R2
RD+_R3
TCT_R4
RCT_R5
RD-_R6
NC_R7
NC_R8
NC_R9
NC_R10
NC_R11
L1Y-
L2Y+
L3G-
L4G+
CHGND
TXOP
TXON
RXIP
RXIN
ACT_N
LINK_N
C6 100nF
R16 330R
R17 330R
R12 49R9
R13 49R9
R14 49R9
R15 49R9
C16 100nF
C17 100nF
P1
7TFCN-FA-1011
DAT2
CD/DAT3
CMD
VDD
CLK
VSS
DAT0
DAT1
SD_SS
MOSI
SCK
MISO
C14 100nF
R5 10k
R6 0R
RST
C15 100nF
R2 10k
RESET_W
U1
W6100-QFN
GNDA
TXON
TXOP
1V2A
RXIN
RXIP
GNDA
3V3A
RSET_BG
GND
XSCO
XSCI
3V3D
WR_N
RD_N
MISO/ADDR1
MOSI/ADDR0
1V2D
SCLK
CS_N
MOD[3]
MOD[2]
MOD[1]
MOD[0]
LINK_N
SPD_N
DPX_N
ACT_N
COL_N
C1 8pF
C2 8pF
Y1 25MHz
R1 1M
R3 12k
R19 300R
R18 4.7k
JP1
Mode Setting Pin Header
Short: W5100S Mode - Default
Open: W5500 mode
AREF
DAC0/A0
A1
A2
A3
A4
A5
A6
0
1
2
3
4
5
5V
VIN
VCC
GND
RST
14/TX
13/RX
12/SCL
11/SDA
10/MISO
9/SCK
8/MOSI
7
6
R7 330R
R8 330R
R9 330R
R10 330R
R11 330R
R4 330R
LED3 LINK_N GREEN
LED4 SPD_N GREEN
LED5 DPX_N YELLOW
LED6 ACT_N YELLOW
LED7 COL_N YELLOW
LED1 RED
TITLE: MKR_W6100_ETH_Shield_V110
Document Number:
REV:
Date: not saved!
Sheet: 1/1