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Experiment NO :- 1

Aim:- Assignment exploiting the various addressing modes of 8051 for accessing internal as well as external memory.

Part 1 :-

Addition of internal RAM with carry:

Theory :-

Internal RAM in 8051 microcontroller: There are 128 byter of RAM inside the 8051 are assigned addresses 00 to 7FH. As we will see in chapter they came be accessed directly as memory locations. These 128 bytes are divided into three different groups as follows:

1) A total of 32 byter from locations 00 to 1FH are set aside for register banks & the stack.
2) A total of 16 byter from locations 30H to 7FH are wed & write storage. or what is normally called a scratch pad. These 80 locations of RAM are widely wed for the purpose of storing data & parameters by 8051 programmers.

Instructions:

1) **INC :-** Jump if no carry jumps if (CY=0)
In this instruction carry flag bit in flag PSW Register is used to make decision wheales to jump.
- In execution "JNC label" processor looks at carry flag to see if it raised (CY=1) If is it not. Cpu starts fetching & executing instruction from address of label.
- If (CY=1) it will not jump but will execute next instruction below JNC.

2) DJNZ :- (Decrement Register & Jump if not zero)
- DJNZ decrements the value of Register by 1. If the initial value of Register is 0, decrementing the value will cause it to reset to 255. If the new value of register is not a the program will branch to the address indicated by relative address.

3) INC : (Increment)
- INC increments the value of Register by 1
- If the initial value of Register If 255, incrementing the value will cause it to reset to 0.
- The carry flag is not set when the value rolls over from 255 to 0.

4) ADDC :-
- The ADDC instruction adds a byte value & the value of the carry flag to the accumulator
- The results of the addition are stored back in the accumulator.

For eg :- ADDC A, R0 - (Add contains of Accumulator & R0 register & carry flag put the result in accumulator

- PSW (program status word) Register :-

CY	AC	F0	RS1	RS0	OV	-	P

1) CY PSW.7 - Carry flag.
2) AC PSW.6 - Auxillary carry flag.
3) F0 PSW.5 - Available to the user for general purpose.

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4) RS1 PSW.4 - Register Bank selector Bit 1.
5) RS0 PSW.3 - Register Bank selector Bit 0
6) OV PSW.2 - Overflow flag.
7) - PSW.1 - User difinable bit.
8) P PSW.0 - parity flag.

- Internal RAM addition:
- Algorithm:
1) load value 40H in R0
2) load counter.
3) Clear Accumulator.
4) clear R7.
5) Add the byte pointer to A by R0
6) If cy=0 don't accumulator carry.
7) Increment R7.
8) Increment R0 (Pointer).
9) Repeat until R0 is zero

- program:

```
        MOV R0, #40H
        MOV R2, #5
        CLR A
        MOV R7, A
Again:  ADD A, @R0
        INC Next.
        INC R7
Next:   INC R0
        DJNZ R7, Again
        END.
```

Part 2:

a) External RAM addition:

- program:

```
        ORG  0000H
        MOV  DPTR, #200H.
        MOV  A, #00H
        MOV  R7, #00H
        MOV  R6, #08H.
Back:   MOVX A, @dptr.
        ADD  a, R7
        MOV  R7, a
        inc  dptr
        djnz R6, Back
        END.
```

b) External RAM Addition with carry.

- program:

```
        ORG  0000H.
        MOV DPTR, #200H
        MOV  A, #00H
        MOV R2, #00H
        MOV R7, #00H
        MOV R6, #08H
Back:   MOVX A, @DPTR.
        ADD  A, R7
        MOV  R7, A
        JNC  Next.
        INC  R2
Next:   INC DPTR
        DJNZ R6, BACK
        END.
```

3) F0 PSW =

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Part3:
Internal to External Block transfer:

- program:

```
        ORG   0000H.
        SJMP  MAIN
MAIN:   MOV R0, #40H
        MOV DPTR, #6000H
        MOV   R2, #05H
BACK :  MOV , @@R0
        MOV A, B.
        MOVX  @.DPTR, A
        INC   R0
        INC   DPTR
        DINZ  R2, BACK
        SJMP  $
        END.
```

Conclusion:
In this experiment we conclude that addressing mode of 8051 for accessing Internal as well as external memory addition & transfer Data internal to external.

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```
  5:                    SJMP MAIN
0x0000    8000        SJMP      MAIN(C:0002)
  6: MAIN:    MOV R0,#40H
0x0002    7840        MOV       R0,#0x40
  7:          MOV DPTR, #6000H
0x0004    906000      MOV       DPTR,#0x6000
  8:                    MOV R2,#05H
0x0007    7A05        MOV       R2,#0x05
  9: BACK:    MOV A,@R0
0x0009    E6          MOV       A,@R0
 10:          MOV A,B
0x000A    E5F0        MOV       A,B(0xF0)
 11:                    MOVX @DPTR,A
0x000C    F0          MOVX      @DPTR,A
 12:                    INC R0
0x000D    08          INC       R0
 13:                    INC DPTR
```

port2_add.asm | port1_add.asm | port3_int.asm*

```
        ORG 0000H
        SJMP MAIN
MAIN:   MOV R0,#40H
        MOV DPTR, #6000H
        MOV R2,#05H
BACK:   MOV B,@R0
        MOV A,B
        MOVX @DPTR,A
        INC R0
        INC DPTR
        DJNZ R2,BACK
        SJMP $
        END
```

```
ess: 0:0X40
40:0: 34 D3 F7 E8 23 E4 E3 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B8:8: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x24: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0xBC: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x08: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x54: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0xA0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0xEC: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

ramtransfer.asm*

```
01      ORG 0000H
02      MOV A,#00H
03      MOV R0,#30H
04      MOV R7 ,#05H
05      MOV DPTR ,#2000H
06      BACK:MOV B,@R0
07      MOV A,B
08      MOVX @DPTR,A
09      INC DPTR
10      INC R0
11      DJNZ R7,BAC
12      END
13
```

Memory 2

Address: X:200H

```
X:0x000200: 01 12 12 10 10
X:0x000205: 00 00 00 00 00
X:0x00020A: 00 00 00 00 00
X:0x00020F: 00 00 00 00 00
X:0x000214: 00 00 00 00 00
X:0x000219: 00 00 00 00 00
X:0x00021E: 00 00 00 00 00
X:0x000223: 00 00 00 00 00
X:0x000228: 00 00 00 00 00
X:0x00022D: 00 00 00 00 00
X:0x000232: 00 00 00 00 00
X:0x000237: 00 00 00 00 00
X:0x00023C: 00 00 00 00 00
```

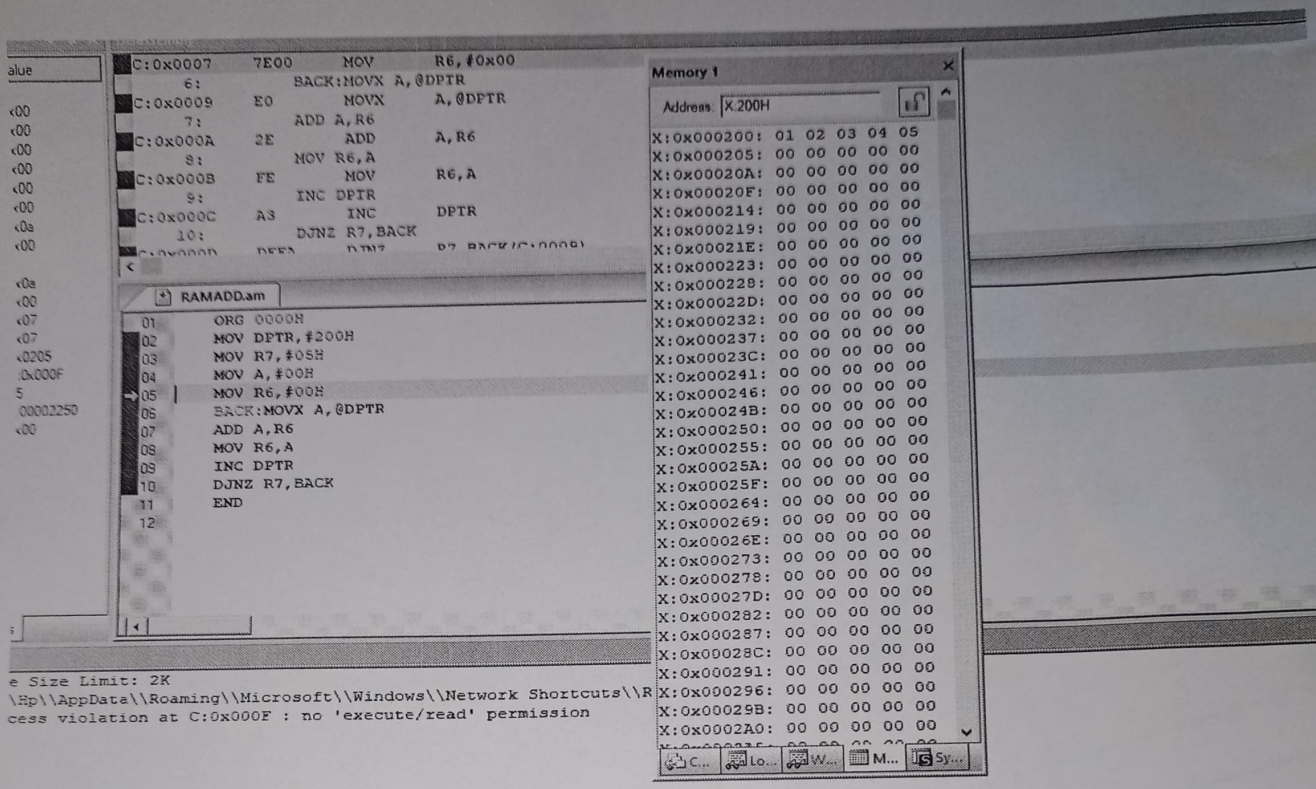
Memory 1

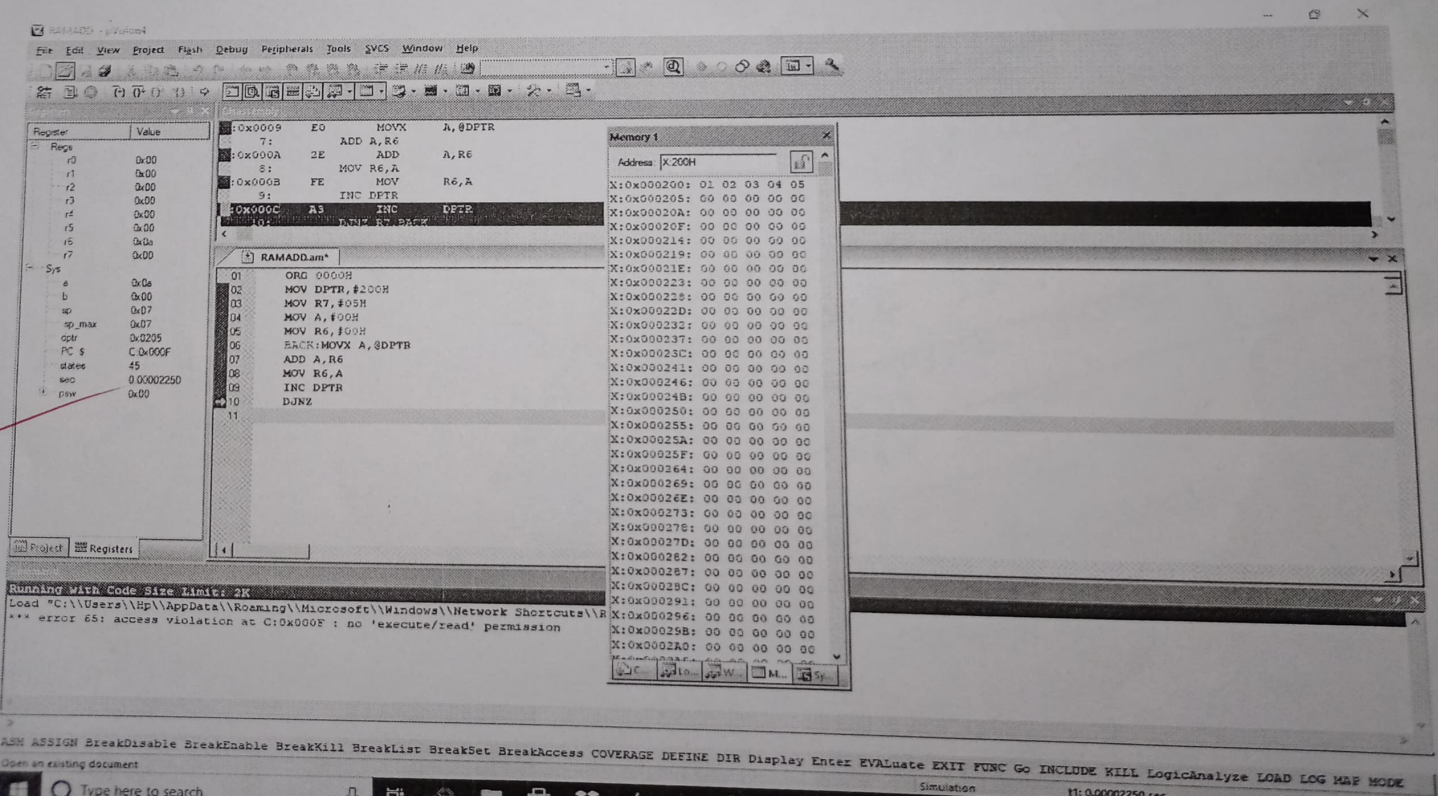
Address: X:200H

```
X:0x000200: 01 12 12 10 10 00 00 00 00 00 00 00 00 (
X:0x000219: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
X:0x000232: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
X:0x00024B: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
X:0x000264: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
X:0x00027D: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
X:0x000296: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
X:0x0002AF: 00 00 00 00 00 00 00 00 00 00 00 00 00 (
```

Call Stack | Locals | Watch 1 | Memory 1 | Symbols

Simulation t1: 0.(


C:0x0007 7E00 MOV R6,#0x00
6: BACK:MOVX A,@DPTR
C:0x0009 E0 MOVX A,@DPTR
7: ADD A,R6
C:0x000A 2E ADD A,R6
8: MOV R6,A
C:0x000B FE MOV R6,A
9: INC DPTR
C:0x000C A3 INC DPTR
10: DJNZ R7,BACK
RAMADD.am
01 ORG 0000H
02 MOV DPTR,#200H
03 MOV R7,#05H
04 MOV A,#00H
05 MOV R6,#00H
06 BACK:MOVX A,@DPTR
07 ADD A,R6
08 MOV R6,A
09 INC DPTR
10 DJNZ R7,BACK
11 END
12
Memory 1
Address: X:200H
X:0x000200: 01 02 03 04 05
X:0x000205: 00 00 00 00 00
X:0x00020A: 00 00 00 00 00
X:0x00020F: 00 00 00 00 00
X:0x000214: 00 00 00 00 00
X:0x000219: 00 00 00 00 00
X:0x00021E: 00 00 00 00 00
X:0x000223: 00 00 00 00 00
X:0x000228: 00 00 00 00 00
X:0x00022D: 00 00 00 00 00
X:0x000232: 00 00 00 00 00
X:0x000237: 00 00 00 00 00
X:0x00023C: 00 00 00 00 00
X:0x000241: 00 00 00 00 00
X:0x000246: 00 00 00 00 00
X:0x00024B: 00 00 00 00 00
X:0x000250: 00 00 00 00 00
X:0x000255: 00 00 00 00 00
X:0x00025A: 00 00 00 00 00
X:0x00025F: 00 00 00 00 00
X:0x000264: 00 00 00 00 00
X:0x000269: 00 00 00 00 00
X:0x00026E: 00 00 00 00 00
X:0x000273: 00 00 00 00 00
X:0x000278: 00 00 00 00 00
X:0x00027D: 00 00 00 00 00
X:0x000282: 00 00 00 00 00
X:0x000287: 00 00 00 00 00
X:0x00028C: 00 00 00 00 00
X:0x000291: 00 00 00 00 00
X:0x000296: 00 00 00 00 00
X:0x00029B: 00 00 00 00 00
X:0x0002A0: 00 00 00 00 00
e Size Limit: 2K
\Hp\\AppData\\Roaming\\Microsoft\\Windows\\Network Shortcuts\\R
cess violation at C:0x000F : no 'execute/read' permission


File Edit View Project Flash Debug Peripherals Tools SVCS Window Help
RAMADD.am*
01 ORG 0000H
02 MOV DPTR,#200H
03 MOV R7,#05H
04 MOV A,#00H
05 MOV R6,#00H
06 BACK:MOVX A,@DPTR
07 ADD A,R6
08 MOV R6,A
09 INC DPTR
10 DJNZ
11
Memory 1
Address: X:200H
Running with Code Size Limit: 2K
Load "C:\\Users\\Hp\\AppData\\Roaming\\Microsoft\\Windows\\Network Shortcuts\\R
*** error 65: access violation at C:0x000F : no 'execute/read' permission
ASM ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE DEFINE DIR Display Enter EVALuate EXIT FUNC Go INCLUDE KILL LogicAnalyze LOAD LOG MAP MODE
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Simulation
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