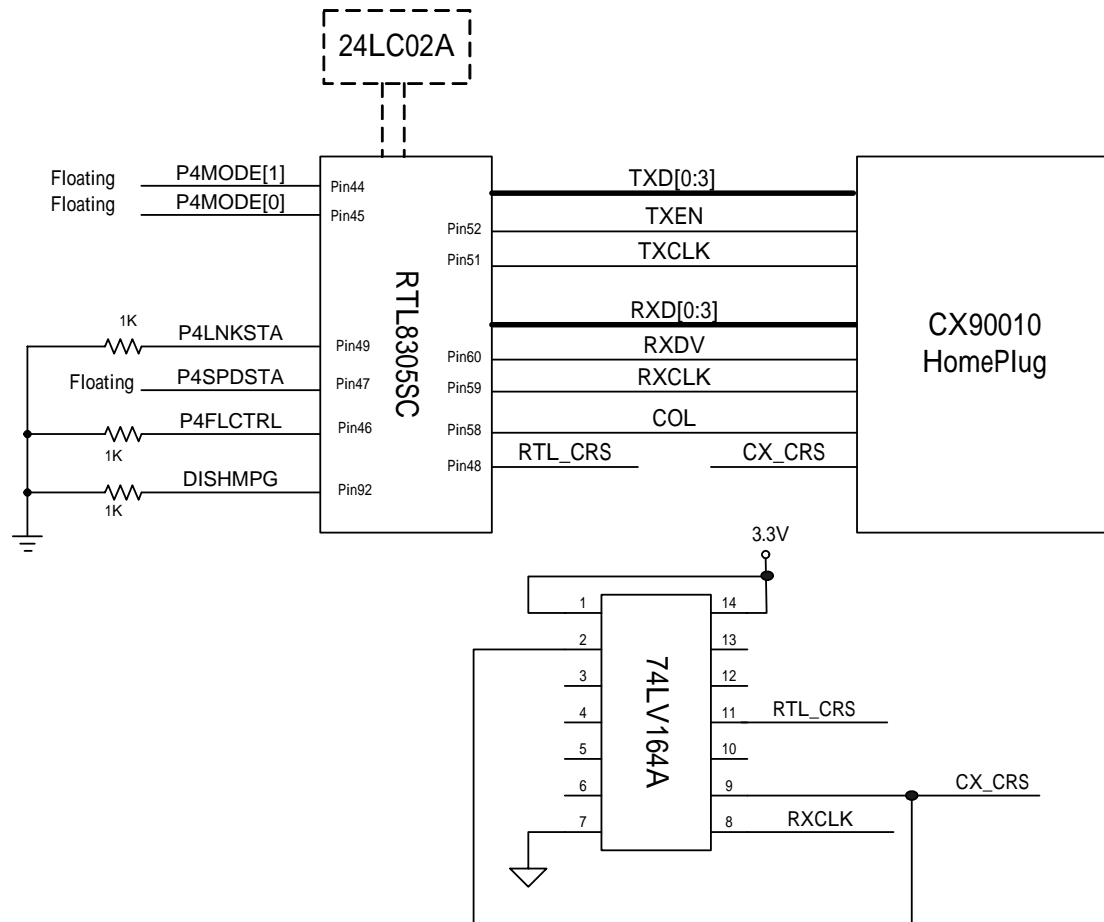# REALTEK

RTL8305SC Ver.C & HomePlug CX90010 Notice

Rev: 1.1
Date: 2005.07.28
Author: Davis Lin

1. The EEPROM 24LC02A could be omitted for the HomePlug CX90010 application.
2. The P4FLCTL (Pin-46) should be tied to ground via an1K resister.
3. The P4DUPSTA (Pin-48) should be connected to shift-register **74LV164A** Pin-11 as figure 1.
4. The P4SPDSTA(Pin-47) should be floating to force the MII interface operated in 100Mbps mode.
5. The P4MODE[1:0] (Pin-44, Pin-45) should be floating for operated in MII MAC mode.
6. The DISHMPG(Pin-92) should be tied to ground via 1K resister for HomePlug application.

Floating   P4MODE[1]   Pin44
Floating   P4MODE[0]   Pin45

RTL8305SC

TXD[0:3]
TXEN   Pin52
TXCLK   Pin51

RXD[0:3]
RXDV   Pin60
RXCLK   Pin59
COL   Pin58
RTL_CRS   Pin48   CX_CRS

CX90010
HomePlug

1K   P4LNKSTA   Pin49
Floating   P4SPDSTA   Pin47
P4FLCTRL   Pin46
1K
DISHMPG   Pin92
1K

3.3V

1   14
2   13
3   12
4   74LV164A   11   RTL_CRS
5   10
6   9   CX_CRS
7   8   RXCLK

RTL8305SC Ver.C & CX90010 Workaround solution

Figure.1