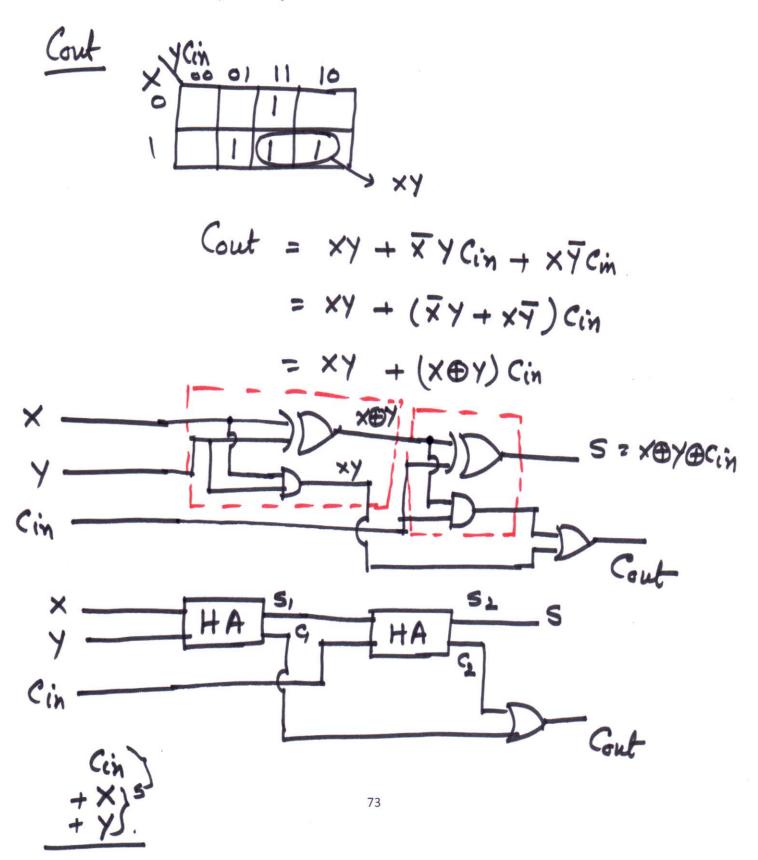
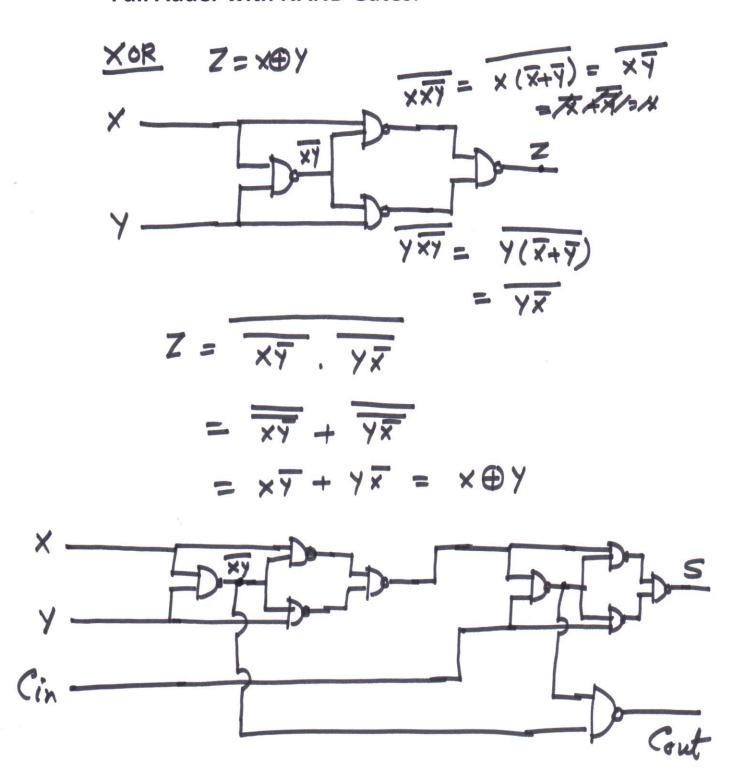
2. Full Adder

	Inputs			Output	
X	Υ	C _{in}	Cout	S	
0	0	0	0	0	
0	0	1	0	1	
0	1	0	6	1	
0	1	1	1	0	
1	0	0	0	1	
1	0	1	1	0	
1	1	0	1	0	
1	1	1	1	1	

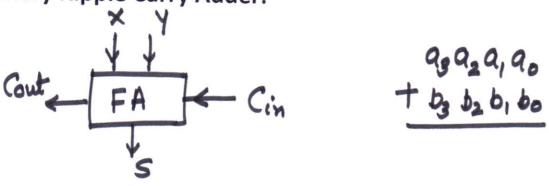
Full Adder (Cont.)

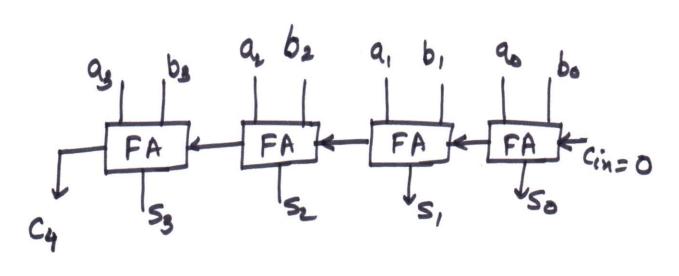


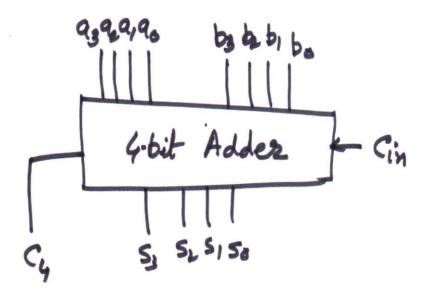
Full Adder with NAND Gates:



3. Binary Ripple Carry Adder:







Binary Subtractor:

1. Half Subtractor

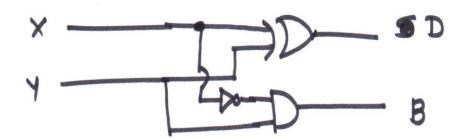
Inp	Inputs		Outputs		
X	Υ	В	D		
0	0	0	0		
0	1	1	1		
1	0	0	1		
1	1	0	0		

D: Difference (X-Y)

B: Borrow

$$\mathcal{D} = \times \oplus \mathcal{Y}$$

$$\mathcal{B} = \overline{\times} \mathcal{Y}$$



4. Full Subtractor

	Inputs			Output	
X	Υ	Bin	B _{out}	D	
0	0	0	0	0	
0	0	1	1	1	
0	1	0	1	1	
0	1	1	1	0	
1	0	0	0	1	
1	0	1	0	0	
1	1	0	0	0	
1	1	1	1	1	

Adder-Subtractor:

Subtraction of two numbers can be accomplished by adding 2's complement of the subtrahend to the minuend and disregarding the final carry, if any.

Controlled inverter-

