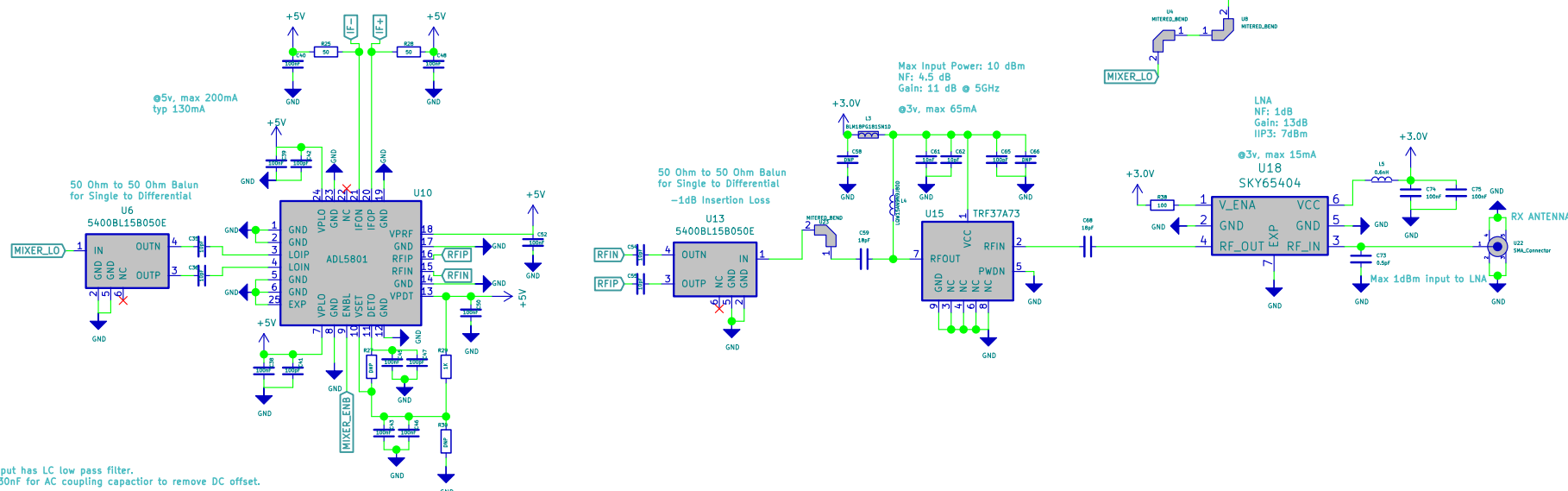


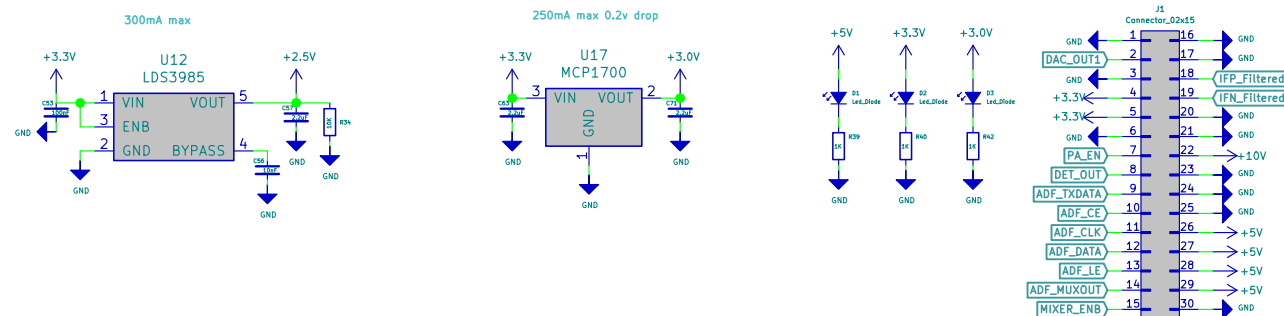
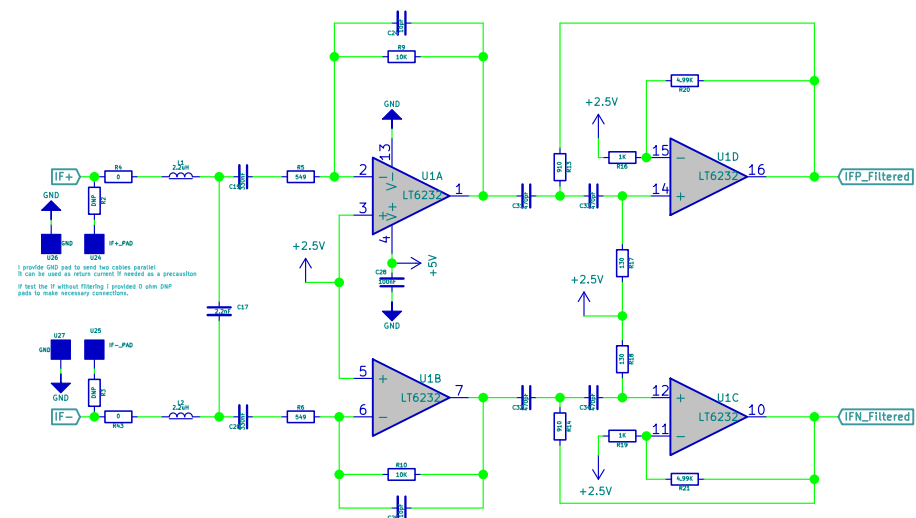
ADF4158 is Freq. Synthesizer to form a PLL topology.
It requires a reference clock and an external VCO. It is not a PLL itself.
It is used to drive VCO in a way the user wants.
So it takes ref clock and VCO output and generates voltage
through charge pump to generate the waveform wanted.(sawtooth etc.)



Input has LC low pass filter.
330nF for AC coupling capacitor to remove DC offset.

1st stage opamp is amplifier.
2nd stage opamp is salien key high pass filter

This filter both filters high freq. and makes range compensation for
objects at different distances.



Genk Keskin

Sheet: /
File: Radar RF.sch

Title: 6GHz Radar

Size: A3 Date: 2019-09-14
KiCad E.D.A. kicad (5.1.4-0-10_14)

Rev: 1.0
Id: 1/1