



SCHOOL OF COMPUTER SCIENCE

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[User:a1779153 - Student - Vandit Jyotindra Gajjar][Sun 03 May 2020 07:42:07pm Australia/Adelaide]

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Assignment Repository to be Submitted:

<https://version-control.adelaide.edu.au/svn/a1779153/2020/s1/adsa/assignment2>

Choose a submission from the "Submission" column to view the test script output and any associated marks. The selected "Submission" item is highlighted in **red**. If available, "Helpful Hints" or "Feedback" can be chosen too. All the items in the "Submission" column are listed with the most recent first. If the assignment deadline is known to the Web Submission system, this will be indicated in the Submissions column too.

Submission	Feedback																							
<p>-----Due Date-----</p> <p>May 03 19:35 r391(100)</p> <p>May 03 19:00 r390(100)</p> <p>May 03 18:55 r389(100)</p> <p>May 03 14:54 r388(100)</p> <p>May 03 14:53 r387(35)</p> <p>May 03 14:42 r386(100)</p> <p>May 03 14:35 r385(100)</p> <p>May 03 14:28 r383(5)</p> <p>May 03 14:26 r382(5)</p> <p>May 03 14:26 r381(0)</p> <p>May 03 14:24 r380(0)</p> <p>May 03 14:19 r379(100)</p> <p>May 03 14:16 r378(0)</p> <p>May 03 14:11 r377(100)</p> <p>May 03 14:07 r376(0)</p> <p>May 03 14:05 r375(100)</p> <p>May 03 13:57 r374(100)</p> <p>May 03 13:54 r373(100)</p> <p>May 03 13:52 r372(0)</p> <p>May 02 13:13 r371(100)</p> <p>May 02 13:10 r370(100)</p> <p>May 02 13:07 r369(100)</p> <p>May 02 12:45 r368(100)</p> <p>May 02 12:33 r367(100)</p>	<p>Due Date: May 03 23:59</p> <p>Final Assignment Mark: 100</p> <p>View Feedback in the Practical Marker</p> <p>Assignment Repository Submitted: https://version-control.adelaide.edu.au/svn/a1779153/2020/s1/adsa/assignment2/?p=391</p> <p>Submitted: May 03 19:35.48</p> <p>Status: Finished execution</p> <hr/> <p>Marks at: May 03 19:35.48</p> <table><tr><th>Part</th><th>Marks</th><th>Maximum</th></tr><tr><td>case 1 correct</td><td>1</td><td></td></tr><tr><td>case 10 correct</td><td>1</td><td></td></tr><tr><td>case 100 correct</td><td>1</td><td></td></tr><tr><td>case 11 correct</td><td>1</td><td></td></tr><tr><td>case 12 correct</td><td>1</td><td></td></tr><tr><td></td><td></td><td></td></tr></table>			Part	Marks	Maximum	case 1 correct	1		case 10 correct	1		case 100 correct	1		case 11 correct	1		case 12 correct	1				
Part	Marks	Maximum																						
case 1 correct	1																							
case 10 correct	1																							
case 100 correct	1																							
case 11 correct	1																							
case 12 correct	1																							

May 02 12:32 r366(0)
 May 02 12:28 r365(100)
 May 02 12:24 r364(0)
 May 02 12:21 r363(3)
 May 02 12:18 r362(3)
 May 02 12:13 r361(3)
 May 02 12:12 r360(0)
 May 02 12:09 r359(0)
 May 02 12:04 r358(3)
 May 02 12:02 r357(0)
 Apr 15 00:54 r311(100)

case 13 correct

1

case 14 correct

1

case 15 correct

1

case 16 correct

1

case 17 correct

1

case 18 correct

1

case 19 correct

1

case 2 correct

1

case 20 correct

1

case 21 correct

1

case 22 correct

1

case 23 correct

1

case 24 correct

1

case 25 correct

1

case 26 correct

1

case 27 correct

1

case 28 correct

1

case 29 correct

1

case 3 correct

1

case 30 correct

1

case 31 correct

1

case 32 correct

1

case 33 correct

1

case 34 correct

1

case 35 correct

1

case 36 correct

1

case 37 correct

1

case 38 correct

1

case 39 correct

1

case 4 correct

1

case 40 correct

1

case 41 correct

1

case 42 correct

1

case 43 correct

1

case 44 correct

1

case 45 correct

1

case 46 correct

1

case 47 correct

1

case 48 correct

1

case 49 correct

1

case 5 correct

1

case 50 correct

1

case 51 correct

1

case 52 correct

1

case 53 correct

1

case 54 correct

1

case 55 correct

1

case 56 correct

1

case 57 correct

1

case 58 correct

1

case 59 correct

1

case 6 correct

1

case 60 correct

1

case 61 correct

1

case 62 correct

1

case 63 correct

1

case 64 correct

1

case 65 correct

1

case 66 correct

1

case 67 correct

1

case 68 correct

1

case 69 correct

1

case 7 correct

1

case 70 correct

1

case 71 correct

1

case 72 correct

1

case 73 correct

1

case 74 correct

1

case 75 correct

1

case 76 correct

1

case 77 correct

1

case 78 correct

1

case 79 correct

1

case 8 correct

1

case 80 correct

1

case 81 correct

1

case 82 correct

1

case 83 correct

1

case 84 correct

1

case 85 correct

1

case 86 correct

1

case 87 correct	1	
case 88 correct	1	
case 89 correct	1	
case 9 correct	1	
case 90 correct	1	
case 91 correct	1	
case 92 correct	1	
case 93 correct	1	
case 94 correct	1	
case 95 correct	1	
case 96 correct	1	
case 97 correct	1	
case 98 correct	1	
case 99 correct	1	
Total Marks	100	100

Output at: May 03 19:35.48

```
$ websubmit-automark -g 4 -n a1779153 -x a1779153 -a 2020/s1/adsa/assignment2@391 -r 391 -f /users/as:
*****
Web Submission Script for Assignment Key: 2020/s1/adsa/assignment2 User: a1779153 SVN Exported revis:
*****
Submitted Files:

.:
total 12
-rw-rw---- 1 legend teaching 9648 May  3 19:35 main.cpp
*****
Output From Web Submission Script:
main.cpp: In function 'int main()':
main.cpp:246:32: warning: comparison between signed and unsigned integer expressions [-Wsign-compare]
    while (i < vectorData.size()) {
                          ^
main.cpp:248:27: warning: comparison between signed and unsigned integer expressions [-Wsign-compare]
    if(i==vectorData.size()-1) {
                          ^
For input  D1 PRE
Output received  EMPTY
```

Output shouldbe EMPTY
correct

For input A1 D2 PRE
Output received 1

Output shouldbe 1
correct

For input A1 D1 POST
Output received EMPTY

Output shouldbe EMPTY
correct

For input A1 A2 A3 A5 A6 D3 D2 D5 D1 D6 IN
Output received EMPTY

Output shouldbe EMPTY
correct

For input D13 D60 D76 D12 A17 D98 A94 D70 D3 A23 A42 D45 A100 D50 A99 A22 A87 A4 A90 D88 A71 A20 D39
Output received

4

6

9

12

13

15

17

19

20

22

23

24

25

27

35

39

40

42

44

45

48

50

58

60

63

71

72

73

75

81
82
87
90
91
97
98

Output shouldbe 4 6 9 12 13 15 17 19 20 22 23 24 25 27 35 39 40 42 44 45 48 50 58 60 63 71 72 73 75 8
correct

For input A88 D77 D95 A78 A71 A2 D9 A2 A60 D80 A85 A65 D11 A30 D8 A68 D87 A28 A88 A96 D29 D26 D88 D47
Output received

3

5 4

15 11 9

22 17

28 25 16

34

40

46

55 47

63 62 61 45 30

71 68

85 74

91

98

100 96 86 65

Output shouldbe 3 5 4 15 11 9 22 17 28 25 16 34 40 46 55 47 63 62 61 45 30 71 68 85 74 91 98 100 96 8
correct

For input A13 D10 D48 D14 D4 D75 A51 D36 A72 D55 D14 D30 D100 A90 D53 D86 D96 A89 D11 A72 A55 A43 A28
Output received

1

6

8

13

```
16
18
28
30
38
40
41
42
43
46
49
51
55
59
63
66
67
68
69
72
74
75
77
83
84
86
89
90
94
```

```
Output shouldbe 1 6 8 13 16 18 28 30 38 40 41 42 43 46 49 51 55 59 63 66 67 68 69 72 74 75 77 83 84 {
correct
```

```
For input A23 D51 A75 A41 A17 A81 D88 A72 D75 A94 A34 D11 A11 A94 D17 D81 D71 A24 D91 A44 D30 D99 A69
Output received
```

```
11
15
19
25
26
27
30
31
34
36
37
38
41
43
47
49
50
55
56
```


59

63

64

69

75

76

77

79

82

85

87

90

92

93

94

98

Output shouldbe 11 15 19 25 26 27 30 31 34 36 37 38 41 43 47 49 50 55 56 59 63 64 69 75 76 77 79 82 85 86 87 90 91 92 93 94 98
correct

For input D23 D87 D47 D36 A55 D39 A4 D26 A11 A93 D38 A44 A98 D1 A98 A95 D39 A24 A64 D22 A32 D51 A9 A64
Output received

5

8

9

11

14

15

17

24

26

28

30

32

35

44

50

51

53

57

59

63

64

66

68

74

77

81

86

91

93

98

Output shouldbe 5 8 9 11 14 15 17 24 26 28 30 32 35 44 50 51 53 57 59 63 64 66 68 74 77 81 86 91 93 98

correct

For input A23 A59 D51 A32 A28 A40 A59 A6 A82 D91 A36 D84 A95 D57 A99 A32 D38 D29 D45 D45 A58 A71 A55
Output received

1

5 3

12

13 6

21

28

31 29 27

34

39 36 32 20

44

50 45

53 52

56

58 55

61

64 63

68

80 71 67

83

88 87

99 95 90 82 59 40

Output shouldbe 1 5 3 12 13 6 21 28 31 29 27 34 39 36 32 20 44 50 45 53 52 56 58 55 61 64 63 68 80 71
correct

For input D93 A60 D27 D14 A91 A85 D43 D81 D12 A38 D24 D92 A6 A53 D78 A35 D24 A85 A89 A55 D54 A26 A94
Output received

3

9

12

14

15
21
22
23
25
26
32
35
38
40
45
46
50
52
53
55
58
59
60
63
68
70
76
84
85
88
89
90
91
92
93
98

Output shouldbe 3 9 12 14 15 21 22 23 25 26 32 35 38 40 45 46 50 52 53 55 58 59 60 63 68 70 76 84 85
correct

For input D79 A28 D52 D84 A39 A85 D20 A19 D49 A86 D96 A73 A11 A100 D83 D19 A93 D10 D40 A36 D33 A19 D6
Output received

3
5
11
14
16
17
18
19
24
30
32
36
38
40
45
50

53
56
58
59
60
72
73
76
81
84
85
89
93
95

Output shouldbe 3 5 11 14 16 17 18 19 24 30 32 36 38 40 45 50 53 56 58 59 60 72 73 76 81 84 85 89 93
correct

For input A69 D16 A31 A98 A45 A17 A50 D17 D44 A87 D100 D73 D95 D53 D75 A30 D6 A30 A46 A42 D33 D26 D78
Output received 50 30 21 12 11

20

25
29

42 31
38

45 43

46
49

81 65 58 53

75 72

90 87 82

88

97 93

100

Output shouldbe 50 30 21 12 11 20 25 29 42 31 38 45 43 46 49 81 65 58 53 75 72 90 87 82 88 97 93 100
correct

For input A60 D39 A27 A5 A44 A17 A75 A69 A63 A50 D79 A92 D59 D72 D6 D17 D57 D58 D67 A67 A13 A61 D7 D8
Output received 60 32 26 10 5

6

17

21

27

31

47 44

54 50

55

70 67 63 61

69

88 75 72

73

77

92 91

95 94

96

Output shouldbe 60 32 26 10 5 6 17 21 27 31 47 44 54 50 55 70 67 63 61 69 88 75 72 73 77 92 91 95 94
correct

For input A99 D41 D35 A96 D40 A3 D60 D12 D67 D89 A21 A32 D58 D85 D47 A26 D32 D29 D79 D14 A44 D65 D79
Output received 42 26 14 6 3

13

18

30 28

29

36 35

39

79 62 57 49 44

52

59

70 65 64

72

74

85 83 81

96 90

92

100

Output shouldbe 42 26 14 6 3 13 18 30 28 29 36 35 39 79 62 57 49 44 52 59 70 65 64 72 74 85 83 81 96
correct

For input A11 D80 A43 A92 A11 D25 A76 A21 D10 A100 D32 A21 D80 D85 D27 A92 A50 D78 A33 A86 A51 D24 A51
Output received

1

2

12

20

24

25

26

30

31

32

37

38

41

42

44

45

48

50

51

53

55

61

64

70

81

84

85

86

87

89

92

97

Output shouldbe 1 2 12 20 24 25 26 30 31 32 37 38 41 42 44 45 48 50 51 53 55 61 64 70 81 84 85 86 87

correct

For input D54 A20 A17 D36 D72 A94 A70 A45 D4 D45 A95 D79 D78 A2 A12 D60 A56 D9 D95 A59 D16 A14 A62 A4
Output received

1
2
4
10
15
19
20
34
35
36
40
42
45
49
56
57
62
63
67
70
72
80
81
91
92
93
94
95

Output shouldbe 1 2 4 10 15 19 20 34 35 36 40 42 45 49 56 57 62 63 67 70 72 80 81 91 92 93 94 95
correct

For input D93 D92 A19 A28 D34 A11 D75 D3 A15 A70 A79 D48 A99 A5 A84 A25 A55 D15 D26 A57 D15 D3 D69 A1
Output received 28 18 12 11 5

16
17

21 19

27

70 53 42 37 34

39

51

57 54

61 58

84 79 73 72

77

82

83

95 93 88

94

99

Output shouldbe 28 18 12 11 5 16 17 21 19 27 70 53 42 37 34 39 51 57 54 61 58 84 79 73 72 77 82 83 95
correct

For input A41 A55 D25 D70 A76 D37 D80 D15 D73 A86 D17 D2 A3 A51 D24 D88 D32 A26 D6 D84 D90 A31 A40 D4
Output received 41 28 16 15 5

26 18 17

19

27

37 33 31

35

40 39

76 55 51 50

52

54

66 63

69

70

99 90 85

93

100


```
Output shouldbe 41 28 16 15 5 26 18 17 19 27 37 33 31 35 40 39 76 55 51 50 52 54 66 63 69 70 99 90 81
correct

For input  D29 D7 A43 D28 D90 A82 A68 D56 D46 A58 D26 D34 A46 A56 A84 A55 D34 D98 D33 D45 D35 A52 A31
Output received 56 31 9 5 4

7

24 13
22

28 25

29

46 40 34
36

43

52
55

81 68 58 57

64 62

77 72

79

95 84 83

87

98 96

100

Output shouldbe 56 31 9 5 4 7 24 13 22 28 25 29 46 40 34 36 43 52 55 81 68 58 57 64 62 77 72 79 95 81
correct

For input  D21 A82 D36 A90 D13 D100 D83 D82 A83 A88 A79 D59 A88 A92 A14 A2 A92 D50 A66 D15 D54 D16 A31
Output received 76 44 20 12 4

9

16

40 31

43
```

55 47 46

49

68 56

69

90 83 81 79

82

86

88

97 93

99

Output shouldbe 76 44 20 12 4 9 16 40 31 43 55 47 46 49 68 56 69 90 83 81 79 82 86 88 97 93 99
correct

For input A3 A22 D86 A67 A86 D66 D6 D51 D44 A63 D11 A75 A76 D89 D99 A36 A62 D90 D47 D90 A71 A7 D30 D6
Output received

8 3

12 10

31 20

41

45 43 38 17

57 47

61 60 59

63

65

68 67 62

80

85

95

```

100 99 86 71 46
Output shouldbe  8 3 12 10 31 20 41 45 43 38 17 57 47 61 60 59 63 65 68 67 62 80 85 95 100 99 86 71 46
correct

For input  A41 A66 D95 A2 A100 A35 D2 D31 A39 A68 A2 A20 D18 D67 D89 D56 D86 A63 A18 D39 A95 A64 A97 A
Output received

1

15 8 2

22 20

29
35 25 18

46

55

57 56 47

60

64 63 58 41

73 71

78

81 79 74

88

93 90 89

98
99 97 82 66
Output shouldbe  1 15 8 2 22 20 29 35 25 18 46 55 57 56 47 60 64 63 58 41 73 71 78 81 79 74 88 93 90 89
correct

For input  A1 D21 A18 D1 D18 D39 D28 A6 A90 D97 D19 A15 D88 D89 A10 A30 D17 D28 A68 D82 D2 A67 A83 D96
Output received

5
6
10
12
15
18

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21
27
28
37
42
45
46
56
59
60
65
66
67
68
70
77
78
79
82
88
90
99

Output should be 5 6 10 12 15 18 21 27 28 37 42 45 46 56 59 60 65 66 67 68 70 77 78 79 82 88 90 99
correct

For input A40 A96 D52 A43 A65 A84 A25 A36 D42 A29 A86 A2 A4 D78 D33 D82 A40 A84 D41 D94 A79 D54 A8 A1
Output received

1
2

8 3

11

17
22 13

34 26

39 36 25 9

45

47 46

62 56

77 76

81

84

89

99 96 86 79 63 43

Output shouldbe 1 2 8 3 11 17 22 13 34 26 39 36 25 9 45 47 46 62 56 77 76 81 84 89 99 96 86 79 63 43
correct

For input A63 A1 A40 A82 A67 D71 A49 A14 D31 A3 D87 A91 A52 A27 A39 A25 A97 A4 A65 A23 D13 D85 A40 A5
Output received

1

3

5

8 6

17 16 13 4

24

36

39 37 25 23

46

53

56 49

62 60 57

65

79 68 67 63

81

87 86 85

97 94 91 80 40

Output shouldbe 1 3 5 8 6 17 16 13 4 24 36 39 37 25 23 46 53 56 49 62 60 57 65 79 68 67 63 81 87 86 85
correct

For input A13 D8 D92 D87 D59 D84 D18 A95 A67 D87 A39 D100 D84 D89 D7 A2 D61 D35 D77 A75 D37 D66 A19 A
Output received 67 35 17 6 4

10

21 19

25 24

54 44 42 39

43

45

47

55

57

81 74 70 69

72

75

78

87 82

100 97

Output shouldbe 67 35 17 6 4 10 21 19 25 24 54 44 42 39 43 45 47 55 57 81 74 70 69 72 75 78 87 82 100
correct

For input D32 A55 D71 A38 D48 A58 D69 A87 D85 D13 A92 D40 A30 A84 D50 A53 A25 D20 D57 D77 D15 D74 A20
Output received

2

4

5

9

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18

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20

22

23

25

27

30

34

36

38

46

49

53

54

55
58
61
62
71
73
76
77
83
87
90
92
96
97

Output shouldbe 2 4 5 9 10 18 19 20 22 23 25 27 30 34 36 38 46 49 53 54 55 58 61 62 71 73 76 77 83 87
correct

For input A20 A97 D97 A96 A72 A34 A2 D23 A23 D13 A75 A80 A6 A51 A86 D5 A63 D82 D56 D94 A84 A80 D42 D56
Output received

2
6
7
8
19
23
28
30
31
34
39
44
46
47
51
56
57
58
62
66
69
70
72
75
78
80
81
82
84
87
89
90
91
95

```

Output shouldbe  2  6  7  8 19 23 28 30 31 34 39 44 46 47 51 56 57 58 62 66 69 70 72 75 78 80 81 82 84 87
correct

For input  D37 A30 A81 A49 D63 D78 A79 A28 D84 D59 D77 D19 A12 D63 A15 D29 A46 A2 A69 A64 D91 D45 A3 I
Output received  49 28 15 2 1

6

22 21

24

39 35 30

36

42 40

64 58 53 50

63 61

91 76 70
71

88 81

89

96 94 92

95

99

Output shouldbe  49 28 15 2 1 6 22 21 24 39 35 30 36 42 40 64 58 53 50 63 61 91 76 70 71 88 81 89 96 94
correct

For input  A59 A55 A1 D4 A26 D29 D30 D33 D86 A22 A70 A61 D79 A27 D4 A14 D38 D55 A17 D75 A64 A81 D8 D64
Output received

1
4
5
9
12
14
15
17
20

```


22
23
26
27
29
32
33
35
41
49
58
59
61
63
64
70
80
81
86
89
90
92
93
94
97
99

Output shouldbe 1 4 5 9 12 14 15 17 20 22 23 26 27 29 32 33 35 41 49 58 59 61 63 64 70 80 81 86 89 90
correct

For input D68 A21 D4 A93 D33 A25 A75 D10 A25 A40 A34 A25 A59 A33 A16 A17 A44 A31 D94 A79 D27 D94 D81
Output received

7
11
15
16
17
21
25
28
31
33
34
36
37
39
40
44
47
49
52
55
59
63

66
68
77
79
87
88
89
90
91
92
93
96
97
100

Output shouldbe 7 11 15 16 17 21 25 28 31 33 34 36 37 39 40 44 47 49 52 55 59 63 66 68 77 79 87 88 89
correct

For input A24 A81 D1 A78 D22 D80 A33 D26 A81 A65 D18 A60 D35 A51 A88 A55 D54 A52 A8 A24 A55 D68 D76 I
Output received

7

11 8

18 16

22 21 12

30

34 31

38

50 43 35 24

54

58 55

66 65

81

82

88 85

95 92 91 78 51

Output shouldbe 7 11 8 18 16 22 21 12 30 34 31 38 50 43 35 24 54 58 55 66 65 81 82 88 85 95 92 91 78
correct

```
For input  A60 D96 D78 A71 A76 D73 D5 D92 A90 A34 D80 D77 A8 A20 A31 A2 D6 A74 A4 A80 D44 A55 D35 A35
Output received
2
4
8
12
16
18
20
22
31
34
35
36
37
39
43
46
50
54
55
59
60
64
66
68
72
74
76
80
88
90
91
93

Output shouldbe  2 4 8 12 16 18 20 22 31 34 35 36 37 39 43 46 50 54 55 59 60 64 66 68 72 74 76 80 88 90 91 93
correct

For input  A27 D9 D38 A10 A26 D80 D63 A73 D90 A77 A69 A95 D49 D30 A55 D42 D51 D6 D49 D41 A71 D48 D63 A71
Output received  27 13 8 1
6

10 9

18 15

25 19

26

60 32 31 30
```

41 36 34

53

81 74 67 66

71

79

94 88

100

Output shouldbe 27 13 8 1 6 10 9 18 15 25 19 26 60 32 31 30 41 36 34 53 81 74 67 66 71 79 94 88 100
correct

For input A90 A87 A72 D57 A61 D99 D31 D91 A65 D54 A71 A36 A76 A66 A39 A93 A60 A47 D13 A80 D7 A27 D99
Output received 72 39 27 11 5 4

8

18 12

21

31 30 29

35 34

59 47 42

52 49

66 60

63

71 69

80 76 75 74

79

87 82

93 90

94

Output shouldbe 72 39 27 11 5 4 8 18 12 21 31 30 29 35 34 59 47 42 52 49 66 60 63 71 69 80 76 75 74
correct

For input A36 D42 D77 D67 A87 D67 D42 A60 D88 D69 D62 A60 A51 A76 D83 A26 A92 A65 D89 D40 A8 A14 A85
Output received 60 33 10 8

26

30

47 42 36 34

37

45

50 48

75 66 65 63

72 69

74

87 79 78

80

96 92 90

94

97

Output shouldbe 60 33 10 8 26 30 47 42 36 34 37 45 50 48 75 66 65 63 72 69 74 87 79 78 80 96 92 90 94
correct

For input D25 A89 D81 D45 A18 A78 D31 D90 D39 A83 D65 D11 D11 A28 A50 A8 A82 A46 A49 D59 A51 D51 A88
Output received

7 2

14

20

27 22 18 8

```
30
33
34 32

38

42 41

46 43 35 28

50

58 57

64

80 78 60

84

88 86

93 90

99 97 95 89 82 49
Output shouldbe 7 2 14 20 27 22 18 8 30 33 34 32 38 42 41 46 43 35 28 50 58 57 64 80 78 60 84 88 86 9
correct

For input A31 A23 A81 A100 D63 A22 A33 D4 D61 A100 A30 A50 D15 A12 D8 A74 A79 D60 A55 A32 D56 D71 A21
Output received

4

8
10 6

13

20 19 15 12

22

24
25 23 21

29

33 32

38
```

```
44
51 45 42 37
53
57 55
61
63 58
67
77 75
86
100 95 81 65 52 28
Output shouldbe 4 8 10 6 13 20 19 15 12 22 24 25 23 21 29 33 32 38 44 51 45 42 37 53 57 55 61 63 58 67 77 75 86 100 95 81 65 52 28
correct
For input A53 D23 D70 A24 D65 D77 D38 D73 A40 A99 D56 D23 A51 D3 A91 D65 A27 A31 D94 D33 D58 A55 D91
Output received 53 31 24 11 2
17
21
30 26
41 36 34
40
52
81 63 55 54
58
74 65 64
68
76
99 90 82
95
100
```

```

Output shouldbe  53 31 24 11 2 17 21 30 26 41 36 34 40 52 81 63 55 54 58 74 65 64 68 76 99 90 82 95 10
correct

For input  A12 A83 D33 D51 A26 D92 A13 A1 A10 A49 D11 A71 D69 D15 A5 A30 A32 A64 A27 A94 A78 A39 A28 A1
Output received

1
5

7

10 8 6

13
14

22 18 12

27

29 28

33
39

44 42 30 26

54 50

70 65 64

80 78

82

91 83

94
97 92 81 71 45
Output shouldbe  1 5 7 10 8 6 13 14 22 18 12 27 29 28 33 39 44 42 30 26 54 50 70 65 64 80 78 82 91 83
correct

For input  A82 D84 D49 D87 D86 A91 D77 D100 D22 D26 D21 D89 A1 A90 A51 D35 D55 D38 A80 A28 A69 D89 A1
Output received

1

12

17 16 11

```



```
26
28 27 18
32
34 33
37
43 42
47 46 35 29
52
56
65 63
70
74
80 79 73 69
83
87 84
91
92 90 82 51
Output shouldbe 1 12 17 16 11 26 28 27 18 32 34 33 37 43 42 47 46 35 29 52 56 65 63 70 74 80 79 73 69
correct
For input D33 A85 D77 A60 D64 D99 A99 A18 D41 A51 D78 D36 A2 A60 D73 D93 D100 A37 D47 A29 A98 D59 A28
Output received
1
3 2
8 7 5
21
28 27 18
31
32
49 47 37
```

54

63 59 51

72

77 75

86 85 78

90

97 96

100 99 98 87 70 29

Output shouldbe 1 3 2 8 7 5 21 28 27 18 31 32 49 47 37 54 63 59 51 72 77 75 86 85 78 90 97 96 100 99
correct

For input A14 A68 A11 A6 D93 D24 D63 D99 D96 A77 A28 D39 A66 A46 D26 A31 D93 D55 D11 D82 A95 A100 A4`
Output received 46 14 6 5

8 7

28 20 19 16

27

31 29

44 33

68 56 51 47

52

65 59

67

94 77 69

71

90 84

91

95

100

Output shouldbe 46 14 6 5 8 7 28 20 19 16 27 31 29 44 33 68 56 51 47 52 65 59 67 94 77 69 71 90 84 91
correct

For input A50 A36 A47 D59 D90 A64 A100 A44 D55 D98 D28 A27 D39 D16 A93 A65 D79 D88 A9 A39 D58 A44 A61
Output received

2
5
6
9
14
18
20
21
22
26
27
28
34
35
36
39
44
45
46
50
54
55
56
57
61
63
64
65
66
72
73
74
77
81
84
88
93
94
100

Output shouldbe 2 5 6 9 14 18 20 21 22 26 27 28 34 35 36 39 44 45 46 50 54 55 56 57 61 63 64 65 66 71
correct

For input D57 A16 D55 D90 D16 A55 A40 A27 A5 D14 A59 A75 A57 A80 D33 D34 A64 A57 A82 A71 A42 A33 A42
Output received

1
3
4

5
13
15
16
20
22
26
27
29
30
33
40
43
51
54
57
62
63
64
71
73
75
82
86
87
88
90
93
95
96
97
100

Output shouldbe 1 3 4 5 13 15 16 20 22 26 27 29 30 33 40 43 51 54 57 62 63 64 71 73 75 82 86 87 88 90
correct

For input A11 D55 A70 D53 A68 D55 A15 A81 A25 A51 A88 A48 A39 A58 D41 D36 A8 D37 A69 A72 A66 D88 D8 I
Output received

2
8
9
11
13
14
18
19
24
25
26
29
33
39
44
56

60
63
64
66
68
69
70
71
72
74
78
80
81
83
94
99

Output shouldbe 2 8 9 11 13 14 18 19 24 25 26 29 33 39 44 56 60 63 64 66 68 69 70 71 72 74 78 80 81 83 94 99
correct

For input D56 D29 A75 A67 D2 A61 D89 D61 A76 A62 D41 D96 D77 A69 A31 D16 D67 D7 A43 A85 A6 A56 A15 A31
Output received

6
13
15
21
23
25
31
32
43
44
56
58
61
67
74
75
78
79
82
85
86
87
89
90

Output shouldbe 6 13 15 21 23 25 31 32 43 44 56 58 61 67 74 75 78 79 82 85 86 87 89 90
correct

For input A51 A57 D43 D78 A73 A50 A66 D85 D37 D24 A71 D33 D38 D48 D39 D31 A28 A93 A86 D70 A38 D71 A31
Output received

1
3

16
19
20
23
26
27
28
29
30
31
34
36
38
39
43
44
47
50
51
58
66
67
68
71
72
73
75
78
81
82
83
84
86
87
88
90
91
93
94
97

Output shouldbe 1 3 16 19 20 23 26 27 28 29 30 31 34 36 38 39 43 44 47 50 51 58 66 67 68 71 72 73 75
correct

For input D53 D85 A91 A48 D1 D6 A84 A76 A75 D59 A32 D66 D51 D65 A42 D5 D13 D33 A29 D68 D39 D99 D80 D8
Output received

2

11 7

18

28 26 16

```
32
39 33 29
46
48

56 55

69 68 60 53 42
74
78
81 79 76
86
89
95
99
100 98 91 84 70
Output shouldbe 2 11 7 18 28 26 16 32 39 33 29 46 48 56 55 69 68 60 53 42 74 78 81 79 76 86 89 95 99
correct

For input A74 A36 D26 D59 D12 D88 A84 A67 D13 D24 A95 A80 A11 A21 A13 D92 A80 A23 D27 A10 D50 D25 D35
Output received

2
10 6

19 17
32 23 11
37
53 48
60 56 36

73 72
76 74
```

85 84

89

99 96 93 86 82 66

Output shouldbe 2 10 6 19 17 32 23 11 37 53 48 60 56 36 73 72 76 74 85 84 89 99 96 93 86 82 66
correct

For input D23 A79 A77 D6 A40 A37 D99 A93 A74 D96 D40 A61 A34 A46 A76 D44 A74 A78 D69 A60 A75 D90 A59
Output received

4 1

9

14 13

27 20 5

36

39

46 40

57 54 51 33

68 65

75 74

78 77 76

85

93

100 99 86 79 61

Output shouldbe 4 1 9 14 13 27 20 5 36 39 46 40 57 54 51 33 68 65 75 74 78 77 76 85 93 100 99 86 79 61
correct

For input A71 D75 D47 D66 A83 D13 A60 D72 A54 A21 A61 D12 A33 A14 A29 D26 D70 D21 A27 D8 A82 D36 A45
Output received

2

3

14

20

27

28

33

34
35
37
39
45
48
53
54
56
60
61
64
71
74
80
82
86
87
88
90
93
95
96

Output should be 2 3 14 20 27 28 33 34 35 37 39 45 48 53 54 56 60 61 64 71 74 80 82 86 87 88 90 93 95
correct

For input D37 D69 D20 D89 D91 D11 A81 A14 A20 D72 A26 D43 D79 D68 A21 D89 D31 A30 D82 A24 D92 D28 A14
Output received

4
8
13
14
20
21
26
27
28
30
35
36
38
44
45
46
48
50
53
55
56
62
71
72
83

86
88
93
95
99

Output shouldbe 4 8 13 14 20 21 26 27 28 30 35 36 38 44 45 46 48 50 53 55 56 62 71 72 83 86 88 93 95
correct

For input D37 D15 A40 D59 A21 D32 A41 D97 D77 A69 D43 A19 A88 A15 D4 D99 A76 A15 D83 A87 A55 A1 A5 A8
Output received 40 14 5 2 1

4

10

27 21 17 16

24 22

25

32

36

67 59 53 41

57

64 61

66

87 76 74

83

88

96

Output shouldbe 40 14 5 2 1 4 10 27 21 17 16 24 22 25 32 36 67 59 53 41 57 64 61 66 87 76 74 83 88 96
correct

For input A14 D79 A42 D79 A92 A72 A10 A25 D45 D62 D14 A5 D38 D2 A17 A81 A48 D24 A47 A55 A10 A73 A86 A1
Output received

2

4

5

7

9

10

11

```
15
18
25
27
36
37
38
39
42
43
47
50
51
52
55
66
68
69
70
71
72
73
75
81
82
83
84
86
90
91
92
94
96
97
100
```

```
Output shouldbe 2 4 5 7 9 10 11 15 18 25 27 36 37 38 39 42 43 47 50 51 52 55 66 68 69 70 71 72 73 75
correct
```

```
For input D63 A80 A22 A58 D40 D73 D92 A20 D33 A30 D24 A14 D18 A26 A50 D12 A71 A25 D39 A99 D99 D79 A36
Output received 48 22 14 9 6
8
```

```
12
```

```
19 15
```

```
20
```

```
30 26 25 24
```

```
28
```

36 33

39 38

43

58 51 49

52

56

71 61 59

70 62

89 75 74

80

96 92

100

Output shouldbe 48 22 14 9 6 8 12 19 15 20 30 26 25 24 28 36 33 39 38 43 58 51 49 52 56 71 61 59 70 62 89 80 96 92 100
correct

For input D76 D70 D63 D7 D60 A39 D19 A60 D54 A12 A80 D66 D12 A38 A11 A86 A18 D69 A62 A69 A61 A39 D7 I
Output received 51 18 11 6
9

14 12

17

29 27 24

28

45 38

46

86 76 62 55 52

59

69

82 81

95 91 88

93

96

Output shouldbe 51 18 11 6 9 14 12 17 29 27 24 28 45 38 46 86 76 62 55 52 59 69 82 81 95 91 88 93 96
correct

For input A12 A10 D63 D25 D59 A89 D78 D65 D23 A97 D38 A29 D61 A14 A50 A35 D40 D50 D10 D31 A11 D93 A94
Output received

1

3

7

8

12

14

16

17

29

30

32

35

37

54

59

60

66

72

77

79

81

83

86

89

93

94

97

Output shouldbe 1 3 7 8 12 14 16 17 29 30 32 35 37 54 59 60 66 72 77 79 81 83 86 89 93 94 97
correct

For input A3 A76 D6 A37 A1 D59 D50 A23 A31 A1 D68 A79 D29 D53 A22 A94 D1 A78 A40 D90 D96 D36 A59 D94
Output received

2

4

20

22 21

30 23 8

```
37
41 38
52
62 58 42 31
73
75 74
78
86 79

99 93 92 76 66
Output shouldbe 2 4 20 22 21 30 23 8 37 41 38 52 62 58 42 31 73 75 74 78 86 79 99 93 92 76 66
correct

For input A76 D59 A69 D33 D8 D30 D54 D44 D36 D89 D38 A34 D64 A100 D60 D31 A92 D19 D98 A25 D16 A79 D16
Output received

8 2
11
14 12
25
31 26 22 10

38 37
64 62

78 73 69
90
94

100 99 92 85 34
Output shouldbe 8 2 11 14 12 25 31 26 22 10 38 37 64 62 78 73 69 90 94 100 99 92 85 34
correct

For input A57 A32 D55 D19 A45 D64 D68 D85 D80 D58 D10 D89 D58 D56 A59 D74 D49 D55 D68 A27 D8 A86 A14
Output received
```

```
1
14
16 15 5
21
23 22 17
28
34 32
36
37 35
42
47 45
58
60 59 52
65
90
97 93 80 61 41 27
Output shouldbe 1 14 16 15 5 21 23 22 17 28 34 32 36 37 35 42 47 45 58 60 59 52 65 90 97 93 80 61 41
correct
For input  A22 A31 A54 A41 D65 D21 A22 D1 A28 A22 A15 D66 D32 D4 D64 D2 A17 D88 D12 D21 D4 D3 D11 D33
Output received 31 13 4 2
3
8 6
10
20 17 15
28 26
54 44 41 39 32
43
50 46
```

67 59 55

65 62

66

79 70

93 90

96

Output shouldbe 31 13 4 2 3 8 6 10 20 17 15 28 26 54 44 41 39 32 43 50 46 67 59 55 65 62 66 79 70 93
correct

For input D18 D6 D66 A13 A98 D15 D54 A38 D47 A44 A98 D46 A25 A60 D88 D64 D65 D30 A1 A85 A28 A45 A49 I
Output received

1

15 10 3

29 26

33 30 18

39

44 40

47 45 38

62

63

66 65

72

77 76

90 83 82 68 50

Output shouldbe 1 15 10 3 29 26 33 30 18 39 44 40 47 45 38 62 63 66 65 72 77 76 90 83 82 68 50
correct

For input A81 D64 D17 A58 A77 D86 D100 D51 D84 D98 A24 D61 A9 A10 A45 D22 A80 D2 A100 A83 D79 A44 D20
Output received

1

4

10
16
17
20
23
24
28
29
31
34
35
43
44
45
46
49
50
54
56
58
60
63
65
68
70
76
78
80
83
88
91
92
95

Output shouldbe 1 4 10 16 17 20 23 24 28 29 31 34 35 43 44 45 46 49 50 54 56 58 60 63 65 68 70 76 78
correct

For input D81 A49 A63 A23 D42 D68 A51 D78 A28 D18 A64 A82 A32 D94 A100 A81 A80 D83 A8 A77 D6 D68 A59
Output received

3
4
7
8
13
14
15
17
20
21
22
23
28
32
35

38
40
41
43
46
49
51
52
53
54
62
63
64
70
72
74
76
78
80
81
82
83
89
91
92
93
100

Output shouldbe 3 4 7 8 13 14 15 17 20 21 22 23 28 32 35 38 40 41 43 46 49 51 52 53 54 62 63 64 70 74
correct

For input D69 D82 D97 A38 D52 D1 A22 A15 A74 A97 D18 A47 A33 A89 A28 D51 D97 D48 A2 A4 A73 A42 A81 A1
Output received 38 10 2 1

7 6

9

22 13 11

20 15

21

33 28

34

74 53 47 42 41

46

48

52

```
60 57 56
```

```
58
```

```
66 63 61
```

```
64
```

```
73
```

```
89 81 80 79
```

```
87
```

```
94 93
```

```
95
```

```
98
```

```
Output shouldbe 38 10 2 1 7 6 9 22 13 11 20 15 21 33 28 34 74 53 47 42 41 46 48 52 60 57 56 58 66 63  
correct
```

```
For input A83 D94 D41 A97 A81 A6 A78 A91 D84 A84 A45 D10 D33 A28 D69 D68 A22 A7 A28 A40 A29 D22 A88 A  
Output received
```

```
1
```

```
6
```

```
8
```

```
20
```

```
23
```

```
27
```

```
28
```

```
29
```

```
30
```

```
38
```

```
39
```

```
40
```

```
44
```

```
45
```

```
47
```

```
49
```

```
50
```

```
61
```

```
66
```

```
69
```

```
73
```

```
74
```

```
80
```

```
81
```

```
84
```

```
86
```

```
88
```

91
93
96
97
100

Output shouldbe 1 6 8 20 23 27 28 29 30 38 39 40 44 45 47 49 50 61 66 69 73 74 80 81 84 86 88 91 93 96
correct

For input A66 D73 D32 A15 D97 D21 D29 D16 D13 A74 A69 D71 A6 A16 A70 A55 D10 A43 A40 D56 A24 A20 A77
Output received 36 23 16 10 6 5

8

15

20 17

27 25

33 30

70 55 47
52

66 57

68

88 74 71

77

84

97 96

Output shouldbe 36 23 16 10 6 5 8 15 20 17 27 25 33 30 70 55 47 52 66 57 68 88 74 71 77 84 97 96
correct

For input A89 A76 D37 D25 D67 D14 A34 A16 A64 D14 A40 D66 D60 D5 D79 D92 A60 D63 D26 A46 D1 A74 A2 A6
Output received

3 2

10 9 4

21
29

```
35
36 34 18

50

57

62 60 54

66
67 64

76

81

87 84 79

93
94

100 98 89 73 41
Output shouldbe 3 2 10 9 4 21 29 35 36 34 18 50 57 62 60 54 66 67 64 76 81 87 84 79 93 94 100 98 89 `
correct

For input D29 A77 D24 A77 A66 D42 D56 A13 A88 D34 A8 A81 D85 A38 D21 D71 D45 D81 D38 D32 D18 D23 A44
Output received 37 26 13 5 3

11

21 15
19

22

34 27
32

36

66 44 39 38

42 41

60 55 51

61

89 75 74

88 81
```

96 91

98

Output shouldbe 37 26 13 5 3 11 21 15 19 22 34 27 32 36 66 44 39 38 42 41 60 55 51 61 89 75 74 88 81
correct

For input A58 D69 A76 A68 D31 A53 A54 A50 A71 D8 A67 A18 A28 D15 A95 A43 D66 A35 D42 D1 D10 A52 D76 A
Output received 54 8 6 2 1

4

7

20 15 13

18

52 50 35

53

74 68 58

72

73

94 80 75

99 97

100

Output shouldbe 54 8 6 2 1 4 7 20 15 13 18 52 50 35 53 74 68 58 72 73 94 80 75 99 97 100
correct

For input D27 A6 A66 D8 D77 D8 A11 A52 D81 A35 A73 D57 D37 D61 D19 A14 D96 D48 D72 A32 A68 A43 A2 D69
Output received

2

6 4

14

16 15

26

```
31 28 24 10
```

```
35
```

```
40 36
```

```
48 43
```

```
60
```

```
67 64 52
```

```
73 70
```

```
84
```

```
100 89 77 68 32
```

```
Output shouldbe 2 6 4 14 16 15 26 31 28 24 10 35 40 36 48 43 60 67 64 52 73 70 84 100 89 77 68 32  
correct
```

```
For input D49 D69 A82 D96 D80 A94 D5 A33 A3 D85 A12 D11 A32 A36 A89 D19 D39 D7 D55 D100 A64 A81 A64 I  
Output received
```

```
1
```

```
3
```

```
6
```

```
8
```

```
10
```

```
12
```

```
14
```

```
17
```

```
21
```

```
22
```

```
32
```

```
33
```

```
36
```

```
43
```

```
48
```

```
52
```

```
53
```

```
56
```

```
57
```

```
61
```

```
64
```

```
66
```

```
67
```

```
69
```

```
70
```

```
73
```

```
75
```

```
81
```

```
82
```

```
88
```

89
90
93
94
95

Output shouldbe 1 3 6 8 10 12 14 17 21 22 32 33 36 43 48 52 53 56 57 61 64 66 67 69 70 73 75 81 82 88
correct

For input D96 A1 D16 D4 D15 A38 D95 D47 A6 A19 A12 D68 D22 A77 A33 A95 A44 A77 D46 A97 D93 D96 D28 D
Output received

1

7
11 5

14

17 15

25 20

34
36 30 19 12

39

42 41

53

57 55

60

65 61 59 43

70

75

84
85 81

88

93

97 95 91 86 67 38

Output shouldbe 1 7 11 5 14 17 15 25 20 34 36 30 19 12 39 42 41 53 57 55 60 65 61 59 43 70 75 84 85 88
correct

For input D32 D71 A32 A65 D52 D49 A52 A36 A61 D79 D32 A95 D31 D27 D17 A88 D97 D66 A99 D77 D39 D42 A81

Output received 52 16 6 2 1

13 8

26 24 17

36

65 61 55 54

58

63 62

64

81 67 66

76

84 83

95 92

Output shouldbe 52 16 6 2 1 13 8 26 24 17 36 65 61 55 54 58 63 62 64 81 67 66 76 84 83 95 92
correct

For input D11 A55 D54 A24 D42 A97 A50 A75 A57 A52 D73 D2 A40 A44 D78 D23 A1 D23 D21 A72 A71 A71 D4 D5
Output received

1
4
5
6
9
10
17
18
19
24
32
33
35
38
40
44
45
50
51
52

53
55
62
64
71
72
78
85
87
92
95
97
99
100

Output shouldbe 1 4 5 6 9 10 17 18 19 24 32 33 35 38 40 44 45 50 51 52 53 55 62 64 71 72 78 85 87 92
correct

For input A28 A90 D5 A7 D94 D83 D44 D96 A52 A46 D57 D15 D21 D50 D94 A64 D70 D42 A66 D60 A39 D47 D60 I
Output received 39 18 4 1
3

8 7

12 10

14

28 23 21

27

33 32

36

66 52 46 44 41

45

49 48

58 54 53

57

62 59

64

83 72 67

78 73

91
97

Output shouldbe 39 18 4 1 3 8 7 12 10 14 28 23 21 27 33 32 36 66 52 46 44 41 45 49 48 58 54 53 57 62
correct

For input A14 D8 A49 A45 D95 A30 D12 D56 A37 D76 D29 A100 A6 D85 A40 A62 D79 A33 A6 A90 D41 A61 A42 1
Output received

3 1

7

13 11 6

17

21 20

25

27 26 23

44 37 30 14

49

52

65

69

77 67 56

85

92 90

100 98 97 84 45

Output shouldbe 3 1 7 13 11 6 17 21 20 25 27 26 23 44 37 30 14 49 52 65 69 77 67 56 85 92 90 100 98 9
correct

For input A39 D99 D85 A94 A87 A53 A70 A45 A22 A22 D57 A14 A14 A2 D65 D63 D50 D31 A94 D66 A38 D40 A3 1
Output received 72 39 13 6 3 2

9

22 18 14

28 27

38

65 55 51 41

53

59

63

70 66

71

87 83 82

94 90

91

97 95

Output shouldbe 72 39 13 6 3 2 9 22 18 14 28 27 38 65 55 51 41 53 59 63 70 66 71 87 83 82 94 90 91 97
correct

For input A42 A84 A57 D44 D26 A100 D84 D73 D49 A76 A15 A13 A81 D81 A28 A5 A21 A56 D42 A38 A68 D25 D5
Output received

7

17 15 12

39

47 45

51

56 53 48 29

59

60

70 68 63

73

76

```
92

100 97 84 71 57
Output shouldbe 7 17 15 12 39 47 45 51 56 53 48 29 59 60 70 68 63 73 76 92 100 97 84 71 57
correct

For input A78 A20 A93 D59 A59 A85 D87 A19 A75 A41 A82 A50 A39 A90 A76 A81 A83 D31 A92 D6 D94 D47 D55
Output received 39 20 8 2

17 16

33 29 25

30

37 35

76 59 50 44 41

45

53
56

67 61

72 69

74

85 83 82 81

84

92 90

93
100

Output shouldbe 39 20 8 2 17 16 33 29 25 30 37 35 76 59 50 44 41 45 53 56 67 61 72 69 74 85 83 82 81
correct

For input D22 A64 D74 D98 D87 D31 A9 A57 A50 A71 A76 D35 A10 A89 A59 A58 A40 A3 A62 D87 D43 D48 A42 A
Output received

2

6 3
```

11 10

14

19 18 12

29

33 31

40

42

52 43 37 25

62

73

77 76 64

83 81

100 87 78 57

Output shouldbe 2 6 3 11 10 14 19 18 12 29 33 31 40 42 52 43 37 25 62 73 77 76 64 83 81 100 87 78 57
correct

For input A61 A18 D81 A12 A35 A14 D72 D38 D97 A6 D26 A68 D23 A58 D36 D81 D50 A53 D62 D6 A51 A99 D52 A
Output received 44 32 18 14 2

23 21 19

22

27

28

36 35 33

40 39 37

42

52 47 45

46

51

64 54

61

77 67 65

73

90 89

99

Output shouldbe 44 32 18 14 2 23 21 19 22 27 28 36 35 33 40 39 37 42 52 47 45 46 51 64 54 61 77 67 65
correct

For input D9 A21 D63 D2 A87 D99 D52 D72 D11 D93 A49 D29 A39 D62 D52 D74 A78 A20 A9 D44 D38 D67 D68 A5
Output received

5

9 7

14

21 19 12

38

41

42 39 27

50

51

54

61 52

70

72 71

76 75 62

79

82 81

91 87 83

99

100 93 78 46

Output shouldbe 5 9 7 14 21 19 12 38 41 42 39 27 50 51 54 61 52 70 72 71 76 75 62 79 82 81 91 87 83 99
correct

For input D50 A1 A45 A82 A59 A11 A53 D90 A50 A73 D93 D2 A8 D85 D29 D79 A55 A72 D33 D66 A94 A46 A25 A5

Output received 45 18 11 2 1

8

17 13

29 26 25

28

35 31 30

44 36

59 54 47 46

53

55

56

85 77 69 64

73

82

84

90 88

91

94

Output should be 45 18 11 2 1 8 17 13 29 26 25 28 35 31 30 44 36 59 54 47 46 53 55 56 85 77 69 64 73 82 84 90 91 94
correct

For input A19 D30 D95 A46 D17 A39 D93 D16 A95 D59 A34 D15 A83 A24 A12 D76 D81 D19 A68 A23 A89 D83 A5
Output received

3

5

29 17

32

35 34 30 7

39

46 40

48

53

58 52 47

69

82

87 86 81

94

100 96 95 93 68 37

Output shouldbe 3 5 29 17 32 35 34 30 7 39 46 40 48 53 58 52 47 69 82 87 86 81 94 100 96 95 93 68 37
correct

For input A56 D44 D59 D72 D62 D61 D59 A93 A88 A51 A97 A21 D38 A92 A18 D62 A69 A73 D37 A52 A23 A85 A96

Output received 56 35 18 9 5 3

7

16 11

17

28 25 23 22

27

33 31

34

51 39 37

44 41

48

54 52

55

88 79 65 59

63

72

85 84 83

86

87

93 92 89

97 95

99

Output shouldbe 56 35 18 9 5 3 7 16 11 17 28 25 23 22 27 33 31 34 51 39 37 44 41 48 54 52 55 88 79 65
correct

For input A66 A20 A5 A96 D66 D62 D49 D76 D42 A39 A24 A64 D66 D63 A23 A14 D32 A7 D89 D78 A47 A56 A10 I
Output received

7

14

23 21 13

29

33 32

38 34 26

48 44

54

56 50

80 65

92

96 94 91 61 39

Output shouldbe 7 14 23 21 13 29 33 32 38 34 26 48 44 54 56 50 80 65 92 96 94 91 61 39
correct

For input A51 D31 D39 D15 A84 D28 A68 A87 A82 D58 D48 A87 D45 D55 D75 D31 A98 A41 D45 D25 D40 A21 A98
Output received

3

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8

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17

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23
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26
27
31
37
41
42
44
45
51
53
55
57
59
63
74
78
79
82
86
95
98
100

Output shouldbe 3 4 8 13 17 18 20 21 23 25 26 27 31 37 41 42 44 45 51 53 55 57 59 63 74 78 79 82 86 95 98 100
correct

For input A99 A2 D80 A67 A20 A58 A75 D86 A68 D28 A30 A82 A30 D6 D62 A99 D27 D19 A43 D27 D5 A82 A56 D6
Output received

2
16

21

28 27 22 17

31

35 34

39 38 30

53 49

58 54

62

64

66 65 63 60

76 70

88

90

99 97 89 82 67 43

Output shouldbe 2 16 21 28 27 22 17 31 35 34 39 38 30 53 49 58 54 62 64 66 65 63 60 76 70 88 90 99 97
correct

For input D79 D82 A37 D45 D81 A24 D72 A28 D87 A49 D38 A23 A80 A82 D12 A31 D68 A40 D63 D74 A96 D7 D50
Output received

8

10

11

12

14

23

24

25

29

31

35

36

40

45

46

49

51

54

60

61

62

69

72

82

89

92

96

98

99

100

Output shouldbe 8 10 11 12 14 23 24 25 29 31 35 36 40 45 46 49 51 54 60 61 62 69 72 82 89 92 96 98 99
correct

For input D85 D64 D57 A88 A63 A99 D17 D38 A50 D1 A31 A73 A63 D92 D73 A28 D9 D67 D59 D59 D19 A56 D14 A
Output received

2
6
9
14
18
22
24
25
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31
33
35
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40
43
50
54
56
59
60
63
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67
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81
83
85
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88
91
93
95
98
99

Output shouldbe 2 6 9 14 18 22 24 25 28 31 33 35 37 38 39 40 43 50 54 56 59 60 63 66 67 75 77 79 81 83 85 87 88 91 93 95 98 99
correct

For input A70 A40 D82 A34 A56 A68 D50 D52 A57 D36 D96 A46 D84 D98 A4 D14 A65 A86 A51 D30 A36 A67 A11
Output received

1
4
7
8
10
11
21
23
24
25

27
29
30
33
34
36
37
38
40
51
52
53
57
64
65
70
71
75
77
82
83
86
93
94
100

Output should be 1 4 7 8 10 11 21 23 24 25 27 29 30 33 34 36 37 38 40 51 52 53 57 64 65 70 71 75 77 82
correct

For input A42 D55 D33 A100 A62 A51 D27 D67 A72 A23 D41 A82 D3 A21 D80 A57 D65 D86 A14 D29 D81 A57 A91
Output received

3

5 4

15

17 10

23

28 25

37 34 33 20

43

47

49 45

54 52

61 60 57 50 42

71 69

75

81 78 72

84

88

90

96

97 93 89 82 62

Output shouldbe 3 5 4 15 17 10 23 28 25 37 34 33 20 43 47 49 45 54 52 61 60 57 50 42 71 69 75 81 78
correct

For input A32 A4 D24 D72 A22 A8 A47 D35 D62 D28 D54 A49 A52 A91 A37 A9 A22 D86 A7 D69 A61 A96 A71 D62
Output received 47 22 13 9 7 5

12

16

19

34 29 24

37

44

61 57 49

56

60 59

90 78 70 67

75

87 83

98 91

99

Output shouldbe 47 22 13 9 7 5 12 16 19 34 29 24 37 44 61 57 49 56 60 59 90 78 70 67 75 87 83 98 91 5

correct

For input D57 A74 A32 A38 D100 A85 D73 A97 A81 A91 A1 A71 A51 D91 D94 D97 A56 A57 D93 D11 A44 A59 D56
Output received

1

4

13

21

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35

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49

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61

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69

71

72

74

81

83

89

91

92

96

99

100

Output shouldbe 1 4 13 21 25 30 32 35 38 40 42 44 46 49 51 57 58 59 61 62 69 71 72 74 81 83 89 91 92
correct

For input D94 D55 D35 A88 D29 D64 A68 A34 D40 D54 D71 D82 A91 D85 A73 A96 A77 A82 D79 A16 A28 A96 D85
Output received 55 26 16 13 8

14

23 17

22

25

34 31

32

44 42 41

43

45

77 68 65 63 56

66

73 70

74

91 82 78

90

96 94

99

Output shouldbe 55 26 16 13 8 14 23 17 22 25 34 31 32 44 42 41 43 45 77 68 65 63 56 66 73 70 74 91 82
correct

For input A54 A99 D60 A80 A28 D4 D60 D62 A76 A100 A34 A31 A29 A31 D57 D59 D95 D90 A46 A91 D44 D81 A56
Output received

2

8

14 10 7

18

26 21

29 28 15

34

36 35

44 38

48 47

50 49 46 31

56

```
58 57

67 60

74

78 76 70 59

91

94 92

100 96 80 54
Output shouldbe 2 8 14 10 7 18 26 21 29 28 15 34 36 35 44 38 48 47 50 49 46 31 56 58 57 67 60 74 78
correct

For input D16 D13 A40 A61 A61 D1 A27 A60 A46 D11 D94 A86 D5 D72 D85 A20 D36 A20 A1 D80 A40 A15 A38 D
Output received 40 20 10 5

14

24 23

27
35

67 51 44 41

46

61 59

63

73 70
71

86 82 74

95

Output shouldbe 40 20 10 5 14 24 23 27 35 67 51 44 41 46 61 59 63 73 70 71 86 82 74 95
correct

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Web Submission Script Complete.
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Authorised by: Head of School, School of Computer Science

Maintained by: School of Computer Science

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