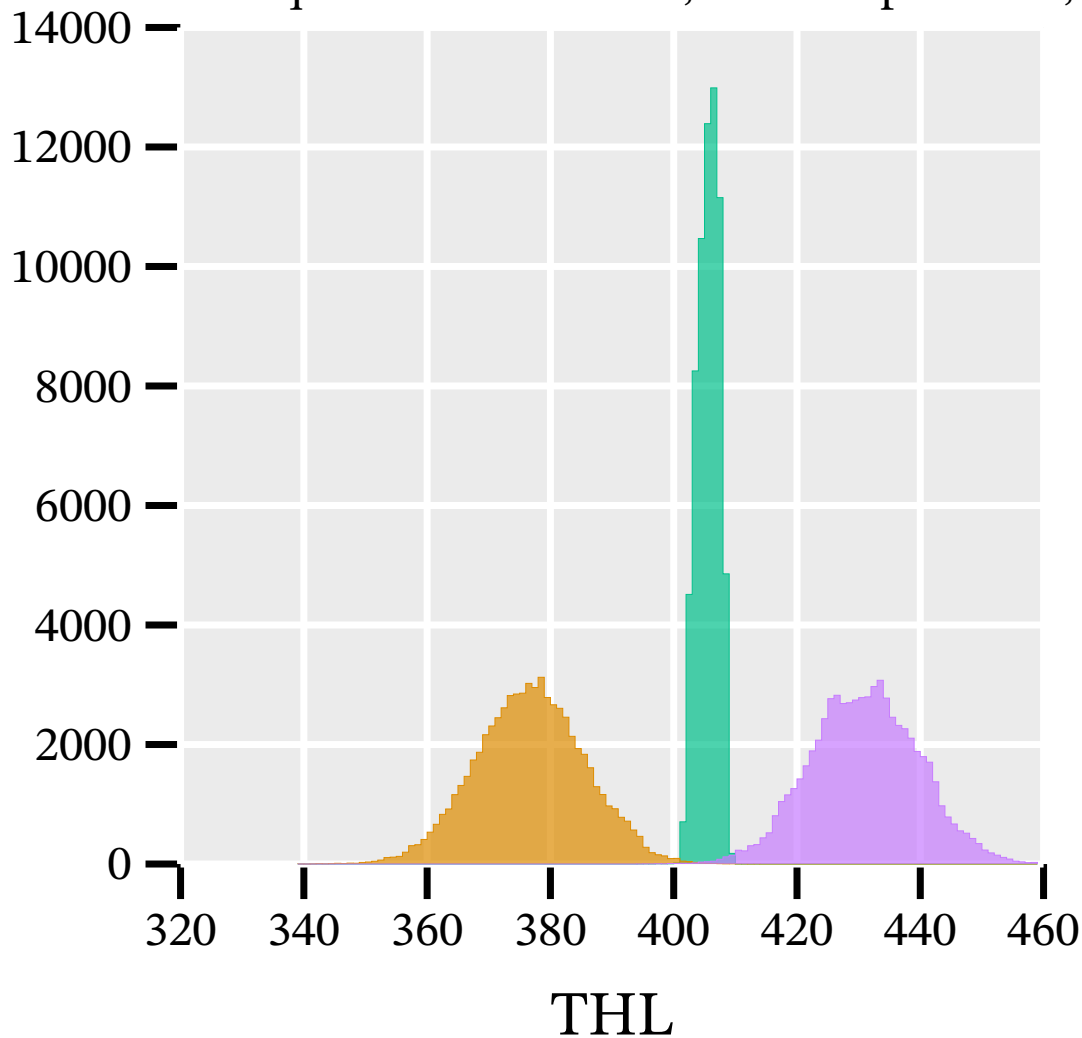


All equalization bits at 0, 15 and optimized, Run3, chip 4

count



type



max

min

opt