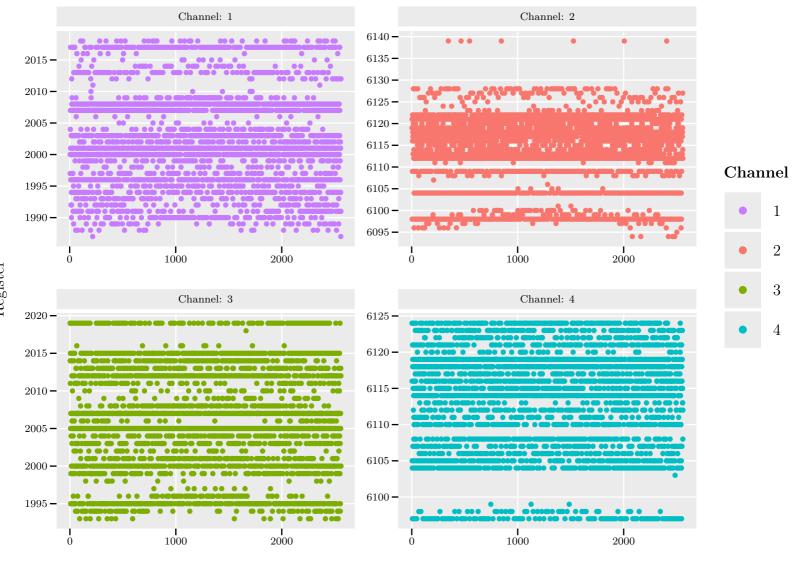
FADC register pedestal values, split by channels



Channel