Assignment-5 Assembly Programming Problems

Full Credit: 20 pts | Due: Sunday 3/20

In this assignment you are asked to translate C code to ARM64/ARMv8 assembly code, or trace assembly code. C code variables should be mapped to allocated stack frame space or ARMv8 registers in assembly. You must add comment to each line of assembly code. Test your code with Dr. Shuqun Zhang's AMR64 Online Simulator.

Q1. Convert the following C functions into ARMv8 assembly language. Again, comment each line of assembly code on what it does. Note that local variables should be kept in function's stack frame.

```
1) Testing arithmetic operations
int arithOps( int a, int b ) {
    int u, v, w;
    u = a << 2;
    v = b >> 2;
    w = u + v;
    return w;
}
sub sp, sp, #32
str w0, [sp, #28]
str w1, [sp, #24]
ldr w8, [sp, #28]
lsl w8, w8, #2
str w8, [sp, #20]
ldr w8, [sp, #24]
asr w8, w8, #2
str w8, [sp, #16]
ldr w8, [sp, #20]
ldr w9, [sp, #16]
add w8, w8, w9
str w8, [sp, #12]
ldr w0, [sp, #12]
add sp, sp, #32
ret
2) Doubles the input value
int doubleMe( int a ) {
    int tmp;
    tmp = 2 * a;
    // tmp = a + a;
    return tmp;
sub sp, sp, #16
str w0, [sp, #12]
ldr w9, [sp, #12]
mov w8, #2
```

```
mul w8, w8, w9
str w8, [sp, #8]
Idr w0, [sp, #8]
add sp, sp, #16
ret
3) Main function to test the above two functions
int main( ) {
    int x = -3;
    int y = 28;
    int z1, z2;
    z1 = arithOps(x, y);
    z2 = doubleMe(x);
    return 0;
}
  ; int main()
         push
                  rbp
                 rbp, rsp
        mov
         sub
                 rsp, 16
   ; int x = -3;
        mov
                 DWORD PTR [rbp-4], -3
   ; int y = 28;
                 DWORD PTR [rbp-8], 28
   ;z1 = arithOps(x,y);
                 edx, DWORD PTR [rbp-8]
        mov
                 eax, DWORD PTR [rbp-4]
        mov
                 esi, edx
        mov
                 edi, eax
        mov
                 arithOps(int, int)
         call
                 DWORD PTR [rbp-12], eax
        mov
   ;z2 = doubleme(x);
                 eax, DWORD PTR [rbp-4]
        mov
                 edi, eax
        mov
         call
                 doubleme(int)
                 DWORD PTR [rbp-16], eax
        mov
```

```
;return 0;
  mov eax, 0
  leave
  ret
```

Q2. [Code-tracing] Given the following ARM64 assembly code, add comments to each line, trace the code execution to figure out what it does, and finally convert it back to its corresponding C-code. Notice that local variables are stored in the main function's stack frame, drawing the stack frame would help you understand the code better.

```
main:
        sub
                 sp, sp, #32
                                       stack pointer increase to 32 memory
        str
                 wzr, [sp, 28] // a
                                      store at sp+28
                 w0, 1
        mov
                                       copy value
                 w0, [sp, 24] // b
                                       store at sp+24
        str
                                       store at sp+16
                 wzr, [sp, 16] // c
        str
                 w0, 8
        mov
                                       copy the data
                 w0, [sp, 12] // n
                                       store at sp+12
        str
        mov
                 w0, 1
                                       copy the data
                 w0, [sp, 20] // i
        str
                                       store at sp+20
                 Checking
        b
Loop:
        ldr
                 w1, [sp, 28]
                                       load into register
                 w0, [sp, 24]
w0, w1, w0
        ldr
                                       load into register
        add
        str
                 w0, [sp, 16]
                                       store at sp+16
        ldr
                 w0, [sp, 24]
                                       load into register
                 w0, [sp, 28]
        str
                                       store at sp+28
        ldr
                 w0, [sp, 16]
                                       load into register
        str
                 w0, [sp, 24]
                                       store at sp+24
        ldr
                 w0, [sp, 20]
                                       load into register
        add
                 w0, w0, 1
                                       addition
                 w0, [sp, 20]
        str
                                       store at sp+20
Checking:
        ldr
                 w1, [sp, 20]
                                       load into register
                 w0, [sp, 12]
w1, w0
        ldr
                                       load into register
        cmp
                                       compare
        b.lt
                 Loop
                 w0, 0
        mov
                                       copy
        add
                 sp, sp, 32
                                       addition
        ret
                                       return address
int main(int r, int t)
int a,b, c;
a = r << 2;
b = t >> 2;
c = a+b;
return c ;
int doublelooping(int r){
```

```
int res;
res= 2*a;
return res;
}
```

Q3. [Code-tracing] Trace the following program, comment every line of code on what it does, draw call stack diagram to keep track on stack growing and shrinking movements. Remember that each function call needs a stack frame for saving return address and local variables. Explain what the overall program does. When function calls finish and back to main, what's the result stored at [sp, 20]?

```
main:
                 x29, x30, [sp, -32]!
                                             stack at the address -32
        stp
                 x29, sp
                                             copy v in 29
        mov
                 w0, 3
                                             w0 has value 3
        mov
                 w0, [sp, 28]
        str
                                             at address 28 stack pointer
                 w0, 5
                                             now 5 moving to w0
        mov
                 w0, [sp, 24]
        str
                                             address 24 will store
                 w1, [sp, 24]
w0, [sp, 28]
        ldr
                                             load address
                                             w0 has value of 3
        ldr
                 findMaxIncr
                                             branch to link calling function 6
        bl
                 w0, [sp, 20]
w0, 0
        str
                                             store at stack pointer 20
        mov
                                             value 0
                                             loading pointer
                 x29, x30, [sp], 32
        ldp
        ret
findMaxIncr:
                 x29, x30, [sp, -48]!
                                             stack at the address -48
        stp
                 x29, sp
                                             copy to register
        mov
                 w0, [sp, 28]
        str
                                             at address 28 stack pointer
                 w1, [sp, 24]
w1, [sp, 28]
        str
                                             at address 24 stack pointer
        ldr
                                             load address
                 w0, [sp, 24]
w1, w0
        ldr
                                             load address
                                             Both comparing
        cmp
        b.ge
                 greatTE
                                             w1>w0
                 w0, [sp, 24]
        ldr
                                             adding 24
                 w0, [sp, 44]
                                             store at 44
        str
                 commonPart
                                             calling function
        b
greatTE:
                 w0, [sp, 28]
        ldr
                                             load data
                 w0, [sp, 44]
                                             moving to stack pointer 44
commonPart:
        ldr
                 w0, [sp, 44]
                                             having value of 5
                 incr0ne
                                             function call
        bl
                 w0, [sp, 40]
                                             at address 40 stack pointer
        str
                 w0, [sp, 40]
        ldr
                                             loading pointer
        ldp
                 x29, x30, [sp], 48
                                             7 is in sp 48
        ret
incrOne:
                 sp, sp, #16
                                             incrementing value
        sub
        str
                 w0, [sp, 12]
                                             store at 12
        ldr
                 w0, [sp, 12]
                                             load at 12
                 w0, w0, 1
        add
                                             adding
                 sp, sp, 16
        add
                                             adding
                                             return which store in stack pointer
        ret
```

Taking two values into two registers and comparing them, and finding greater value and later it is incrementing the value which come from function and returning to the main.

Submission Instructions: Place all your source code answers either in ARM64 assembly or C into a single file, name it as **LastFirstInitial_a5.c**, which must include all required comments. Put all explanations and drawings of call stack movements into a separate pdf file, name it as LastFirstInitial a5.pdf, and then upload them through your blackboard account by the due day.

Note: If you cannot write into the pdf directly, then you can work on your assignment in Microsoft Word and then save it as pdf before submission.