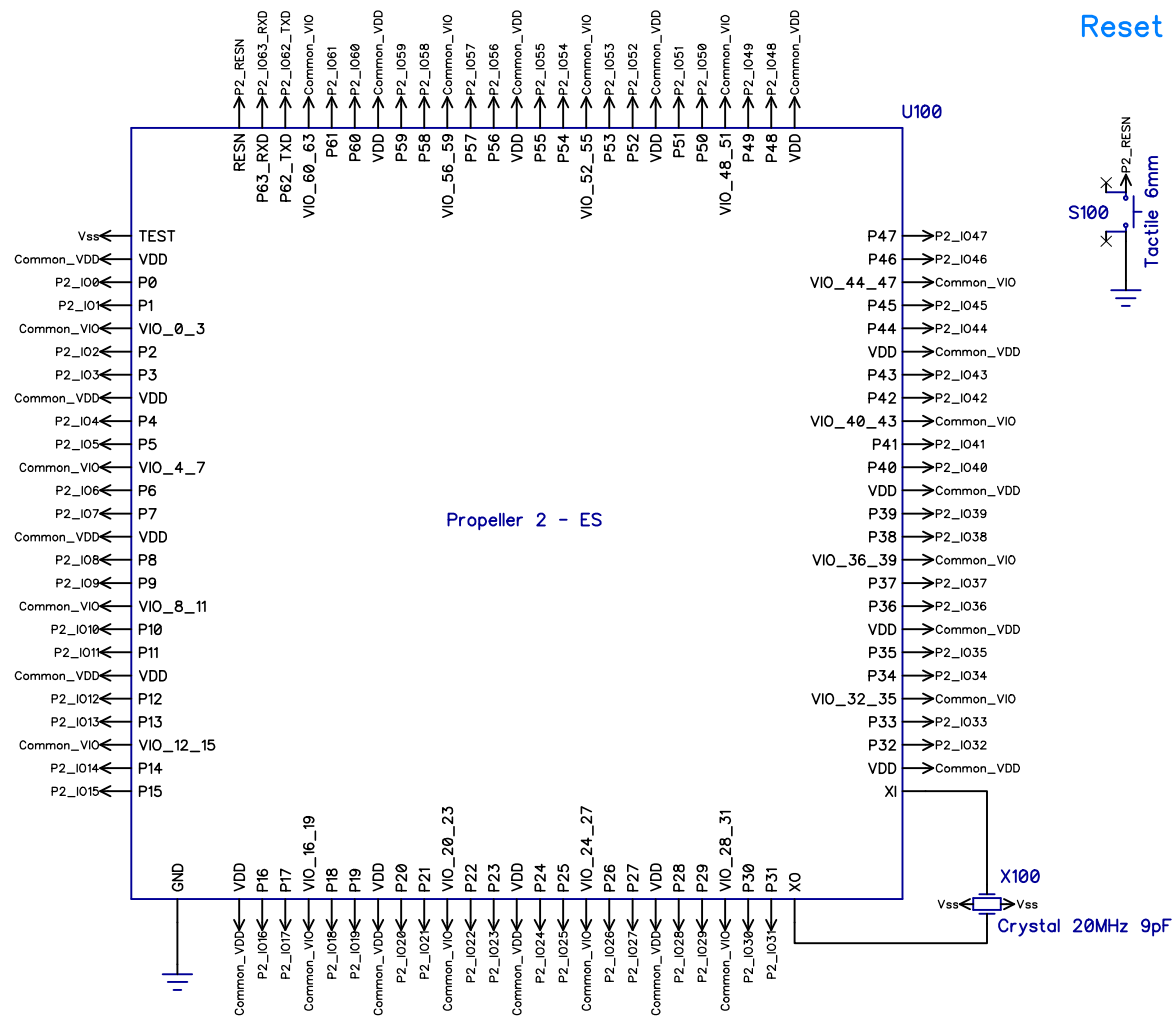
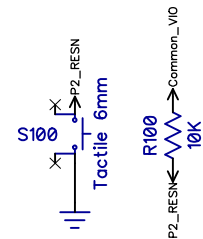


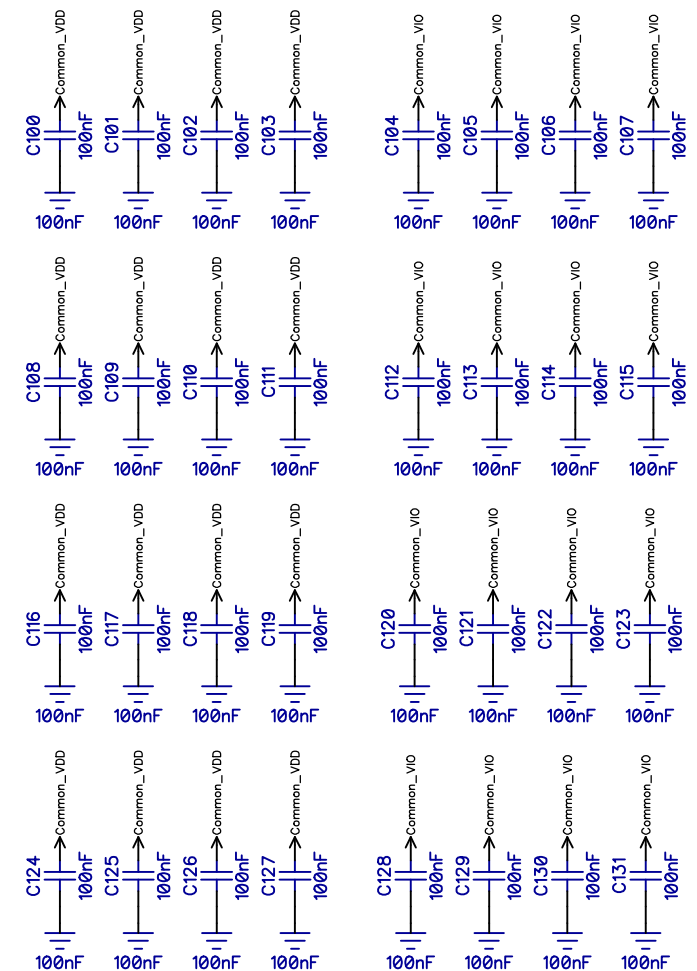
# Microcontroller



## Reset Switch



## VDD and VIO local decoupling



Place decoupling capacitor close to each VIO and VDD pin  
Minimum two per side, in the case of a common voltage bus

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<http://www.parallax.com/>

## Parallax P2X8C4M64P - Getting Started Schematic

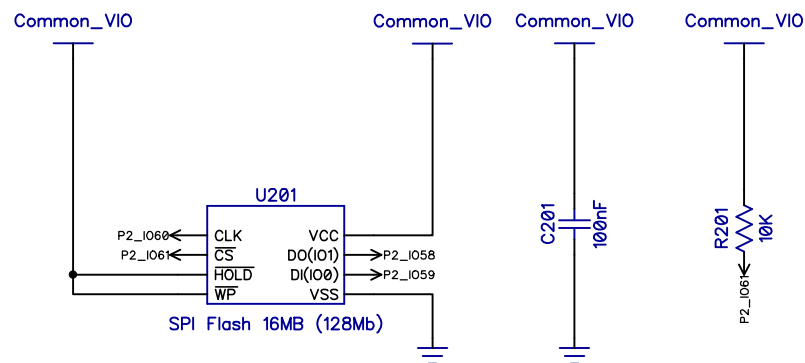
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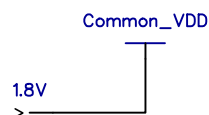
Last Update: 11 Dec 2019 (MM)

# Memory and Power Rails

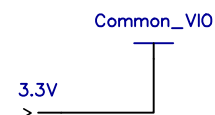
## SPI Flash Memory



VDD Power Supply - Nominal 1.8V 2.5A  
DO NOT EXCEED 2.2V OR RISK PERMANENT DAMAGE!



VIO Power Supply - Nominal 3.3V 2.5A  
DO NOT EXCEED 4.0V OR RISK PERMANENT DAMAGE!



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## Parallax P2X8C4M64P - Getting Started Schematic

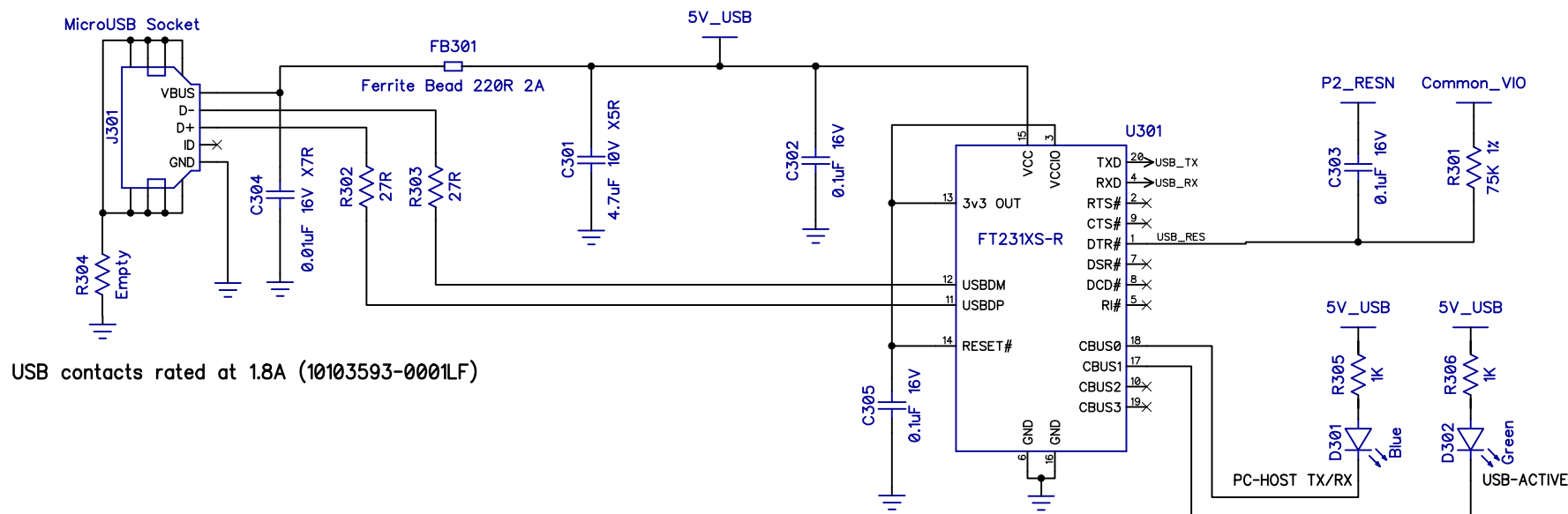
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## Optional USB Socket

Optional USB interface for power and serial data



USB contacts rated at 1.8A (10103593-0001LF)

## FTDI Configuration

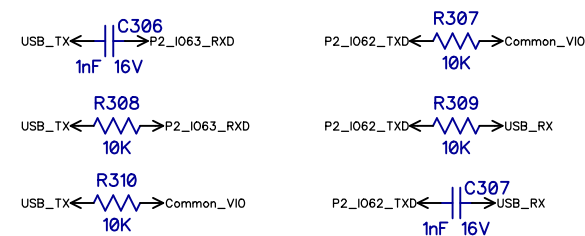
CBUS0 = TX&RXLED

CBUS1 = PWREN#

CBUS2 = Drive 0

**CBUS3 = Drive 0**

USB Config / MaxPower = 500mA



Pull-up serial bus to prevent floating-state terminal chars  
Include series to prevent parasitic power in either direction  
Add small caps for high-speed serial. (PASS > 3Mbaud)

Note: For programming the P2, could use a PropPlug connected to P2\_I062\_TXD, P2\_I063\_RXD and P2\_RESn instead of having a dedicated FTDI USB interface on the pcb

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