Whiteblock Test Plan Document

whiteblock

Prepared for Syscoin

SYSCOIN

Test Case Procedure	Expected Output
1. Build new network according to specified topology 2. Create addresses (total num varies by test case) 3. Receiving node group assigned 1 address each 4. Remaining addresses are evenly divided among transaction node group 5. Create assets for each address 6. Send asset allocations from Tx group to Rcv group 7. Log tx time on sending node 8. Timeout 10 seconds 9. Get tpsTestInfo on each node 10. Get mempool info on each node and output to DB 11. Get raw mempool on each node and output to DB 12. Repeat until block height reaches specified value 13. Make sure all data is saved to DB in proper format 14. Backup data locally 15. Tear down network 16. Check data after each test	Tx ID Sender address Receiver address Send time Rcv time Total time Block number Block hash Mempool info Raw mempool data

SYSTEM SPECS	
ITEM	**DESCRIPTION**
Operating System	Ubuntu Bionic 18 LTS
Syscoin Core Version	4.0.0.0-80da881
Whiteblock Genesis Version	Diet-v1.1-stable
Whiteblock Syscoin Test Library Version	v.3.1
CPU Platform	Intel Xeon Scalable Processor (Skylake)
Base Frequency (GHz)	2
All-Core Turbo Frequency (GHz)	2.7
Single-Core Max Turbo Frequency (GHz)	3.5
vCPUs	3
RAM	4
Storage	10Gb SSD
Max Number of Nodes in Network	30

TEST SERIES				
Series No/Var	**Topology**	**Case A**	**Case B**	**Case C**
1 Control	Total Nodes	30	-	-
	Master Nodes	24	-	-
	# of Assets	9	-	-
	Tx/Asset	250	-	-
	Network Latency (ms)	0	-	-
2 Network Latency	Total Nodes	30	30	30
	Master Nodes	24	24	24
	# of Assets	9	9	9
	Tx/Asset	250	250	250
	Network Latency (ms)	50	100	150
3 # of Assets	Total Nodes	30	30	30
	Master Nodes	24	24	24
	# of Assets	21	30	39
	Tx/Asset	250	250	250
	Network Latency (ms)	24	24	24
4 Total Nodes	Total Nodes	10	15	20
	Master Nodes	4	9	14
	# of Assets	9	9	9
	Tx/Asset	250	250	250
	Network Latency (ms)	24	24	24
5 Double Latency on % of Nodes	Total Nodes	30	30	30
	Master Nodes	24	24	24
	# of Assets	9	9	9
	Tx/Asset	250	250	250
	Network Latency (ms)	24	24	24

Addendums & Appendices