

EE 204-2018-2 Analog Circuits Homework #1 Solution

Question 1 (a)

The following is the curve for transconductance as a function of gate voltage when $V_{DS} = 5V$ for NMOS with $W = 10\mu m$ and $L = 0.18\mu m$

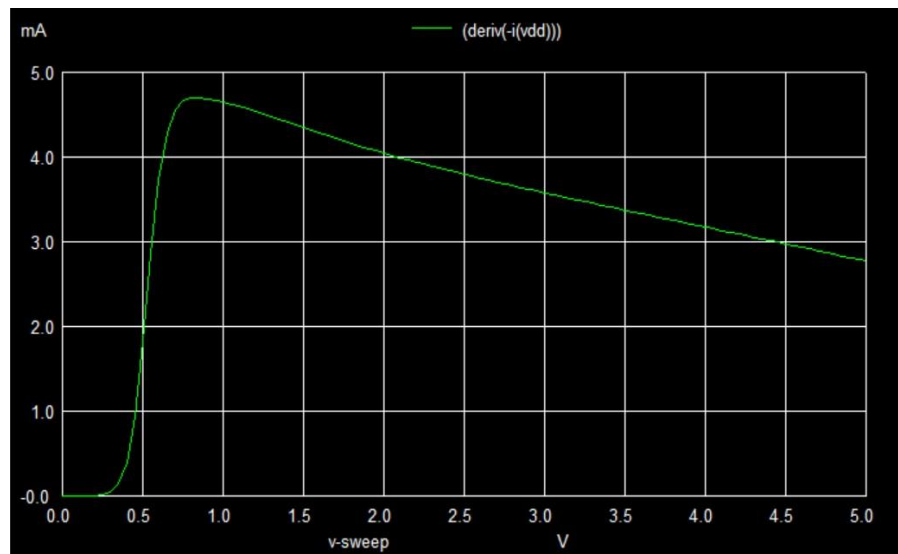


Figure 1: Transconductance as a function of gate voltage applied

The maximum value of transconductance (g_m) is **4.695mS** at $V_{GS} = 0.83V$.

SPICE code:

```
*Transconductance of a n-MOSFET
.include tsmc.txt
M1 2 1 0 0 CMOSN W=10u L=180u
VDD 2 0 DC 5v
VGG 1 0 DC 3.84V
* defining the run-time control functions
dc VGG 0 5 0.05
.control
run
plot(deriv(-I (vdd )))
.endc
.end
```

Question 1 (b)

The corresponding V_{GS} value is **0.83V**.

Question 1 (c)

We get the following curve of output resistance at $V_{GS} = 0.83V$.

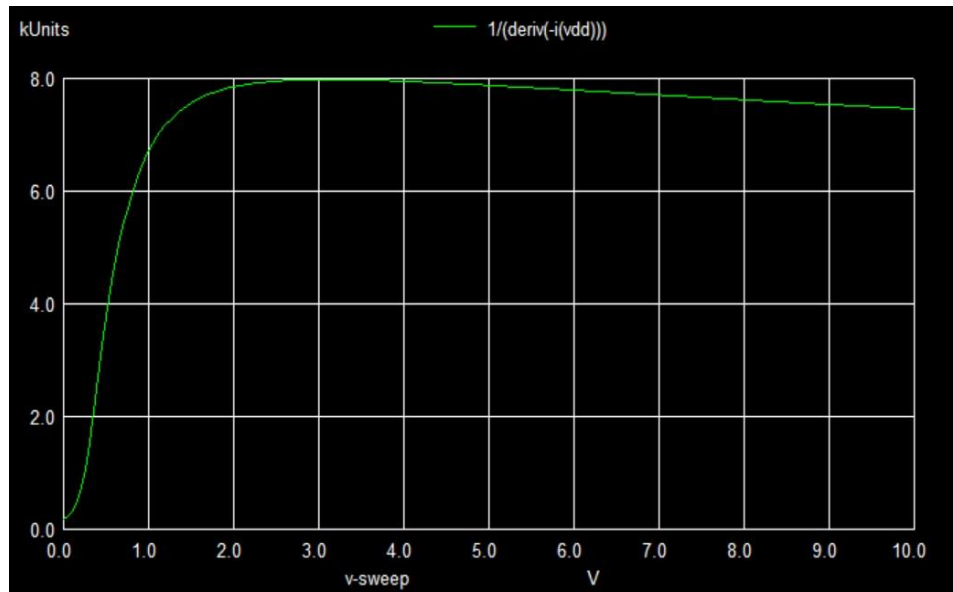


Figure 2: Output resistance as a function of drain voltage applied

The value of **output resistance** at $V_{DS}=5V$ is **7.878k Ω** .

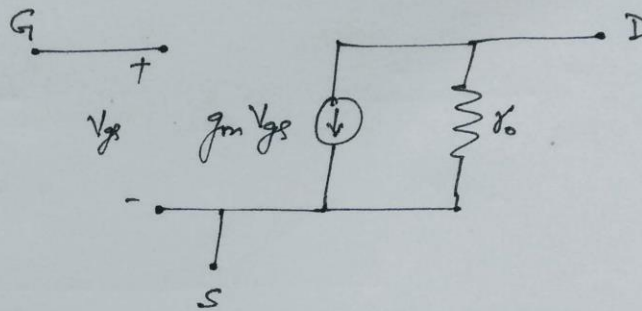
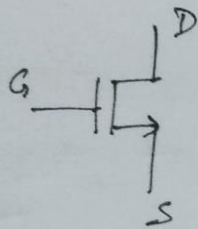
SPICE code:

```
*Output conductance of a n-MOSFET
.include tsmc.txt
M1 2 1 0 0 CMOSN W=10u L=0.18u
VDD 2 0 DC 5V
VGG 1 0 DC 0.83V
* defining the run-time control functions
dc VDD 0 10 0.05
.control
run
* plotting input and output voltages
plot 1/(deriv(-I (vdd )))
.endc
.end
```

Question 1 (d)

Qus. 1 (d)

Small Signal equivalent of the MOSFET \rightarrow



$$V_{DS} = 3V$$

$$V_{gs} = 0.83V$$

[Q1(b)]

$$g_m = 4.695 \text{ mS}$$

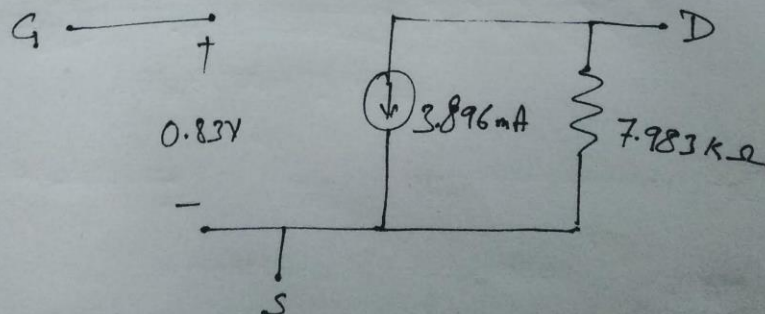
[Q1(a)]

$$r_o = 7.983 \text{ k}\Omega$$

[Q1(c)]

$$V_{TH} = 0.3694V$$

[TSMC model]



Question 1 (e)

c.

For DC biasing

$$V_{GG} = V_{GS}$$

KVL in output loop

$$V_{DD} = V_{DS} + I_D R_D$$

Small signal analysis

$$v_{gs} = v_i$$

$$v_o = -g_m v_{gs} (r_o \parallel R_D)$$

$$A_{ve} = \frac{v_o}{v_i} = -g_m (r_o \parallel R_D)$$

given $A_{ve} = 2$

$$g_m \frac{r_o R_D}{r_o + R_D} = 2$$

$$R_D (g_m r_o - 2) = 2 r_o$$

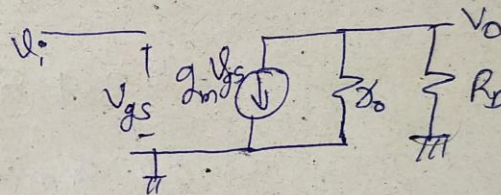
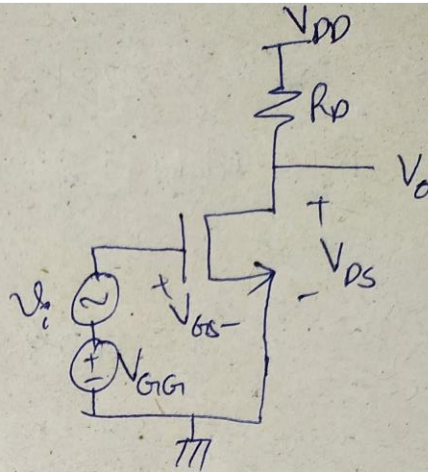
$$R_D = \frac{2 r_o}{g_m r_o - 2}$$

we have

$$g_m = k' (V_{GS} - V_T)$$

$$k' = \frac{g_m}{(V_{GS} - V_T)}$$

and $I_D = \frac{k'}{2} (V_{GS} - V_T)^2$



From simulations we have

$$g_m = 4.695 \text{ mS}$$

$$V_{DS} = 5 \text{ V} \quad V_T = 0.3694 \text{ V}$$

$$r_o = 7.878 \text{ k}\Omega$$

$$V_{GS} = 0.83 \text{ V}$$

$$s_1 \quad R_D = \frac{2r_o}{g_m r_o + 2} = \frac{2(7.878) \text{ k}}{(4.695 \text{ m})(7.878 \text{ k}) + 2}$$

$$R_D = 450.33 \Omega$$

$$k' = \frac{g_m}{(V_{GS} - V_T)} = \frac{4.695 \text{ m}}{(0.83 - 0.3694)} = 10.19 \text{ m}$$

$$I_D = \frac{k'}{2} (V_{GS} - V_T)^2 = \frac{10.19 \text{ m}}{2} = 1.08 \text{ mA}$$

$$V_{DD} = V_{DS} + I_D R_D = 5 + (1.08 \text{ mA})(0.45033 \text{ k}) = 5.4863 \text{ V}$$

$$V_{GG} = V_{GS} = 0.83 \text{ V}$$

From small signal equivalent circuit

$$\text{output impedance with } R_D = (r_o \parallel R_D) = (7.878 \text{ k}) \parallel 450.33 \Omega = 425.9 \Omega$$

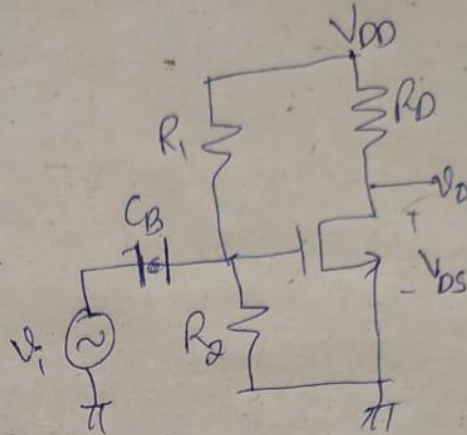
$$\text{without } R_D = r_o = 7.878 \text{ k}\Omega$$

Question 1 (f)

$$V_{DS} = 3V$$

$$V_{DD} = 5V$$

$$V_{GS} = 0.83V$$



$$V_{GG} = V_{GS} = \frac{R_2}{R_1 + R_2} V_{DD}$$

$$R_2(V_{DD} - V_{GS}) = R_1 V_{GS}$$

$$R_1 = \frac{V_{DD} - V_{GS}}{V_{GS}} R_2$$

$$= \frac{5 - 0.83}{0.83} R_2$$

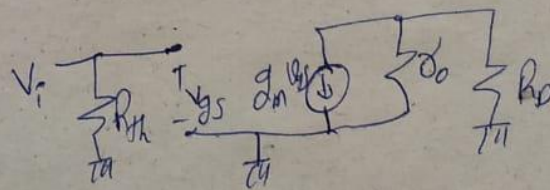
$$R_{Th} = (R_1 \parallel R_2)$$

$$R_1 = 5.024 R_2$$

So, for $R_2 = 10k\Omega$ $R_1 = 50.24k\Omega$

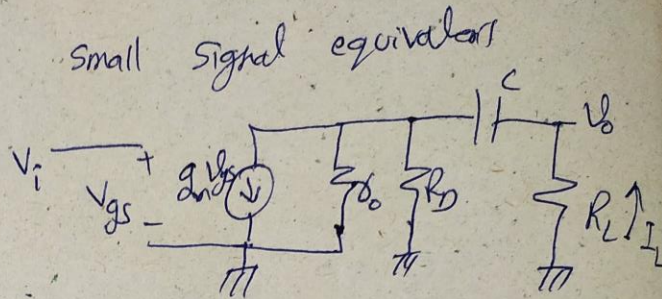
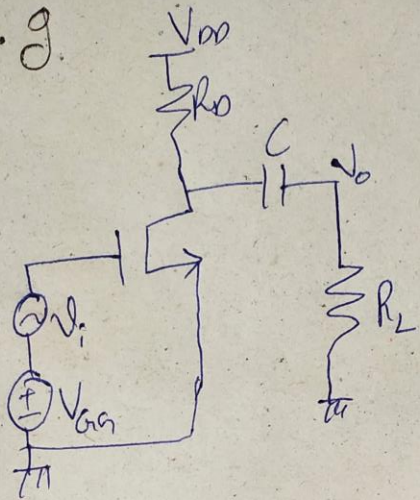
$$V_{RD} = V_{DD} - V_{DS} = 5 - 3$$

$$V_{RD} = 2V$$



Question 1 (g)

1. g



Using current division

$$i_{L'} = \frac{(r_o \parallel R_C) g_m v_{gs}}{(r_o \parallel R_C) + R_L + \frac{1}{sC}}$$

$$v_o = -R_L i_{L'}$$

$$v_o = \frac{-R_L (r_o \parallel R_C) sC}{sC((r_o \parallel R_C) + R_L) + 1} g_m v_{gs}$$

$$A_v = \frac{v_o}{v_i} = \frac{-R_L (r_o \parallel R_C) sC}{sC((r_o \parallel R_C) + R_L) + 1} g_m \quad (v_{gs} = v_i)$$

We have $r_o = 7.878 \text{ k}\Omega$

$$R_D = 404.13 \Omega$$

$$g_m = 4.695 \text{ mS}$$

$$R_L = 10 \text{ k}\Omega$$

$$C = 1 \mu\text{F}$$

$$r_o \parallel R_D = \frac{r_o R_D}{r_o + R_D} = 425.98 \Omega$$

$$A_{v_e} = \frac{-10 \text{ k} (0.42598 \text{ k}) 1 \mu\text{S}}{5 \text{ M} [0.42598 \text{ k} + 10 \text{ k}] + 1} g_m$$

$$= \frac{-4.259 \text{ S}}{5 [10.4259 \text{ M}] + 1} [4.695 \text{ m}]$$

$$|A_{v_e}| = \frac{+19.99 \omega \times 10^3}{\sqrt{1 + (10.4259 \text{ m} \omega)^2}}$$

at $f = 1 \text{ MHz}$ $\omega = 2\pi f = 2\pi \times 1 \text{ M rad/s}$

$$(A_{v_e})_{f=1\text{M}} = \frac{125.6 \times 10^3}{65507.8617}$$

$$= 1.917$$

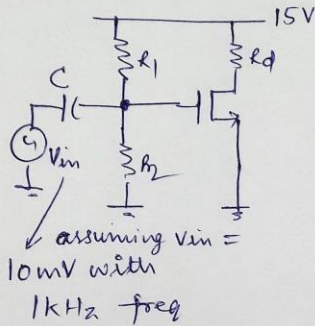
at $f = 1 \text{ mHz}$ $\omega = 2\pi f = 2\pi \text{ m rad/s}$

$$(A_{v_e})_{f=1\text{m}} = \frac{125.6 \times 10^6}{1} = 125.6 \times 10^6$$

Question 2 (a)

Ques 2(a) Design of CS amplifier

let us use potential divider ckt for biasing the ckt.



Given $k' = \mu_n \times \frac{W}{L} = 2 \text{ mA/V}^2$
 $V_t = 0.01 \text{ V}$

lets start by assuming that the drain current is 1 mA .

$$I_m = \frac{1}{2} \times 2 \text{ mA} (V_{GS} - V_t)^2$$

$$V_{GS}^2 = 1$$

$$V_{GS} = 1 + V_{th}$$

$$V_{GS} = 1 + 0.4$$

$$= 1.4 \text{ V}$$

($V_{th} \approx 0.4 \text{ V}$
from model file)

$$\boxed{V_G = 1.4 \text{ V}} \quad (\text{as source is grounded})$$

to get 1.4 V at gate

$$\Rightarrow \frac{15 R_2}{R_1 + R_2} = 1.4 \text{ V} \Rightarrow \frac{R_1}{R_2} = \frac{68}{7}$$

$$\boxed{R_2 = 7 \text{ K}} \\ \boxed{R_1 = 68 \text{ K}}$$

find g_m

$$g_m = \frac{2 I_D}{V_{GS}}$$

$$= \frac{2 \times 1 \text{ mA}}{1}$$

$$\boxed{g_m = 2 \text{ mA/V}}$$

$$\text{gain} \Rightarrow g_m R_D$$

$$\approx 2 \text{ mA/V}$$

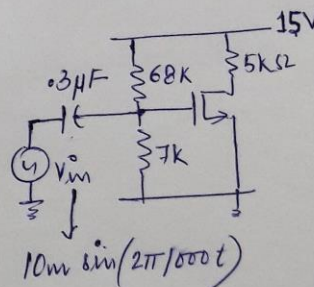
$$2 \text{ mA/V} = 10 \quad (|A_v| = 10)$$

$$\boxed{R_D = \frac{10}{2 \text{ mA}} = 5 \text{ K}\Omega}$$

$$r_o = \frac{1}{\lambda I_D} = \frac{1}{0.01 \times 1 \text{ mA}} = 100 \text{ K}\Omega$$

Here in our expression of gain we neglected r_o , the reason is $R_D = 5 \text{ K}\Omega$ is much less than $r_o = 100 \text{ K}\Omega$.

$$R_o = r_o \parallel R_D \approx R_D$$

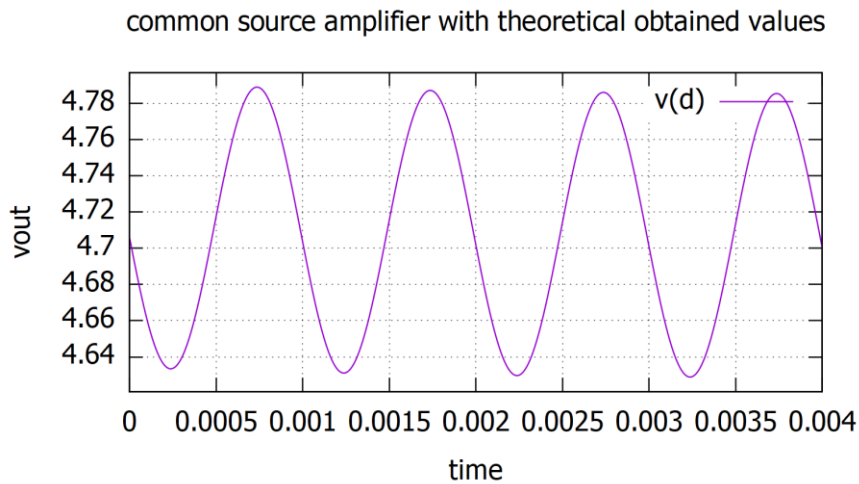


finding C.

$$7 \text{ K} \parallel 68 \text{ K} \geq \frac{10}{2 \pi f C}$$

$$C \geq 0.25 \mu\text{F}$$

$$\boxed{C = 0.3 \mu\text{F}}$$



SPICE Code:

*Common source amplifier with theoretical obtained values

```
.include tsmc.txt
```

```
m1 d g 0 0 CMOSN w=5u l=.18u
```

```
rd d 1 5k
```

```
vdd 1 0 15
```

```
r1 g 1 68k
```

```
r2 g 0 7k
```

```
cb1 g 2 .3u
```

```
vin 2 0 sin (0 10m 1k 0 0 )
```

```
.tran 1u 4m 0
```

```
.control
```

```
run
```

```
plot v(d) xlabel time ylabel vout
```

```
plot v(2) xlabel time ylabel vin
```

```
.endc
```

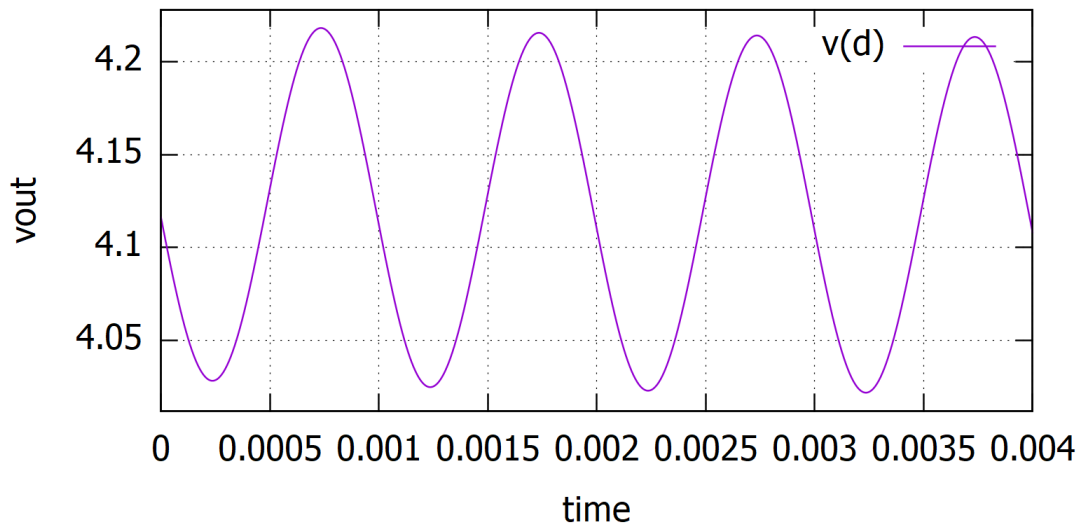
```
.end
```

*In this case the gain = 8, the reason is that

*the modal file is advanced that's why we get little bit deviation

*hence we need to change resistance values.

common source amplifier with gain 10 after changing r2 and rd



SPICE Code:

*Common source amplifier with gain 10 after changing r2 and rd

.include tsmc.txt

m1 d g 0 0 CMOSN w=5u l=.18u

rd d 1 6.5k

vdd 1 0 15

r1 g 1 68k

r2 g 0 6.2k

cb1 g 2 .3u

vin 2 0 sin (0 10m 1k 0 0)

.tran 1u 4m 0

.control

run

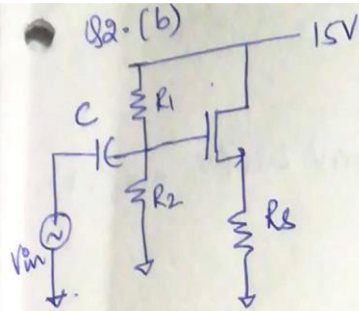
plot v(d) xlabel time ylabel vout

plot v(2) xlabel time ylabel vin

.endc

.end

Question 2 (b)



$$K' = 2 \text{ mA/V}^2$$

$$\lambda = 0.01 \text{ V}^{-1}$$

Let's assume drain current = 1 mA

$$I_{DS} = \frac{1}{2} \times 2 \times 10^{-3} \times (V_{GS} - V_{th})^2 = 1 \times 10^{-3}$$

$$V_{GS} - V_{th} = 1$$

$$V_{GS} = 1 + 0.4$$

$$V_{GS} = 1.4$$

$$(V_{th} = 0.4 \text{ V})$$

$$V_S = I_{DS} \times R_S = 10^{-3} \times R_S$$

$$V_G = V_S + 1.4 = 10^{-3} \times R_S + 1.4$$

$$\frac{15R_2}{R_1 + R_2} = V_G = 10^{-3} R_S + 1.4 \quad \text{--- (1)}$$

$$g_m = \frac{2I_{DS}}{(V_{GS} - V_{th})} = \frac{2 \times 1 \times 10^{-3}}{1} = 2 \text{ mA/V}$$

$$\text{gain} = \frac{g_m r_o R_S}{g_m r_o R_S + r_o + R_S} = \frac{g_m R_S}{g_m R_S + 1} \approx \frac{g_m}{g_m + \frac{1}{r_o \parallel R_S}} \approx \frac{g_m}{g_m + \frac{1}{R_S}}$$

$$0.9 = \frac{2 \times 10^{-3}}{2 \times 10^{-3} + \frac{1}{R_S}}, \quad \frac{1}{R_S} = \frac{2}{9} \times 10^{-3}$$

$$\Rightarrow R_S = \frac{9}{2} \times 10^3 = 4.5 \text{ k}\Omega$$

$R_S \gg r_o$ hence neglecting r_o is justified.

$$\begin{aligned} r_o &= \frac{1}{\lambda I_D} \\ &= \frac{1}{0.01 \times 10^{-3}} \\ &= 10^5 \Omega \\ &\downarrow \\ &\text{very small} \\ &\text{hence neglected} \end{aligned}$$

$$\text{In (1), } \frac{15R_2}{R_1 + R_2} = 10^{-3} \times 4.5 \times 10^3 + 1.4 = 5.9$$

$$15R_2 = 5.9R_1 + 5.9R_2 \Rightarrow 9.1R_2 = 5.9R_1 \Rightarrow \frac{R_2}{R_1} = \frac{59}{91}$$

$$\Rightarrow R_1 = 91 \text{ k}\Omega \text{ and } R_2 = 59 \text{ k}\Omega$$

for capacitance,

$$\frac{R_1 R_2}{R_1 + R_2} \geq \frac{10}{2\pi f C}$$

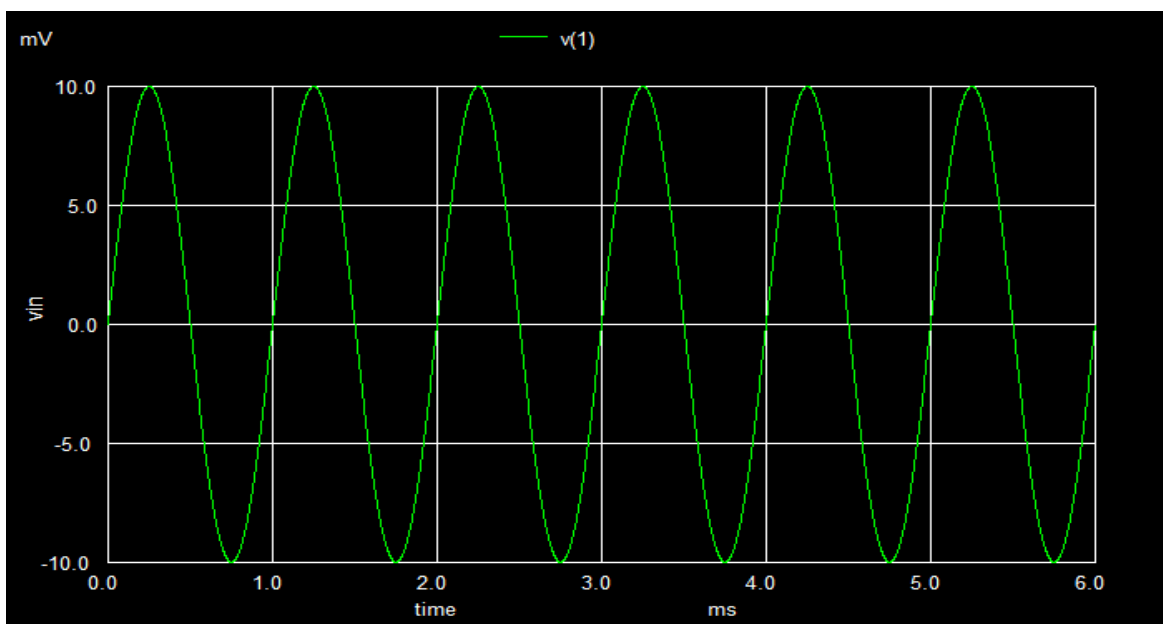
Considered input signal as $V_m = 10\text{mV} \sin(2\pi \times 1\text{kHz} \times t)$
 $f = 1\text{kHz}$

$$C \geq \frac{10 \times (59 + 91)}{2\pi \times 10^3 \times 59 \times 91 \times 10^3}$$

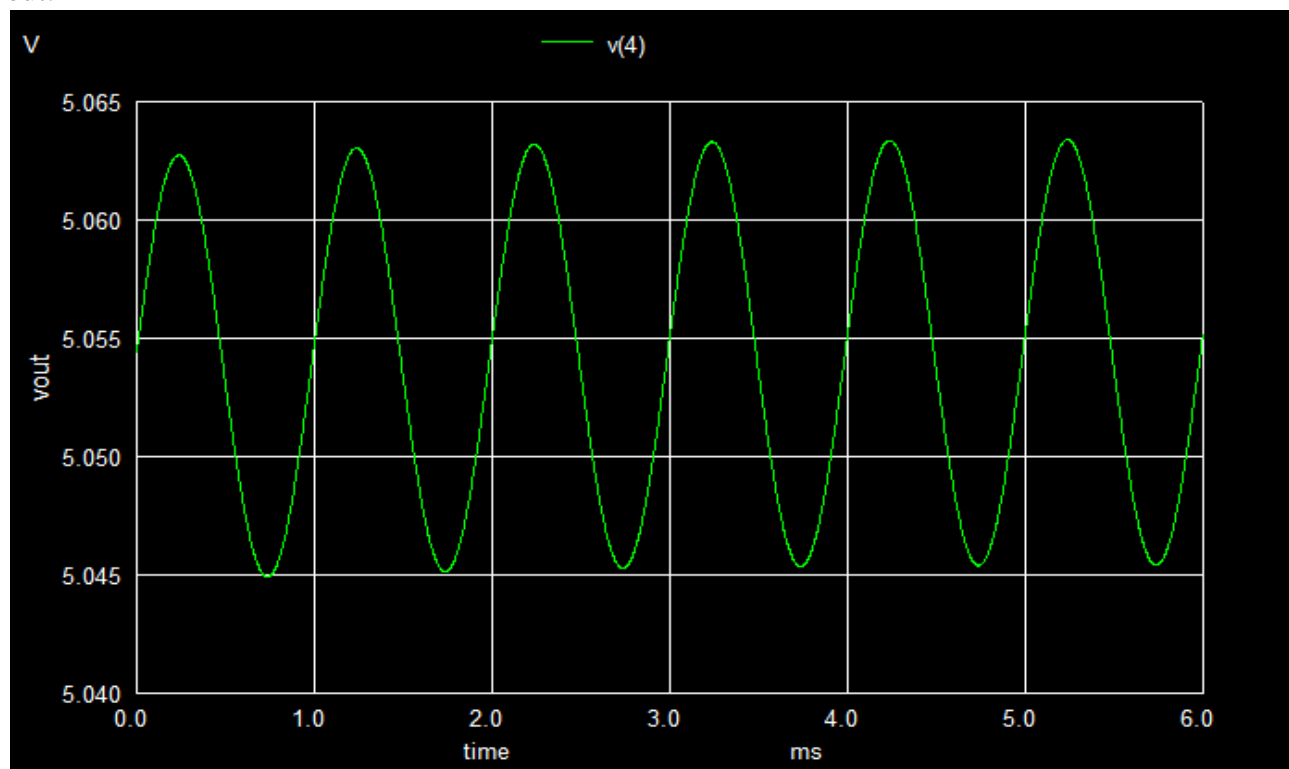
$$\Rightarrow C \geq 0.044 \mu\text{F}$$

$$\text{Take } \underline{C = 0.05 \mu\text{F}}$$

Vin:



Vout:



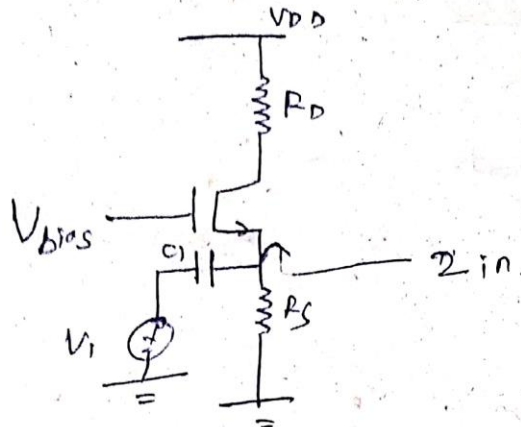
SPICE Code:

```
.include tsmc.txt
vdd 3 0 15
c1 2 1 0.05u
r2 2 0 59k
r1 2 3 91k
m1 3 2 4 4 cmosn w=5u l=0.18u
rs 4 0 4.5k
vin 1 0 sin (0 10m 1k 0 0)
.tran 0.002m 6m
.control
run
plot v(4) xlabel time ylabel vout
plot v(1) xlabel time ylabel vin
.endc
.end
```


Question 2 (c)

2-c

Common gate - Amplifier



As this is a design problem
let us consider $I_D = 1\text{mA}$

$$V_{GS} - V_T = 0.2\text{V}$$

$$g_m = \frac{2I_D}{V_{GS} - V_T} = \frac{2 \times 1\text{mA}}{0.2} = 10\text{mS}$$

$$r_o = \frac{1}{\lambda I_D} = \frac{1}{0.01 \times 1\text{mA}} = 100\text{k}\Omega$$

→ Looking in to the source

$$Z = \frac{1}{g_m} \parallel r_o \quad \left| \quad \frac{1}{g_m} = 0.1\text{k}\Omega \right.$$

$$Z \approx 0.1\text{k}\Omega$$

We have considered

$$V_{GS} - V_T = 0.2$$

$$V_G - V_S - V_{TH} = 0.2$$

$$V_G = 0.2 + 2.5 + V_{TH}$$

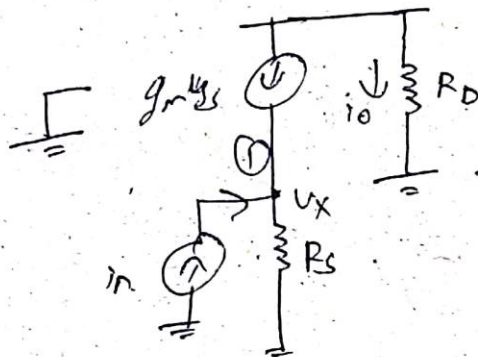
$$= 3.2\text{V}$$

Assumed

$$V_{TH} = 0.5$$

$$V_S = 2.5$$

small-signal - Analysis.



$$i_o = -g_m v_{be}$$

$$i_o = -g_m (0 - v_x)$$

$$i_o = g_m v_x$$

$$v_x = \frac{i_o}{g_m}$$

ECL - At node - I

$$-i_{in} + \frac{i_o}{R_s g_m} + i_o = 0$$

$$i_{in} = i_o \left(\frac{1}{g_m R_s} + 1 \right)$$

$$\frac{i_o}{i_{in}} = \frac{1}{1 + \frac{1}{g_m R_s}}$$

Given

$$0.8 = \frac{g_m R_s}{1 + g_m R_s}$$

$$0.8 = \frac{10m \times R_s}{1 + 8.10m R_s}$$

$$R_S = \frac{0.8}{2} K$$

$$= 0.4 K$$

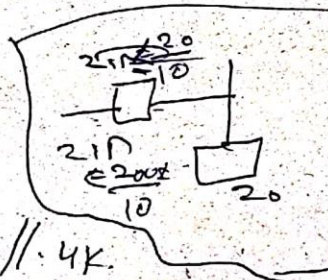
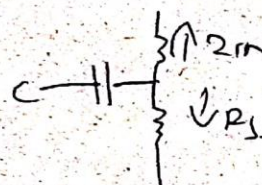
$$V_A = 0.2 + V_S + V_{R_S}$$

$$= 0.2 + 0.4 + 0.5$$

$$= 1.1$$

$$V_S = I_S R_S$$

$$= I_D R_S$$



$$\frac{0.08K}{0.8K} = \frac{1}{2 \pi \times f_c \times C}$$

$$= 0.8K \parallel 0.08K$$

$$C = \frac{1}{2 \pi \times 14 \times 1000 \times 0.006 \times 10^3}$$

$$= 26 \text{ nF}$$

$$C = 26 \text{ nF}$$

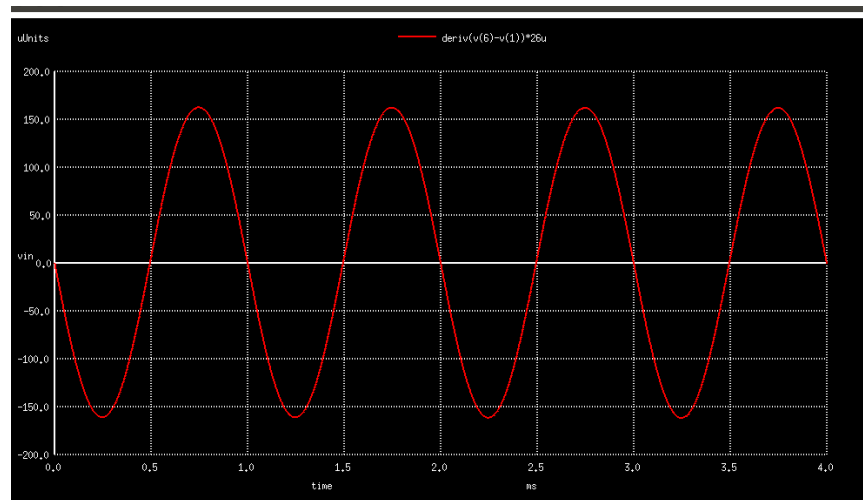
$$\rightarrow Z_o = r_o + R_S (1 + g_m r_o)$$

$$= 100K + 5K (1 + 10mS \times 100K)$$

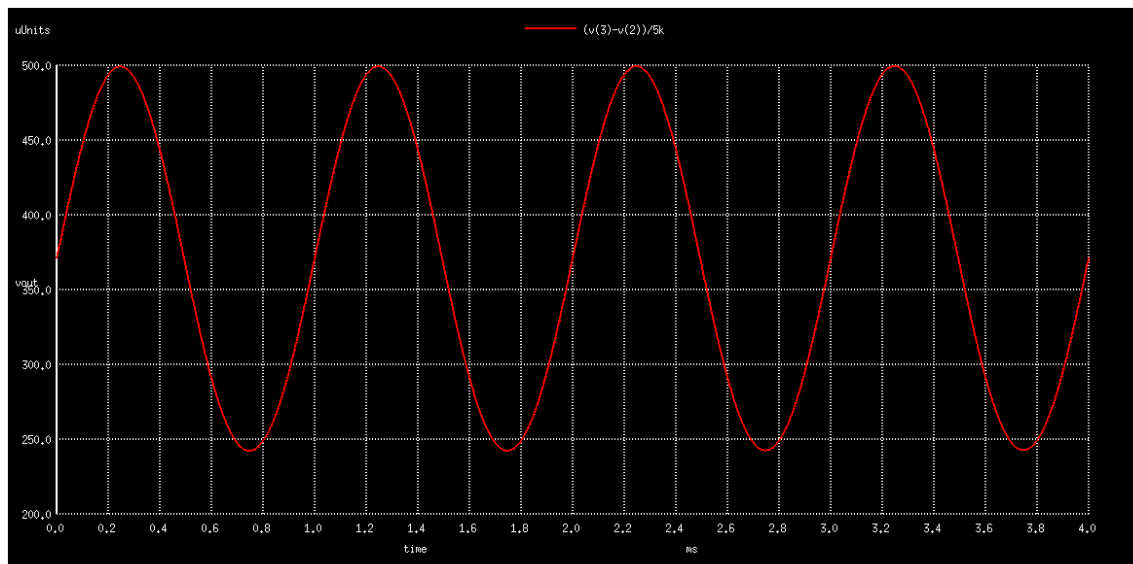
$$= 5.105 \times 10^6 \Omega$$

With designed R_S we can't get the required current gain, so we tweak R_S to 1.5K to get required A_I .

Iin:



Iout:



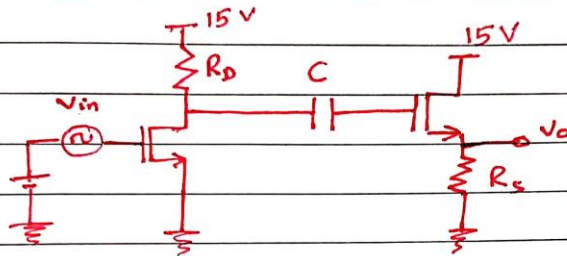
$$\text{Current Gain} = (499-371)/162 = 0.79$$

SPICE Code:

```
*common gate
.include tsmc.txt
M1 2 4 1 1 CMOSN w=5u l=.18u
VDD 3 0 DC 15v
c1 6 1 26u
VGG 4 0 DC 1.1
rs 1 0 1.5k
rd 3 2 5K
vn 0 6 sin (0 50m 1k 0 0)
.tran 1u 4m 0
.control
run
plot (v(3)-v(2))/5k xlabel time ylabel vout
plot deriv(v(6)-v(1))*26u xlabel time ylabel vin
.endc
.end
```

Question 2 (d)

Q2 d)



Coupling Capacitor
needs to be very
high.

$$\begin{aligned}\text{Overall gain} &= \text{gain of CS} \times \text{gain of CD} \\ &= -10 \times 0.9 \\ &= -9\end{aligned}$$

$$\begin{aligned}\text{Output impedance} &= (Z_o)_{\text{common drain}} \\ &= \frac{1}{g_m + \frac{1}{r_o \parallel R_S}} \approx \frac{1}{g_m} = 202 \, \Omega \\ &\quad \rightarrow = 183 \, \Omega\end{aligned}$$