CSE 3003 –E11+12+13-Operating Systems Concepts 9th Ed by Silberschatz, Galvin and Gagne Study Guide

UNIT-I

Basic of Operating System and Its Structures Introduction: Computer System Organization-Architecture-Structure-Operations. Management: Process-Memory-Storage. Structures: Services-System Interface- System Calls- System Program-Design-structure

Ch.1 – Introduction- Computer System Organization-Architecture

- An OS is a program that acts as an intermediary between a user of a computer and the computer hardware
- Goals: Execute user programs, make the comp. system easy to use, utilize hardware efficiently
- Computer system: Hardware \leftrightarrow OS \leftrightarrow Applications \leftrightarrow Users (\leftrightarrow = 'uses')
- OS is:
 - Resource allocator: decides between conflicting requests for efficient and fair resource use
 - Control program: controls execution of programs to prevent errors and improper use of computer
- Kernel: the one program running at all times on the computer
- <u>Bootstrap program:</u> loaded at power-up or reboot
 - Stored in ROM or EPROM (known as <u>firmware</u>), Initializes all aspects of system, loads OS kernel and starts execution
- I/O and CPU can execute concurrently
- Device controllers inform CPU that it is finished w/ operation by causing an interrupt
 - Interrupt transfers control to the interrupt service routine generally, through the <u>interrupt vector</u>, which contains the addresses of all the service routines
 - Incoming interrupts are disabled while another interrupt is being processed
 - Trap is a software generated interrupt caused by error or user request
 - OS determines which type of interrupt has occurred by <u>polling</u> or the <u>vectored interrupt</u> system
- System call: request to the operating system to allow user to wait for I/O completion
- Device-status table: contains entry for each I/O device indicating its type, address, and state
 - OS indexes into the I/O device table to determine device status and to modify the table entry to include interrupt
- Storage structure:
 - Main memory random access, volatile
 - Secondary storage extension of main memory That provides large non-volatile storage
 - Disk divided into <u>tracks</u> which are subdivided into <u>sectors</u>. <u>Disk controller</u> determines logical interaction between the device and the computer.
- <u>Caching</u> copying information into faster storage system
- <u>Multiprocessor Systems:</u> Increased throughput, economy o scale, increased reliability
 - Can be asymmetric or symmetric
 - <u>Clustered systems</u> Linked multiprocessor systems
- Multiprogramming Provides efficiency via job schedulin
 - When OS has to wait (ex: for I/O), switches to another
- <u>Timesharing</u> CPU switches jobs so frequently that each user



- can interact with each job while it is running (<u>interactive computing</u>)
- <u>Dual-mode</u> operation allows OS to protect itself and other system components <u>User mode</u> and kernel mode
 - Some instructions are only executable in kernel mode, these are <u>privileged</u>
- Single-threaded processes have one <u>program counter</u>, multi-threaded processes have one PC per thread
- <u>Protection</u> mechanism for controlling access of processes or users to resources defined by the OS
- Security defense of a system against attacks
- <u>User IDs (UID)</u>, one per user, and <u>Group IDs</u>, determine which users and groups of users have which privileges

Ch.2 – Management:Process-Memory-Storage. Structures:Services-System Interface- System Calls- System Program-Design-structure

User Interface (UI) – Can be Command-Line (CLI) or Graphics User Interface (GUI) or Batch

- $^{\circ}$ These allow for the user to interact with the system services via system calls (typically written in C/C++)
- Other system services that a helpful to the <u>user</u> include: program execution, I/O operations, file-system manipulation, communications, and error detection
- Services that exist to ensure efficient OS operation are: resource allocation, accounting, protection and security
- Most system calls are accessed by <u>Application Program Interface (API)</u> such as Win32, POSIX, Java
- Usually there is a number associated with each system call
 - System call interface maintains a table indexed according to these numbers
- Parameters may need to be passed to the OS during a system call, may be done by:
 - Passing in <u>registers</u>, address of parameter stored in a <u>block</u>, <u>pushed</u> onto the stack by the program and popped off by the OS
 - Block and stack methods do not limit the number or length of parameters being passed
- <u>Process control</u> system calls include: end, abort, load, execute, create/terminate process, wait, allocate/free memory
- <u>File management</u> system calls include: create/delete file, open/close file, read, write, get/set attributes
- <u>Device management</u> system calls: request/release device, read, write, logically attach/detach devices
- user application
 open ()

 kernel mode

 system call interface

 open ()

 Implementation of open () system call

 i system call
- <u>Information maintenance</u> system calls: get/set time, get/set system data, get/set process/file/device attributes
- <u>Communications</u> system calls: create/delete communication connection, send/receive, transfer status information
- OS Layered approach:

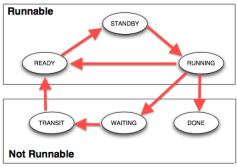
- The operating system is divided into a number of layers (levels), each built on top of lower layers. The bottom layer (layer 0), is the hardware; the highest (layer N) is the user interface
- With modularity, layers are selected such that each uses functions (operations) and services of only lower-level layers
- <u>Virtual machine</u>: uses layered approach, treats hardware and the OS kernel as though they were all hardware.
 - Host creates the illusion that a process has its own processor and own virtual memory
 - Each guest provided with a 'virtual' copy of the underlying computer
- Application failures can generate <u>core dump</u> file capturing memory of the process
- Operating system failure can generate <u>crash dump</u> file containing kernel memory.

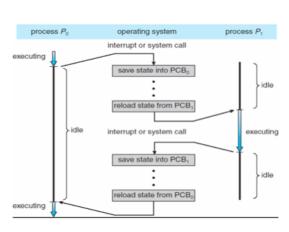
UNIT-II

Process and Threads Introduction to Process – Scheduling – Operations-Interprocess Communication. Synchronization: Critical Section-Hardware-Mutex- Semaphore – Monitors. Threads: Multithreading Models-

Ch.3 – Processes

- Process contains a program counter, stack, and data section.
 - <u>Text section</u>: program code itself
 - <u>Stack</u>: temporary data (function parameters, return addresses, local variables)
 - Data section: global variables
 - <u>Heap</u>: contains memory dynamically allocated during run-time
- <u>Process Control Block (PCB)</u>: contains information associated with each process: process state, PC, CPU registers, scheduling information, accounting information, I/O status information
- Types of processes:
 - <u>I/O Bound</u>: spends more time doing I/O than computations, many short CPU bursts
 - <u>CPU Bound</u>: spends more time doing computations, few very long CPU bursts
- When CPU switches to another process, the system must save the state of the old process (to PCB) and load the saved state (from PCB) for the new process via a <u>context</u> switch
 - Time of a context switch is dependent on hardware
- Parent processes create children processes (form a tree)
 - PID allows for process management
 - Parents and children can share all/some/none resources
 - Parents can execute concurrently with children or wait until children terminate
 - <u>fork()</u> system call creates new process





- <u>exec()</u> system call used after a fork to replace the processes' memory space with a new program
- Cooperating processes need <u>interprocess communication (IPC)</u>: shared memory or message passing
- Message passing may be blocking or non-blocking
 - Blocking is considered synchronous
 - Blocking send has the sender block until the message is received
 - <u>Blocking receive</u> has the receiver block until a message is available
 - Non-blocking is considered asynchronous
 - Non-blocking send has the sender send the message and continue
 - Non-blocking receive has the receiver receive a valid message or null

Ch.4 – Threads

- <u>Threads</u> are fundamental unit of CPU utilization that forms the basis of multi-threaded computer systems
- Process creation is heavy-weight while thread creation is light-weight
 - Can simplify code and increase efficiency
- Kernels are generally multi-threaded
- Multi-threading models include: Many-to-One, One-to-One, Many-to-Many
 - Many-to-One: Many user-level threads mapped to single kernel thread
 - One-to-One: Each user-level thread maps to kernel thread
 - Many-to-Many: Many user-level threads mapped to many kernel threads
- Thread library provides programmer with API for creating and managing threads
- Issues include: thread cancellation, signal handling (synchronous/asynchronous), handling thread-specific data, and scheduler activations.
 - Cancellation:
 - Asynchronous cancellation terminates the target thread immediately
 - Deferred cancellation allows the target thread to periodically check if it should be canceled
 - Signal handler processes signals generated by a particular event, delivered to a process, handled
 - <u>Scheduler</u> activations provide <u>upcalls</u> a communication mechanism from the kernel to the thread library.
 - Allows application to maintain the correct number of kernel threads

Ch.5 – Process Synchronization

- <u>Race Condition</u>: several processes access and manipulate the same data concurrently, outcome depends on which order each access takes place.
- Each process has <u>critical section</u> of code, where it is manipulating data
 - To solve critical section <u>problem</u> each process must ask permission to enter critical section in <u>entry section</u>, follow critical section with <u>exit section</u> and then execute the <u>remainder</u> section
 - Especially difficult to solve this problem in preemptive kernels
- Peterson's Solution: solution for two processes
 - Two processes share two variables: int turn and Boolean flag[2]
 - **turn:** whose turn it is to enter the critical section
 - **flag:** indication of whether or not a process is ready to enter critical section
 - flag[i] = true indicates that process P_i is ready

```
\label{eq:continuous_process} \begin{split} &\circ & Algorithm \ for \ process \ P_i: \\ & do \ \{ \\ & flag[i] = TRUE; \\ & turn = j; \\ & while \ (flag[j] \ \&\& \ turn == j) \\ & critical \ section \\ & flag[i] = FALSE; \\ & remainder \ section \\ \} \ while \ (TRUE); \end{split}
```

• Modern machines provide atomic hardware instructions: <u>Atomic</u> = non-interruptable

• Solution using Locks:

• Solution using <u>Test-And-Set</u>: Shared boolean variable lock, initialized to FALSE

```
boolean TestAndSet (boolean *target){
            boolean rv = *target;
            *target = TRUE;"
            return rv:
}
```

• Solution using <u>Swap</u>: Shared bool variable lock initialized to FALSE; Each process has local bool variable key

- <u>Semaphore</u>: Synchronization tool that does not require busy waiting
 - Standard operations: wait() and signal() ← these are the only operations that can access semaphore S
 - Can have counting (unrestricted range) and binary (0 or 1) semaphores
- <u>Deadlock</u>: Two or more processes are waiting indefinitely for an event that can be caused by only one of the waiting processes (most OSes do not prevent or deal with deadlocks)
 - Can cause <u>starvation</u> and <u>priority inversion</u> (lower priority process holds lock needed by higher-priority process)

Ch.5 – Process Synchronization Continued

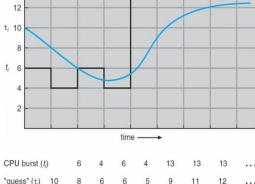
- Other synchronization problems include Bounded-Buffer Problem and Readers-Writers Problem
- <u>Monitor</u> is a high-level abstraction that provides a convenient and effective mechanism for process synchronization
 - Only one process may be active within the monitor at a time
 - Can utilize <u>condition</u> variables to suspend a resume processes (ex: condition x, y;)
 - x.wait() a process that invokes the operation is suspended until x.signal()
 - x.signal() resumes one of processes (if any) that invoked x.wait()
 - Can be implemented with semaphores

UNIT-III

Processor Scheduling and Deadlocks CPU Scheduling: Scheduling Criteria- Algorithms-Evaluation. Deadlocks: Principles- Prevention- Avoidance-Detection- Recovery.

Ch.6 – CPU Scheduling

- Process execution consists of a cycle of CPU execution and I/O wait
- CPU scheduling decisions take place when a process:
 - Switches from running to waiting (nonpreemptive)
 - Switches from running to ready (preemptive)
 - Switches from waiting to ready (preemptive)
 - Terminates (nonpreemptive)
- The <u>dispatcher</u> module gives control of the CPU to the process selected by the short-term scheduler
 - <u>Dispatch latency</u>- the time it takes for the dispatcher to stop one process and start another
- Scheduling algorithms are chosen based on optimization criteria (ex: throughput, turnaround time, etc.)
 - FCFS, SJF, Shortest-Remaining-Time-First (preemptive SJF), Round Robin, Priority
- Determining length of next CPU burst: <u>Exponential</u> Averaging:
 - 1. $t_n = \text{actual length of } n^{th} \text{ CPU burst}$
 - 2. τ_{n+1} = predicted value for the next CPU burst
 - 3. α , $0 \le \alpha \le 1$ (commonly α set to 1/2)
 - Define: $\tau_{n+1} = \alpha * t_n + (1-\alpha)\tau_n$
- <u>Priority Scheduling</u> can result in <u>starvation</u>, which can be solved by <u>aging</u> a process (as time progresses, increase the priority)
- In Round Robin, small time quantums can result in large amounts of context switches
 - Time quantum should be chosen so that 80% of processes have shorter burst times that the time quantum
- <u>Multilevel Queues</u> and <u>Multilevel Feedback Queues</u> have multiple process queues that have different priority levels
 - In the Feedback queue, priority is not fixed → Processes can be promoted and demoted to different queues
 - Feedback queues can have different scheduling algorithms at different levels
- Multiprocessor Scheduling is done in several different ways:



- <u>Asymmetric multiprocessing</u>: only one processor accesses system data structures → no need to data share
- <u>Symmetric multiprocessing</u>: each processor is self-scheduling (currently the most common method)
- <u>Processor affinity</u>: a process running on one processor is more likely to continue to run on the same processor (so that the processor's memory still contains data specific to that specific process)
- <u>Little's Formula</u> can help determine average wait time per process in any scheduling algorithm:
 - \circ $n = \lambda \times W$
 - n = avg queue length; W = avg waiting time in queue; λ = average arrival rate into queue
- Simulations are programmed models of a computer system with variable clocks
 - Used to gather statistics indicating algorithm performance
 - Running simulations is more accurate than queuing models (like Little's Law)
 - Although more accurate, high cost and high risk

Ch.7 – Deadlocks

- <u>Deadlock Characteristics</u>: deadlock can occur if these conditions hold simultaneously
 - Mutual Exclusion: only one process at a time can use a resource
 - Hold and Wait: process holding one resource is waiting to acquire resource held by another process
 - No Preemption: a resource can be released only be the process holding it after the process completed its task
 - \sim <u>Circular Wait</u>: set of waiting processes such that P_{n-1} is waiting for resource from P_n , and P_n is waiting for P_0
 - "Dining Philosophers" in deadlock

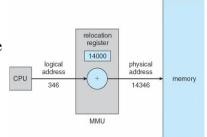
UNIT-IV

Memory and Storage Management

Main Memory: Swapping-Contiguous Memory Allocation – Segmentation – Paging. Virtual Memory: Demand Paging- Page Replacement Algorithm. Secondary Storage: Disk Scheduling-Disk Management- RAID

Ch.8 – Main Memory

- Cache sits between main memory and CPU registers
- Base and limit registers define logical address space usable by a process
- Compiled code addresses bind to relocatable addresses
 - Can happen at three different stages
 - Compile time: If memory location known a priori, absolute code can be generated
 - <u>Load time</u>: Must generate <u>relocatable code</u> if memory location not known at compile time
 - Execution time: Binding delayed until run time if the process can be moved during its execution
- <u>Memory-Management Unit (MMU)</u> device that maps virtual to physical address
- Simple scheme uses a <u>relocation register</u> which just adds a base value



to address

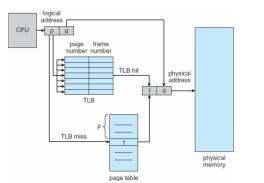
- Swapping allows total physical memory space of processes to exceed physical memory
 - Def: process swapped out temporarily to backing store then brought back in for continued execution
- Backing store: fast disk large enough to accommodate copes of all memory images
- Roll out, roll in: swapping variant for priority-based scheduling.
 - Lower priority process swapped out so that higher priority process can be loaded
- Solutions to **Dynamic Storage-Allocation Problem**:
 - First-fit: allocate the first hole that is big enough
 - <u>Best-fit</u>: allocate the smallest hole that is big enough (must search entire list) → smallest leftover hole
 - Worst-fit: allocate the largest hole (search entire list) \rightarrow largest leftover hole
- External Fragmentation: total memory space exists to satisfy request, but is not contiguous
 - Reduced by compaction: relocate free memory to be together in one block
 - Only possible if relocation is dynamic
- Internal Fragmentation: allocated memory may be slightly larger than requested memory
- Physical memory divided into fixed-sized <u>frames</u>: size is power of 2, between 512 bytes and 16 MB
- Logical memory divided into same sized blocks: pages
- <u>Page table</u> used to translate logical to physical addresses
 - Page number (p): used as an index into a page table
 - Page offset (d): combined with base address to define the physical memory address
- Free-frame list is maintained to keep track of which frames can be allocated

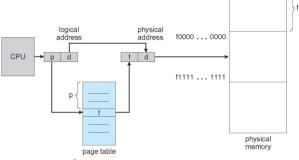
page number	page offset
p	d
m - n	n

For given logical address space 2^m and page size 2ⁿ

Ch.8 - Main Memory Continued

- <u>Transition Look-aside Buffer (TLB)</u> is a CPU cache that memory management hardware uses to improve virtual address translation speed
 - Typically small 64 to 1024 entries
 - On TLB miss, value loaded to TLB for faster access next time
 - TLB is associative searched in parallel

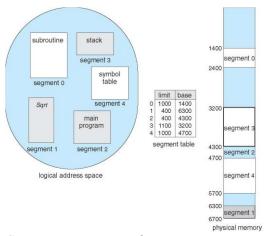


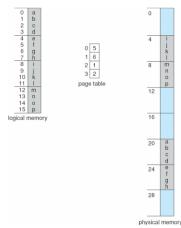


Paging with TLB

Paging without TLB

- Effective Access Time: EAT = $(1 + \varepsilon) \alpha + (2 + \varepsilon)(1 \alpha)$
 - \circ $\varepsilon = \text{time unit}, \ \alpha = \text{hit ratio}$
- Valid and invalid bits can be used to protect memory
 - "Valid" if the associated page is in the process' logical address space, so it is a legal page
- Can have multilevel page tables (paged page tables)
- <u>Hashed Page Tables</u>: virtual page number hashed into page table
 - Page table has chain of elements hashing to the same location
 - Each element has (1) virtual page number, (2) value of mapped page frame, (3) a pointer to the next element
 - Search through the chain for virtual page number
- Segment table maps two-dimensional physical addresses
 - Entries protected with valid bits and r/w/x privileges



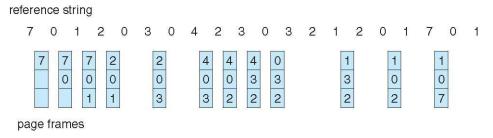


Segmentation example

Page table example

Ch.9 – Virtual Memory

- <u>Virtual memory</u>: separation of user logical memory and physical memory
 - Only part of program needs to be in memory for execution → logical address space > physical address space
 - Allows address spaces to be shared by multiple processes → less swapping
 - Allows pages to be shared during fork(), speeding process creation
- Page fault results from the first time there is a reference to a specific page \rightarrow traps the OS
 - Must decide to abort if the reference is invalid, or if the desired page is just not in memory yet
 - If the latter: get empty frame, swap page into frame, reset tables to indicate page now in memory, set validation bit, restart instruction that caused the page fault
 - If an instruction accesses multiple pages near each other → less "pain" because of <u>locality</u> of reference
- Demand Paging only brings a page into memory when it is needed → less I/O and memory needed
 - <u>Lazy swapper</u> never swaps a page into memory unless page will be needed
 - Could result in a lot of page-faults
 - ∘ Performance: EAT = [(1-p)*memory access + p*(page fault overhead + swap page out + swap page in + restart overhead)]; where Page Fault Rate $0 \Box p \Box 1$
 - if p = 0, no page faults; if p = 1, every reference is a fault
 - Can optimize demand paging by loading entire process image to swap space at process load time
- Pure Demand Paging: process starts with no pages in memory
- <u>Copy-on-Write (COW)</u> allows both parent and child processes to initially share the same pages in memory
 - If either process modifies a shared page, only then is the page copied
- <u>Modify (dirty) bit</u> can be used to reduce overhead of page transfers → only modified pages written to disk
- When a page is replaced, write to disk if it has been marked dirty and swap in desired page
- Pages can be replaced using different algorithms: FIFO, LRU (below)
 - Stack can be used to record the most recent page references (LRU is a "stack" algorithm)

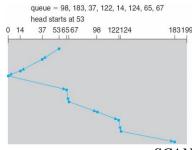


- Second chance algorithm uses a reference bit
 - If 1, decrement and leave in memory
 - If 0, replace next page
- Fixed page allocation: Proportional allocation Allocate according to size of process
 - $s_i = \text{size of process } P_i$, $S = \sum s_i$, m = total number of frames, $a_i \text{allocation for } P_i$
 - \circ $a_i = (s_i/S)*m$
- Global replacement: process selects a replacement frame from set of all frames
 - One process can take frame from another
 - Process execution time can vary greatly

- Greater throughput
- Local replacement: each process selects from only its own set of allocated frames
 - More consistent performance
 - Possible under-utilization of memory
- Page-fault rate is very high if a process does not have "enough" pages
 - Thrashing: a process is busy swapping pages in and out → minimal work is actually being performed
- <u>Memory-mapped</u> file I/O allows file I/O to be treated as routine memory access by <u>mapping</u> a disk block to a page in memory
- I/O Interlock: Pages must sometimes be locked into memory

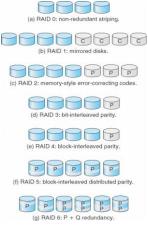
Ch.10 – Mass-Storage Systems

- Magnetic disks provide bulk of secondary storage rotate at 60 to 250 times per second
 - Transfer rate: rate at which data flows between drive and computer
 - <u>Positioning time (random-access time)</u> is time to move disk arm to desired cylinder <u>(seek time)</u> and time for desired sector to rotate under the disk head <u>(rotational latency)</u>
 - Head crash: disk head making contact with disk surface
- Drive attached to computer's I/O bus EIDE, ATA, SATA, USB, etc.
 - Host controller uses bus to talk to disk controller
- Access latency = Average access time = average seek time + average latency (fast ~5ms, slow ~14.5ms)
- Average I/O time = avg. access time + (amount to transfer / transfer rate) + controller overhead
 - Ex: to transfer a 4KB block on a 7200 RPM disk with a 5ms average seek time, 1Gb/sec transfer rate with a .1ms controller overhead = 5ms + 4.17ms + 4KB / 1Gb/sec + 0.1ms = 9.27ms + .12ms = 9.39ms
- Disk drives addressed as 1-dimensional arrays of logical blocks
 - 1-dimensional array is mapped into the sectors of the disk sequentially
- Host-attached storage accessed through I/O ports talking to I/O buses
 - Storage area network (SAN): many hosts attach to many storage units, common in large storage environments
 - Storage made available via LUN masking from specific arrays to specific servers
- <u>Network attached storage (NAS)</u>: storage made available over a network rather than local connection
- In disk scheduling, want to minimize seek time; Seek time is proportional to seek distance
- <u>Bandwidth</u> is (total number of bytes transferred) / (total time between first request and completion of last transfer)
- Sources of disk I/O requests: OS, system processes, user processes
 - OS maintains queue of requests, per disk or device
- Several algorithms exist to schedule the servicing of disk I/O requests
 - FCFS, SSTF (shortest seek time first), <u>SCAN</u>, CSCAN, LOOK, CLOOK
 - <u>SCAN/elevator</u>: arm starts at one end and moves towards other end servicing requests as it goes, then reverses direction
 - CSCAN: instead of reversing direction, immediately goes back to beginning
 - LOOK/CLOOK: Arm only goes as far as the last request in each directions, then reverses immediately



SCAN

- <u>Low level/physical formatting</u>: dividing a disk into sectors that the disk controller can read and write usually 512 bytes of data
- <u>Partition</u>: divide disk into one or more groups of cylinders, each treated as logical disk
- Logical formatting: "making a file system"
- Increase efficiency by grouping blocks into <u>clusters</u> Disk I/O is performed on blocks
 - Boot block initializes system <u>bootstrap loader</u> stored in boot block
- Swap-space: virtual memory uses disk space as an extension of main memory
 - Kernel uses swap maps to track swap space use
- <u>RAID</u>: Multiple disk drives provide reliability via redundancy increases mean time to failure
 - Disk striping uses group of disks as one storage unit
 - Mirroring/shadowing (RAID 1) keeps duplicate of each disk
 - Striped mirrors (RAID 1+0) or mirrored striped (RAID 0+1) provides high performance/reliability
 - Block interleaved parity (RAID 4, 5, 6) uses much less redundancy
- Solaris ZFS adds <u>checksums</u> of all data and metadata detect if object is the right one and whether it changed
- Tertiary storage is usually built using <u>removable media</u> can be <u>WORM</u> or <u>Read-only</u>, handled like fixed disks
- Fixed disk usually more reliable than removable disk or tape drive
- Main memory is much more expensive than disk storage



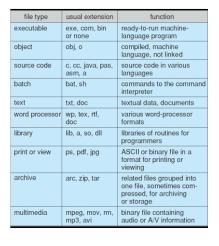
UNIT-V

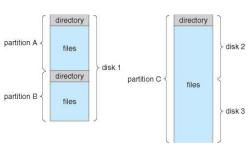
File System, I/O and Security

File Systems: Concepts- Structure-Allocation Methods. I/O Systems: Harware-Interface-Transformation. Security and Protection: Access Matrix- Access Control-Program Threats-Cryptography-Defense Mechanism

Ch.11 – File-System Interface

- <u>File</u> Uniform logical view of information storage (no matter the medium)
 - Mapped onto physical devices (usually nonvolatile)
 - Smallest allotment of nameable storage
 - Types: Data (numeric, character, binary), Program, Free form, Structured
 - Structure decided by OS and/or program/programmer
- Attributes:
 - Name: Only info in human-readable form
 - Identifier: Unique tag, identifies file within the file system
 - Type, Size
 - Location: pointer to file location
 - Time, date, user identification
- File is an abstract data type
- Operations: create, write, read, reposition within file, delete, truncate
- Global table maintained containing process-independent open file information: open-file table
 - Per-process open file table contains pertinent info, plus pointer to entry in global open file table
- Open file locking: mediates access to a file (shared or exclusive)
 - Mandatory access denied depending on locks held and requested
 - Advisory process can find status of locks and decide what to do
- File type can indicate internal file structure
- Access Methods: Sequential access, direct access
 - Sequential Access: tape model of a file
 - Direct Access: random access, relative access
- Disk can be subdivided into <u>partitions</u>; disks or partitions can be <u>RAID</u> protected against failure.
 - Can be used <u>raw</u> without a file-system or <u>formatted</u> with a file system
 - Partitions also knows as minidisks, slices
- <u>Volume</u> contains file system: also tracks file system's info in <u>device directory</u> or <u>volume table of</u> contents
- File system can be general or special-purpose. Some <u>special purpose FS</u>:
 - tmpfs temporary file system in volatile memory
 - objfs virtual file system that gives debuggers access to kernel symbols
 - ctfs virtual file system that maintains info to manage which processes start when system boots
 - lofs loop back file system allows one file system to be accessed in place of another
 - procfs virtual file system that presents information on all processes as a file system





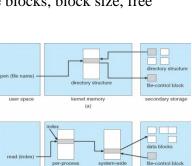
File-System Organization

- Directory is similar to symbol table translating file names into their directory entries
 - Should be efficient, convenient to users, logical grouping
 - <u>Tree structured</u> is most popular allows for grouping
 - Commands for manipulating: remove rm<file-name>; make new sub directory mkdir<dir-name>
- Current directory: default location for activities can also specify a path to perform activities in
- Acyclic-graph directories adds ability to directly share directories between users
 - Acyclic can be guaranteed by: only allowing shared files, not shared sub directories;
 garbage collection; mechanism to check whether new links are OK
- File system must be <u>mounted</u> before it can be accessed kernel data structure keeps track of mount points
- In a <u>file sharing</u> system <u>User IDs</u> and <u>Group IDs</u> help identify a user's permissions
- <u>Client-server</u> allows multiple clients to mount remote file systems from servers <u>NFS</u> (UNIX),
 <u>CIFS</u> (Windows)
- <u>Consistency semantics</u> specify how multiple users are to access a shared file simultaneously similar to synchronization algorithms from Ch.7
 - One way of protection is <u>Controlled Access</u>: when file created, determine r/w/x access for users/groups

Ch.12 – File System Implementation

- File system resides on secondary storage disks; file system is organized into layers →
- <u>File control block</u>: storage structure consisting of information about a file (exist per-file)
- <u>Device driver</u>: controls the physical device; manage I/O devices
- <u>File organization module</u>: understands files, logical addresses, and physical blocks
 - Translates logical block number to physical block number
 - Manages free space, disk allocation
- <u>Logical file system</u>: manages metadata information maintains file control blocks
- Boot control block: contains info needed by system to boot OS from volume
- <u>Volume control block</u>: contains volume details; ex: total # blocks, # free blocks, block size, free block pointers
- Root partition: contains OS; mounted at boot time
- For all partitions, system is consistency checked at mount time
 - Check metadata for correctness only allow mount to occur if so
- Virtual file systems provide object-oriented way of implementing file systems
- Directories can be implemented as <u>Linear Lists</u> or <u>Hash Tables</u>
 - Linear list of file names with pointer to data blocks simple but slow
 - Hash table linear list with hash data structure decreased search time
 - Good if entries are fixed size
 - Collisions can occur in hash tables when two file names hash to same location
- Contiguous allocation: each file occupies set of contiguous blocks
 - Simple, best performance in most cases; problem finding space for file, external fragmentation
 - Extent based file systems are modified contiguous allocation schemes extent is allocated for file allocation
- Linked Allocation: each file is a linked list of blocks no external fragmentation
 - Locating a block can take many I/Os and disk seeks
- Indexed Allocation: each file has its own index block(s) of pointers to its data blocks
 - Need index table; can be random access; dynamic access without external fragmentation but has overhead
- Best methods: linked good for sequential, not random; contiguous good for sequential and random
- File system maintains <u>free-space list</u> to track available blocks/clusters
- <u>Bit vector</u> or <u>bit map</u> (n blocks): block number calculation \rightarrow (#bits/word)*(# 0-value words)+(offset for 1st bit)

Example: block size = 4KB = 212 bytes disk size = 240 bytes (1 terabyte) n = 240/212 = 228 bits (or 256 MB) if clusters of 4 blocks -> 64MB of memory



application programs

logical file system

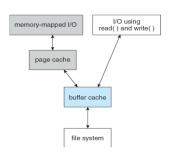
file-organization module

basic file system

I/O control

devices

(a) open() (b) read()



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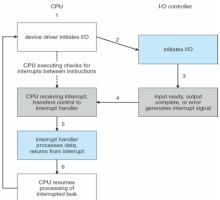
- Space maps (used in ZFS) divide device space into metaslab units and manages metaslabs
 - Each metaslab has associated space map
- <u>Buffer cache</u> separate section of main memory for frequently used blocks
- Synchronous writes sometimes requested by apps or needed by OS no buffering

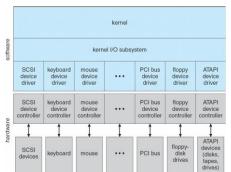
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- Asynchronous writes are more common, buffer-able, faster
- Free-behind and read-ahead techniques to optimize sequential access
- Page cache caches pages rather than disk blocks using virtual memory techniques and addresses
 - Memory mapped I/O uses page cache while routine I/O through file system uses buffer (disk) cache

•	<u>Unified buffer cache</u> : uses same page cache to cache both memory-mapped pages and ordinary file system I/O to avoid <u>double caching</u>

- Ch.13 I/O Systems
- <u>Device drivers</u> encapsulate device details present uniform device access interface to I/O subsystem
- Port: connection point for device
- Bus: daisy chain or shared direct access
- Controller (host adapter): electronics that operate port, bus, device sometimes integrated
 - Contains processor, microcode, private memory, bus controller
- <u>Memory-mapped I/O</u>: device data and command registers mapped to processor address space
 - Especially for large address spaces (graphics)
- Polling for each byte of data busy-wait for I/O from device
 - Reasonable for fast devices, inefficient for slow ones
 - Can happen in 3 instruction cycles
- CPU <u>interrupt-request line</u> is triggered by I/O devices <u>interrupt</u> handler receives interrupts
 - Handler is maskable to ignore or delay some interrupts
 - Interrupt vector dispatches interrupt to correct handler based on priority; some nonmaskable
 - Interrupt chaining occurs if there is more than one device at the same interrupt number
 - Interrupt mechanism is also used for exceptions
- <u>Direct memory access</u> is used to avoid <u>programmed I/O</u> for large data movement
 - Requires DMA controller
 - Bypasses CPU to transfer data directly between I/O device and memory
- Device driver layer hides differences among I/O controllers from kernel
- Devices vary in many dimensions: <u>character stream/block</u>, <u>sequential/random access</u>, <u>synchronous/asynchronous</u>, <u>sharable/dedicated</u>, <u>speed</u>, <u>rw/ro/wo</u>
- Block devices include disk drives: Raw I/O, Direct I/OU
 - Commands include read, write, seek
- Character devices include keyboards, mice, serial ports
 - Commands include get(), put()
- Network devices also have their own interface; UNIX and Windows NT/9x/2000 include socket interface
 - Approaches include pipes, FIFOs, streams, queues, mailboxes
- Programmable interval timer: used for timings, periodic interrupts
- <u>Blocking I/O</u>: process suspended until I/O completed easy to use and understand, not always best method
- <u>Nonblocking I/O</u>: I/O call returns as much as available implemented via multi-threading, returns quickly
- <u>Asynchronous</u>: process runs while I/O executes difficult to use, process signaled upon I/O completion
- Spooling: hold output for a device if device can only serve one request at a time (ex: printer)
- Device Reservation: provides exclusive access to a device must be careful of deadlock
- Kernel keeps state info for I/O components, including open file tables, network connections,





character device states

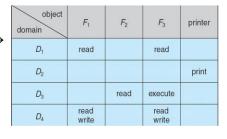
- Complex data structures track buffers, memory allocation, "dirty" blocks
- <u>STREAM</u>: full-duplex communication channel between user-level process and device in UNIX
 - Each module contains <u>read queue</u> and <u>write queue</u>
 - Message passing used to communicate between queues <u>Flow control</u> option to indicate available or busy
 - Asynchronous internally, synchronous where user process communicates with stream head
- I/O is a major factor in system performance demand on CPU, context switching, data copying, network traffic

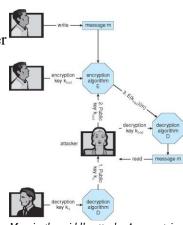
Ch.14 - Protection

- <u>Principle of least privilege</u>: programs, users, systems should be given just enough privileges to perform their tasks
- Access-right = <obj-name, rights-set> w/ rights-set is subset of all valid operations performable on the object
 - <u>Domain</u>: set of access-rights
 - UNIX system consists of 2 domains: user, supervisor
 - MULTICS domain implementation (domain rings) if $j \le i \rightarrow D_i \subseteq D_i$
- Access matrix: rows represent domains, columns represent objects
 - Access(i,j) is the set of operations that a process executing in Domain_i can invoke on Object_i
 - Can be expanded to dynamic protection
- Access matrix design separates <u>mechanism</u> from <u>policy</u>
 - Mechanism: OS provides access-matrix and rules ensures matrix is only manipulated by authorized users
 - Policy: User dictates policy who can access what object and in what mode
- Solaris 10 uses <u>role-based access control (RBAC)</u> to implement least privilege
- Revocation of access rights
 - Access list: delete access rights from access list simple, immediate
 - <u>Capability list</u>: required to locate capability in system before capability can be revoked reacquisition, back-pointers, indirection, keys
- <u>Language-Based Protection</u>: allows high-level description of policies for the allocation and use of resources
 - Can provide software for protection enforcement when hardware-supported checking is unavailable

Ch.15 – Security

- System <u>secure</u> when resources used and accessed as intended under all circumstances
- Attacks can be accidental or malicious
 - Easier to protect against accidental than malicious misuse
- Security violation categories:
 - Breach of confidentiality unauthorized reading of data
 - Breach of integrity unauthorized modification of data
 - Breach of availability unauthorized destruction of data
 - Theft of service unauthorized use of resources
 - Denial of service prevention of legitimate use
- Methods of violation:
 - Masquerading pretending to be an authorized user
 - Man-in-the-middle intruder sits in data flow, masquerading as sender to receiver and vice versa
 - Session hijacking intercept and already established session to bypass authentication
- Effective security must occur at four levels: physical, human, operating system, network
- <u>Program threats</u>: trojan horse (spyware, pop-up, etc.), trap door, logic bomb, stack and buffer overflow
- <u>Viruses</u>: code fragment embedded in legitimate program; self-replicating





Man-in-the-middle attack - Asymmetric Cryptography

- Specific to CPU architecture, OS, applications
- Virus dropper: inserts virus onto the system
- Windows is the target for most attacks most common, everyone is administrator
- Worms: use spawn mechanism standalone program
- <u>Port scanning</u>: automated attempt to connect to a range of ports on one or a range of IP addresses
 - Frequently launched from <u>zombie systems</u> to decrease traceability
- <u>Denial of service</u>: overload targeted computer preventing it from doing useful work
- <u>Cryptography</u>: means to constrain potential senders and/or receivers based on <u>keys</u>
 - Allows for confirmation of source, receipt by specified destination, trust relationship
- <u>Encryption</u>: [K of keys], [M of messages], [C of ciphertexts], function E:K to encrypt, function D:K to decrypt
 - Can have symmetric and asymmetric (distributes <u>public</u> encryption key, holds <u>private</u> decipher key) encryption
 - Asymmetric is much more compute intensive not used for bulk data transaction
 - Keys can be stored on a key ring
- Authentication: constraining a set of potential senders of a message
 - Helps to prove that the message is unmodified
 - Hash functions are basis of authentication
 - Creates small, fixed-size block of data (message digest, hash value)
- Symmetric encryption used in message-authentication code (MAC)
- Authenticators produced from authentication algorithm are <u>digital signatures</u>
- Authentication requires fewer computations than encryption methods
- <u>Digital Certificates</u>: proof of who or what owns a public key
- Defense in depth: most common security theory multiple layers of security
- Can attempt to detect intrusion:
 - Signature-based: detect "bad patterns"
 - Anomaly detection: spots differences from normal behavior
 - Both can report false positives or false negatives

Auditing, accounting, and logging specific system or network activity

- Firewall: placed between trusted and untrusted hosts
 - Limits network access between the two domains
 - Can be tunneled or spoofed
- Personal firewall is software layer on given host
 - Can monitor/limit traffic to/from host
- Application proxy firewall: Understands application protocol and can control them
- <u>System-call firewall</u>: Monitors all important system calls and apply rules and restrictions to them

