Pg 1

Name: Abhishek Srivastava.

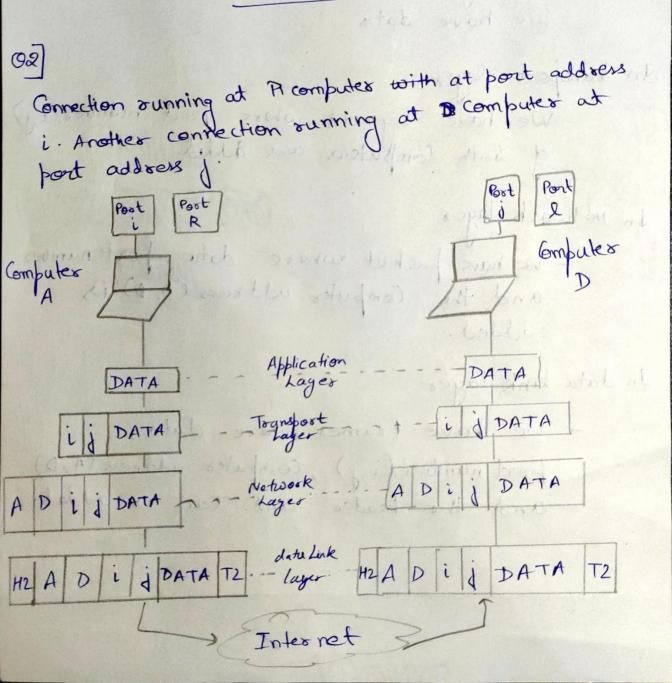
Reg. No.: 19BCE 10071

Subject : Computer Networks

Date : Max 7, 2021

Slot: A11+ A12+ A13

Midterm Examination.



Explanation.

2

At Afflication Lager

- We have data

In transport layer

- We have Segment where port number(ij)

of both Computers are Added.

In Network layer.

- he have packet where data, port number and the Computer address (A, D) is added.

In data link Layer

- We have Frames where data,

port number (i,j), computer address (4,D)

and the header and Tail is added.

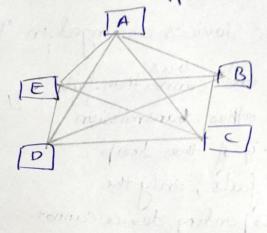
01]

Consequences if the connection faits

(a) Five devices arranged in Mesh topology.

If one connection fails, the other connections will still be pocking.

which means if A fails, it will not affect the working of B, C, D or D.



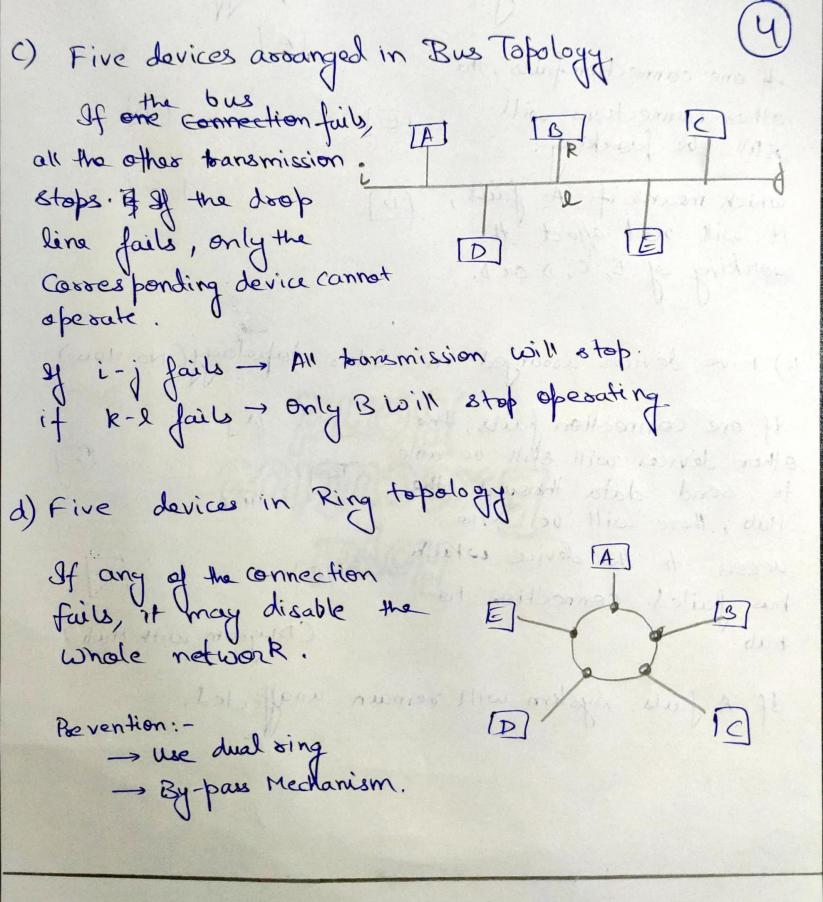
b) Five devices assurged in Star topology (No HUb)

If one connection fails, the other devices will still be able to send data through the Hub; there will be ma access to the device which has failed connection to hub.

HUB (E)

CDiagram with Hub)

If A fails, system will remain unaffected.



(a) A circuit switched network needs and to-end addressing during the setup and teadown phases to create a connection for the entire data bansfer phase.

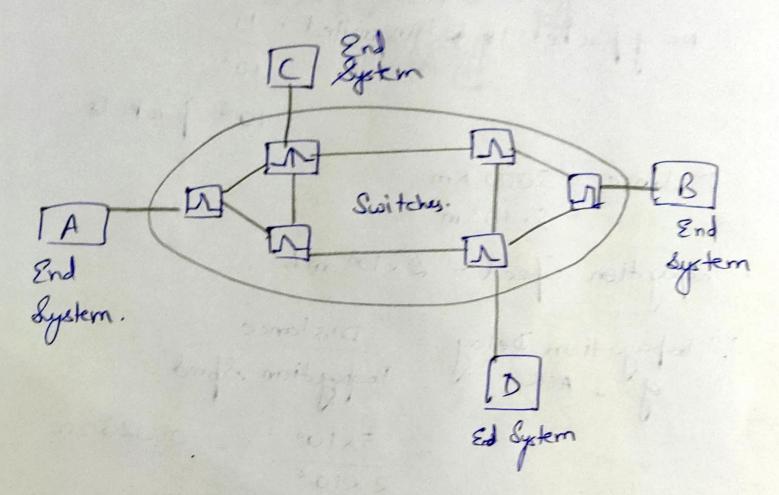
Once a connection is made, the data flow through the resources which are reserved. The duration of entire data transfer, the switches remain connected. There is no necessity for further addressing.

(b) Virtual switching network is a category of packet switching network, there a Northold path is established between the source and the destination system for data communication to occur.

Vistual circuit retwork needs addressing in three phases (setup phase, Data trans for, teardown phase). It is needed to make entry in the switching table. The entry is made for each request for connection. During the data transfor phase, each packet needs to carry a virtual-circuit identifier to show which violated concuit that particular packet of lows.

VIRTUAL CIRCUIT NETWORK





in the special received and the second

Sarl July

1000 - 10 - 10 - 10 × (10 - 11) 10 × 0001

Some of the best of the second and

#355 (18)

with the field so to a six word -

108 + 67 - 108 + 620 0 x (20) .

04

Given

Total data to be toursmitted = 10°00 bite Packet Size = 10° lite

to be bansmitted = 100 .. No of packet

= 103 packets

Distance = 5000 Km = 5x10° m

Poopagation Speed = 2 x 108 mk

: Bobagation Delay = Distance
of 1 ACK Poopagation Speed. = 0.025 100. = 5×106

Transmission Delay = 10 sec.
Processing Delay = 10 sec.
Waiting delay = 10 sec.

Total Time = 4000 x(No. of pucket) * (2x peopagation delay) + Transmission delay + processions delay
+ waiting time.
= 1000 x (2x0.025) + 10+10+10

= (1000 x 0.05) +30 = 50 +30

2 × 10 8

= 80 sect.

THE STATE OF THE S

Given: - del all short of molosomes and a

Bit Stream = 11011110

Polynomial Generator = x4+1

\$ 10001 = [xx/+x3x0+x3x0] +xx0+x°x1]

mollocarates in stones.

use division method, add add 3 0's at end Stream. a say dreated political led not le

I both of a west, train entition totally 10001 110111100000 0,0 -> 0 XOR 10001 0,1-1 1,0 -) | XOR 10001 1 1 1 0.000110000 parte procession xor (000) stown 1000 protested concert that particular lacket

CRC _

CRC - 0011

The last 4 bit we appended, will be replaced by CRC - doil de la la la dissort dissort de la de la

Actual bit string to ans forced = 1101111100011

het us suppose, third bit from left is inverted.

Received Bit string = 111111100011

Error Detection: Dividing again by 1001, if it heaves
ous remainder, means there is

10001 111 1111 000 11 x0R 100011 XOR 10001 XOR 10001 10001 X10011 10001 10

This shows that there

TIB some

excor in occived code.