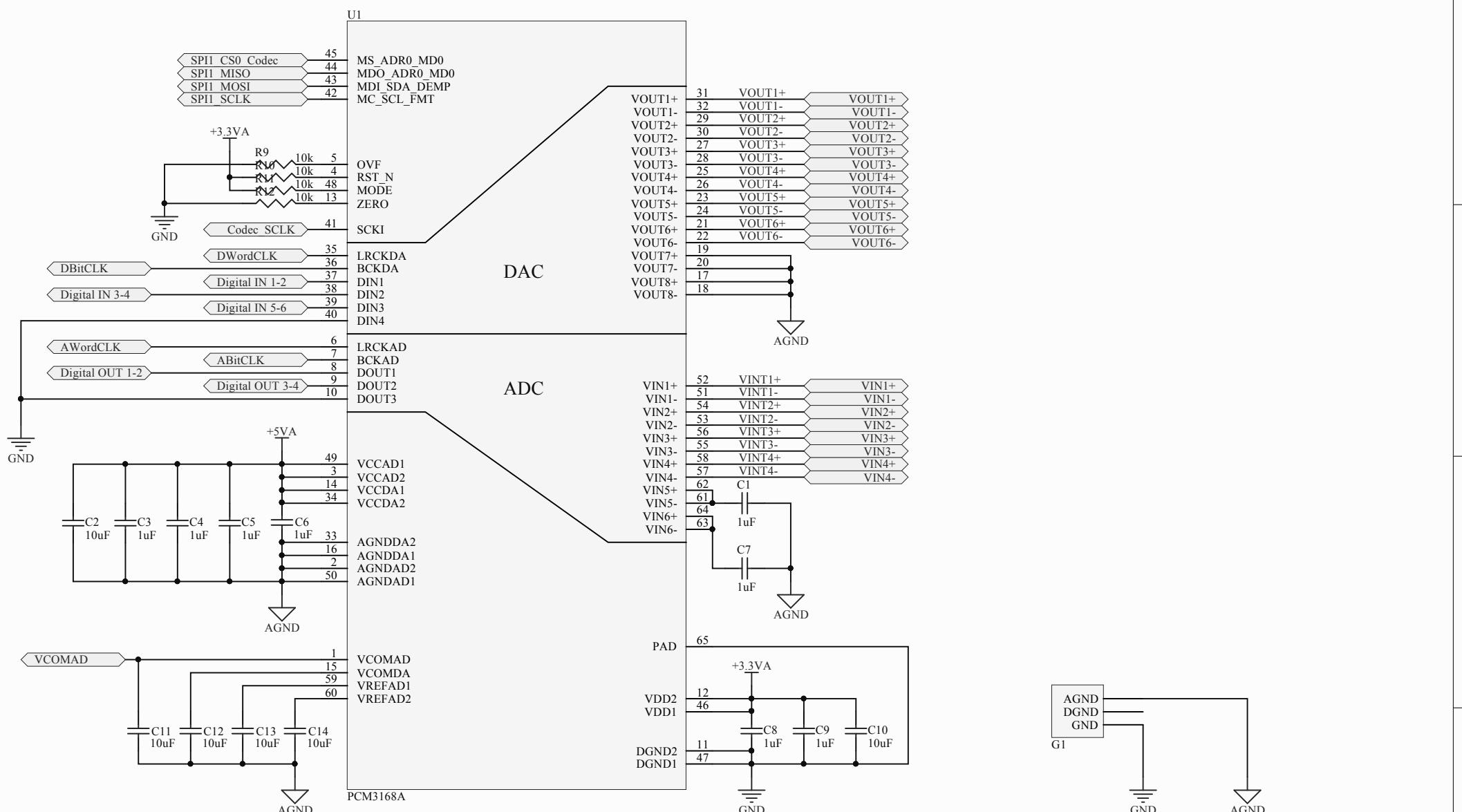
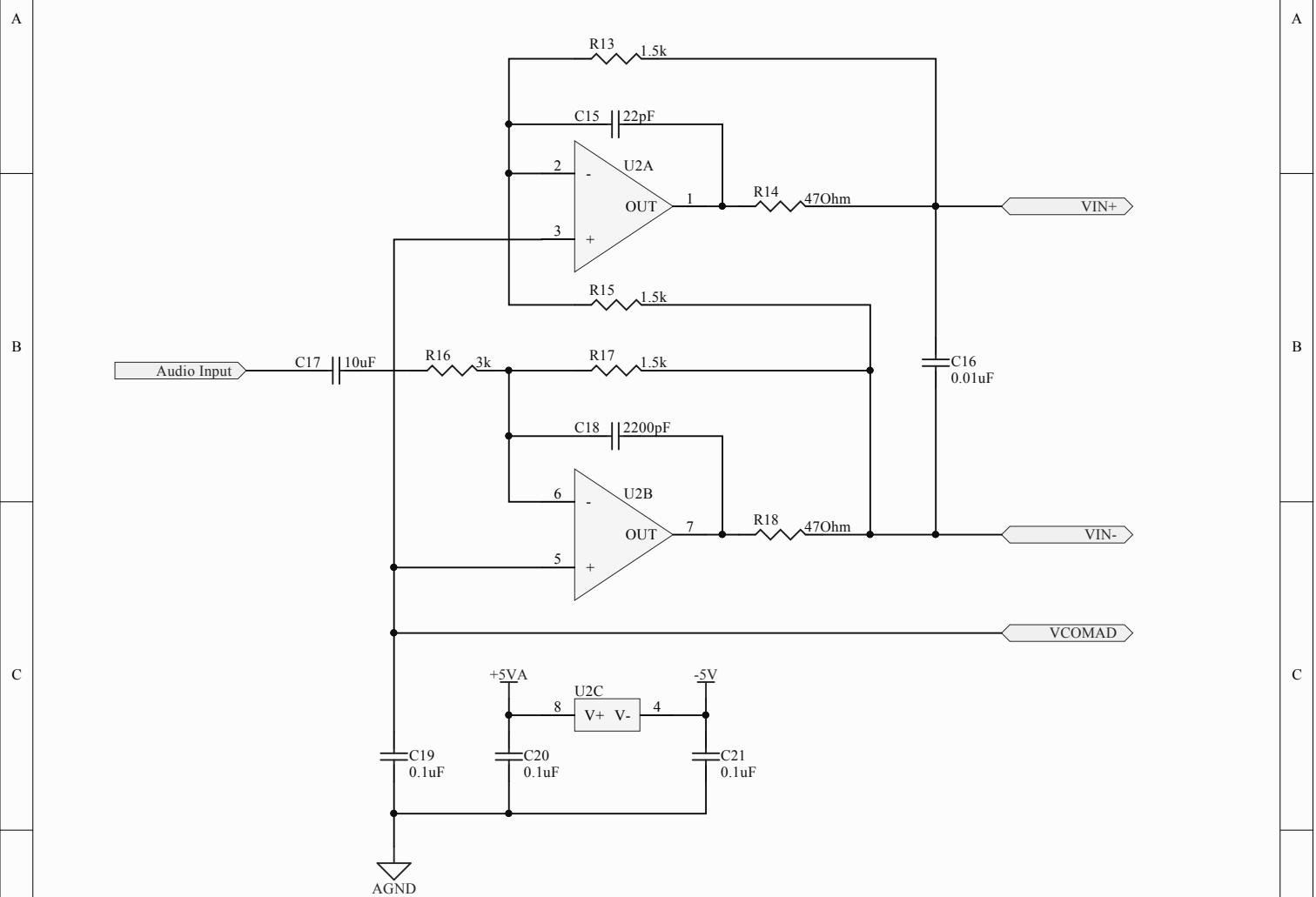


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Size	Number
A1	Revision
Date:	3/22/2018
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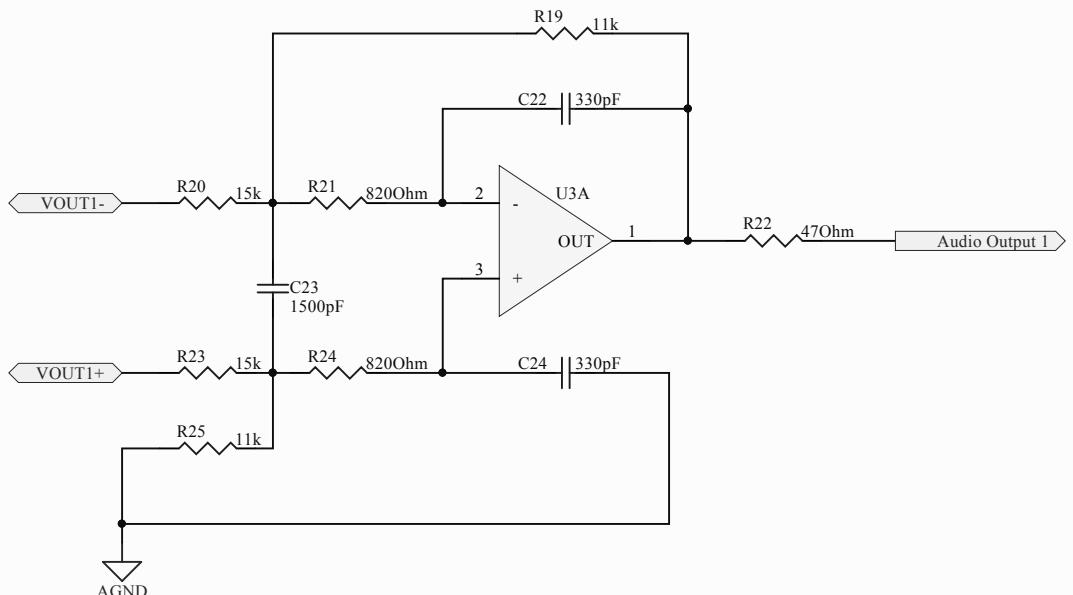


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Size	Number	Revision
A4		
Date: 3/22/2018	Sheet of	
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A



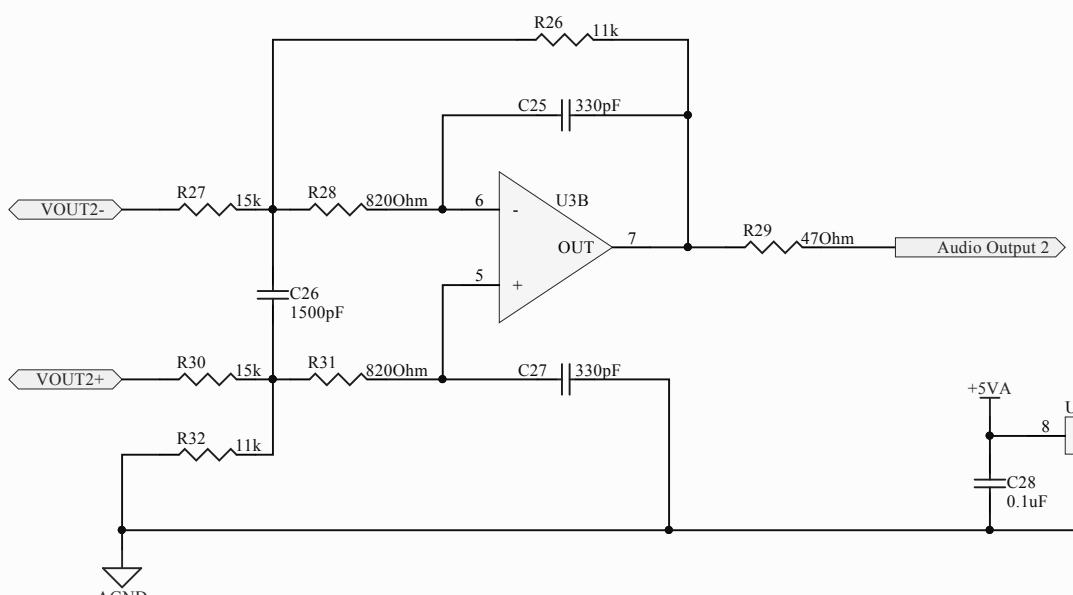
A

B

B

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C

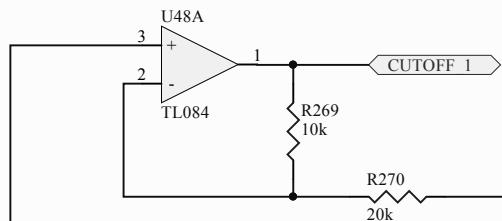


D

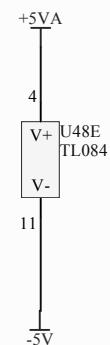
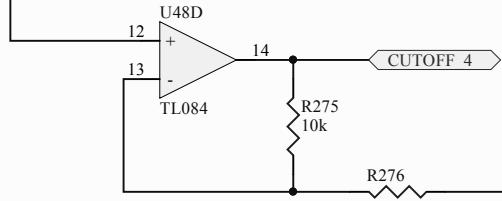
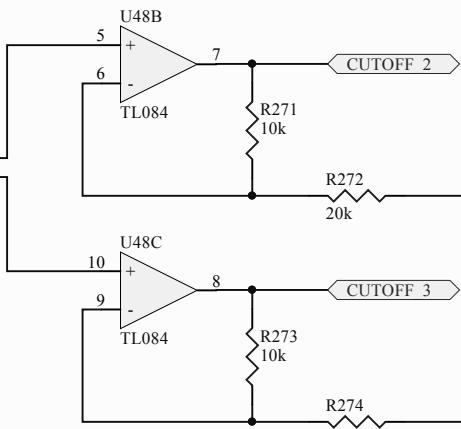
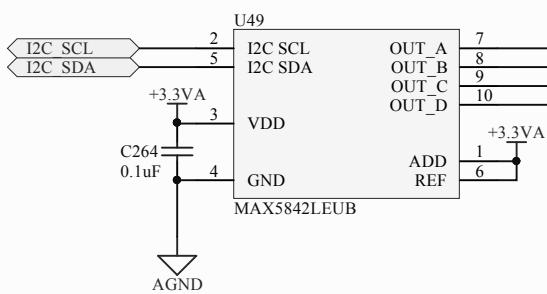
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Size A4	Number	Revision
Date: 3/22/2018		Sheet of
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A

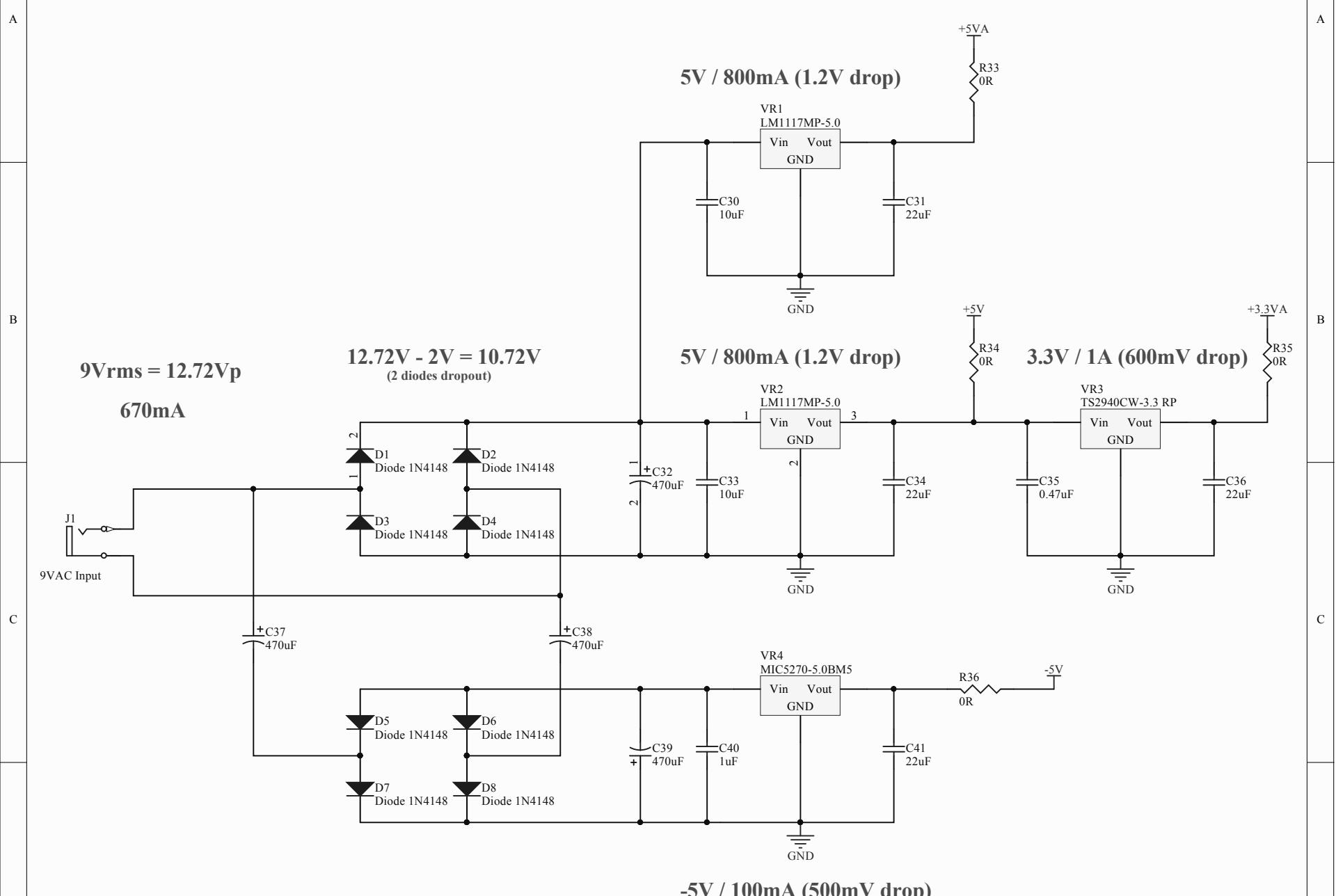


B



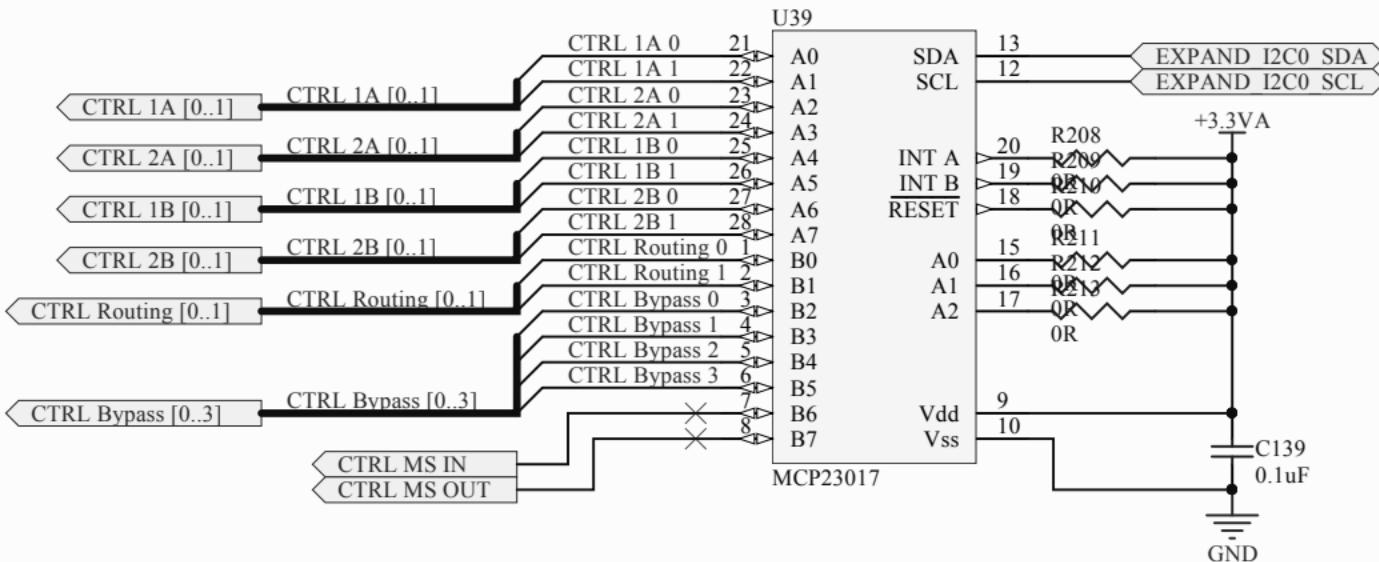
C

Title		
Size A4	Number	Revision
Date: 3/22/2018		Sheet of
File: C:\Users..\PC4 DAC Cutoff.SchDoc		Drawn By:



Title		
Size A	Number	Revision
Date: 3/22/2018	Sheet of	
File: C:\Users\...\PF1 Alim.SchDoc	Drawn By:	

# Expander Address 0100111



Title

A

Number

Revision

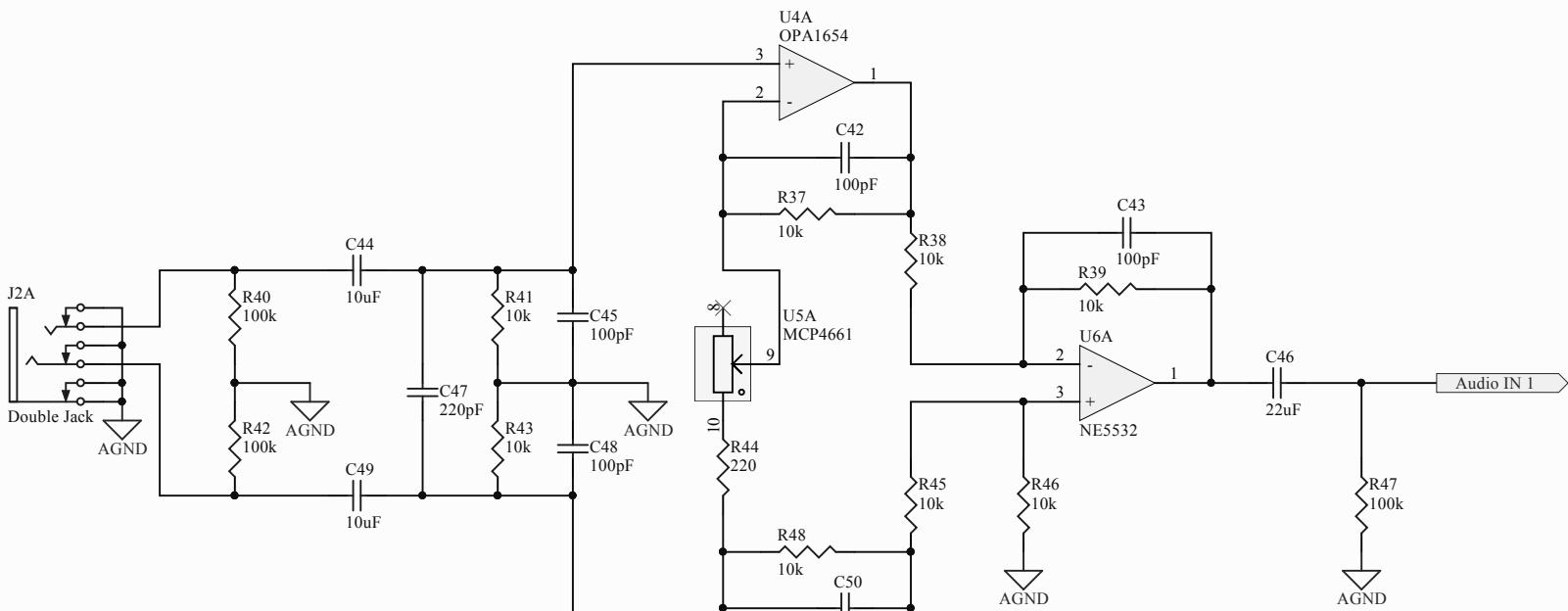
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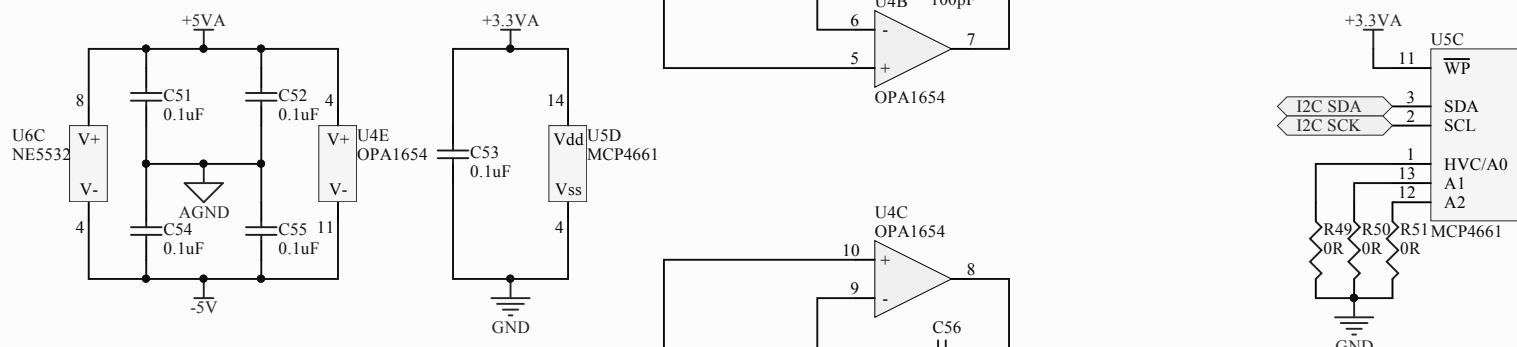
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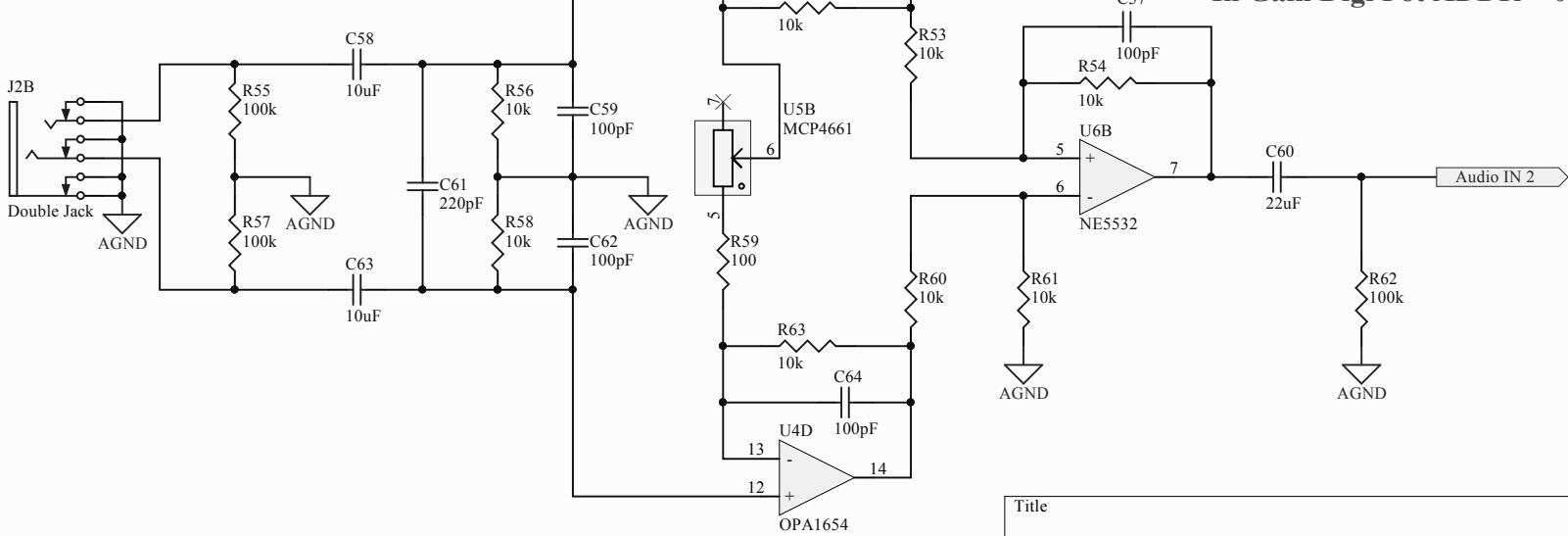
A



B

**In Gain Digi Pot ADDR = 000**

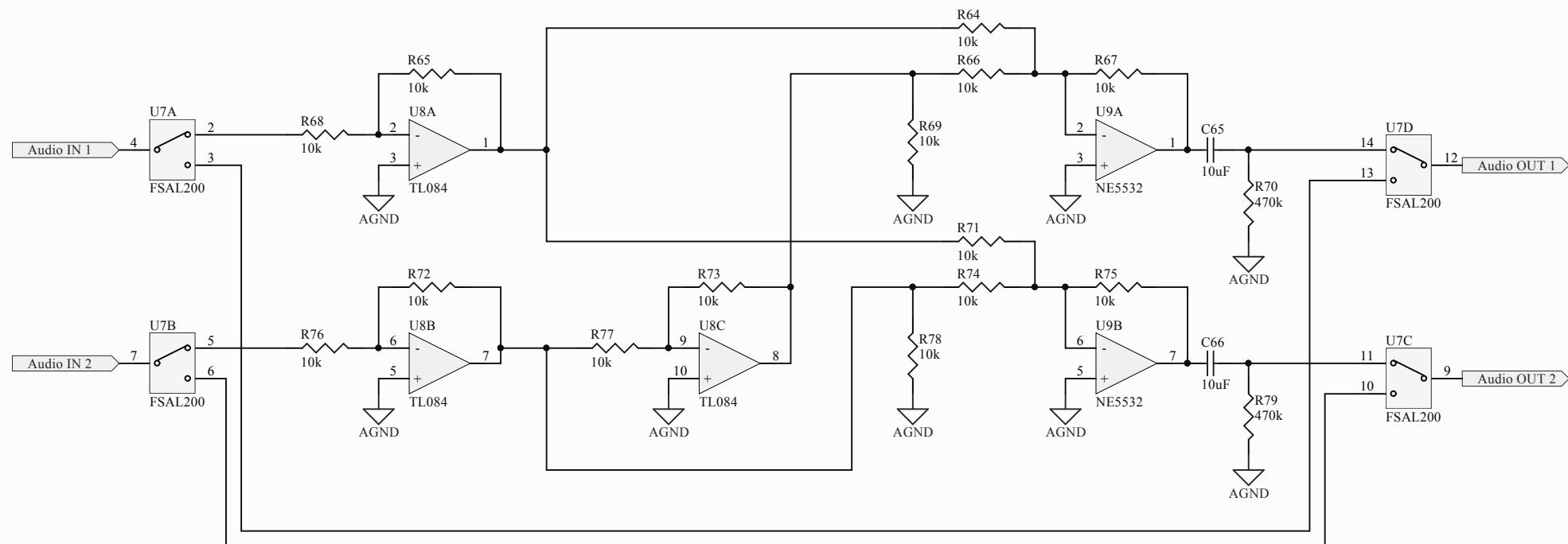
C



D

Title		
Size	Number	Revision
A		
Date:	3/22/2018	Sheet of
File:	C:\Users\...\PF2_AudioInput.SchDoc	Drawn By:

A



B

C

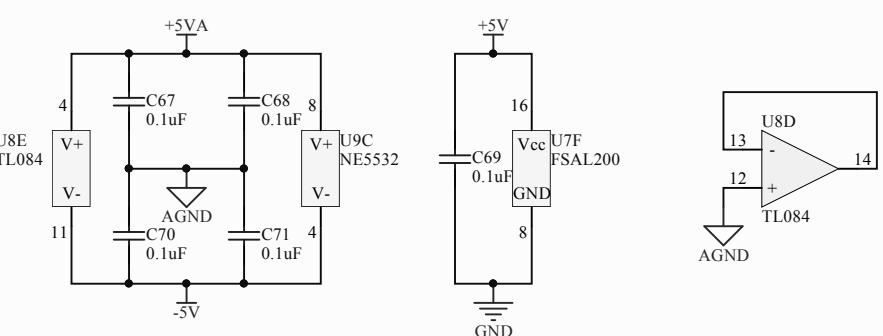
D

A

B

C

D



Title

Size

Number

Revision

Date: 3/22/2018

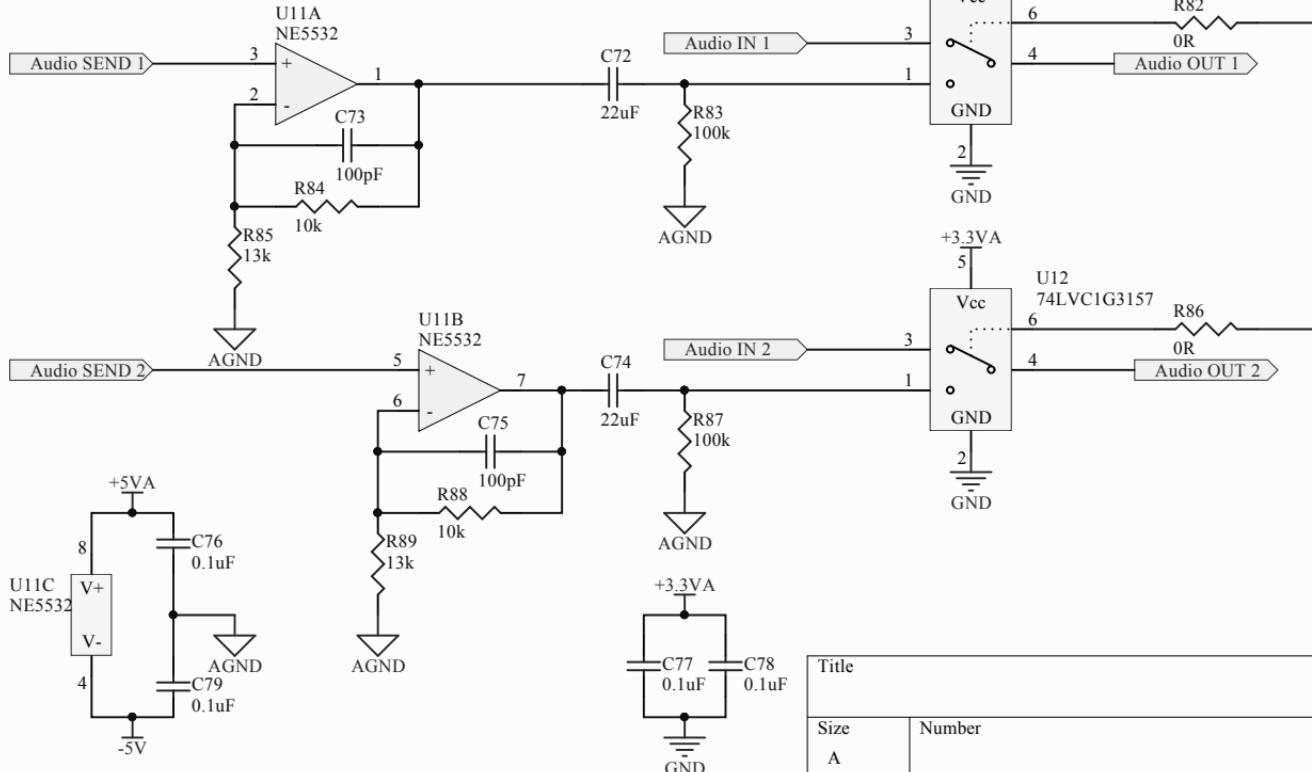
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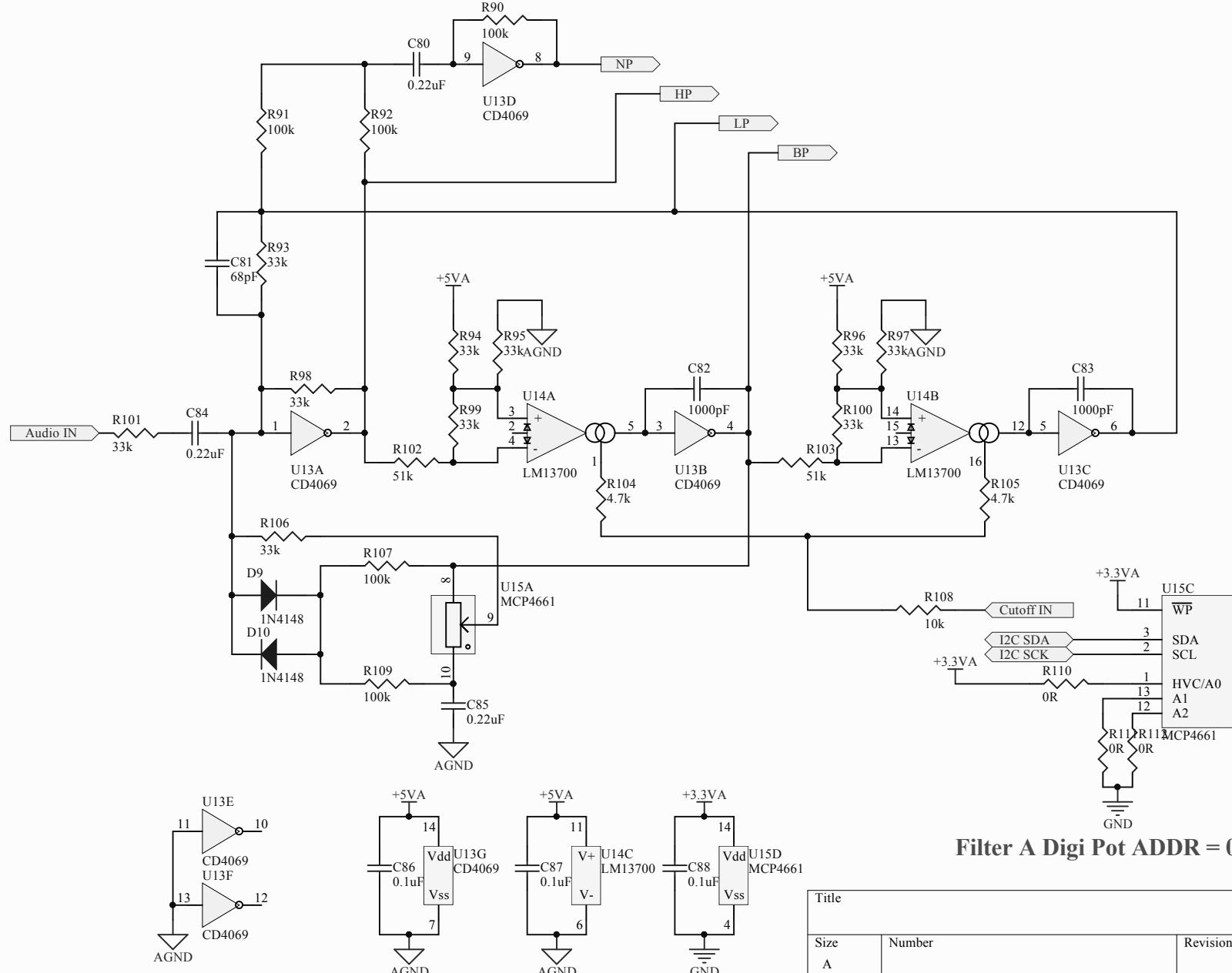
Drawn By:

**Gain = 1.75**

$$2V_{rms} = 2.82 \text{ Vp} * 1.75 = 4.94 \text{ Vp}$$



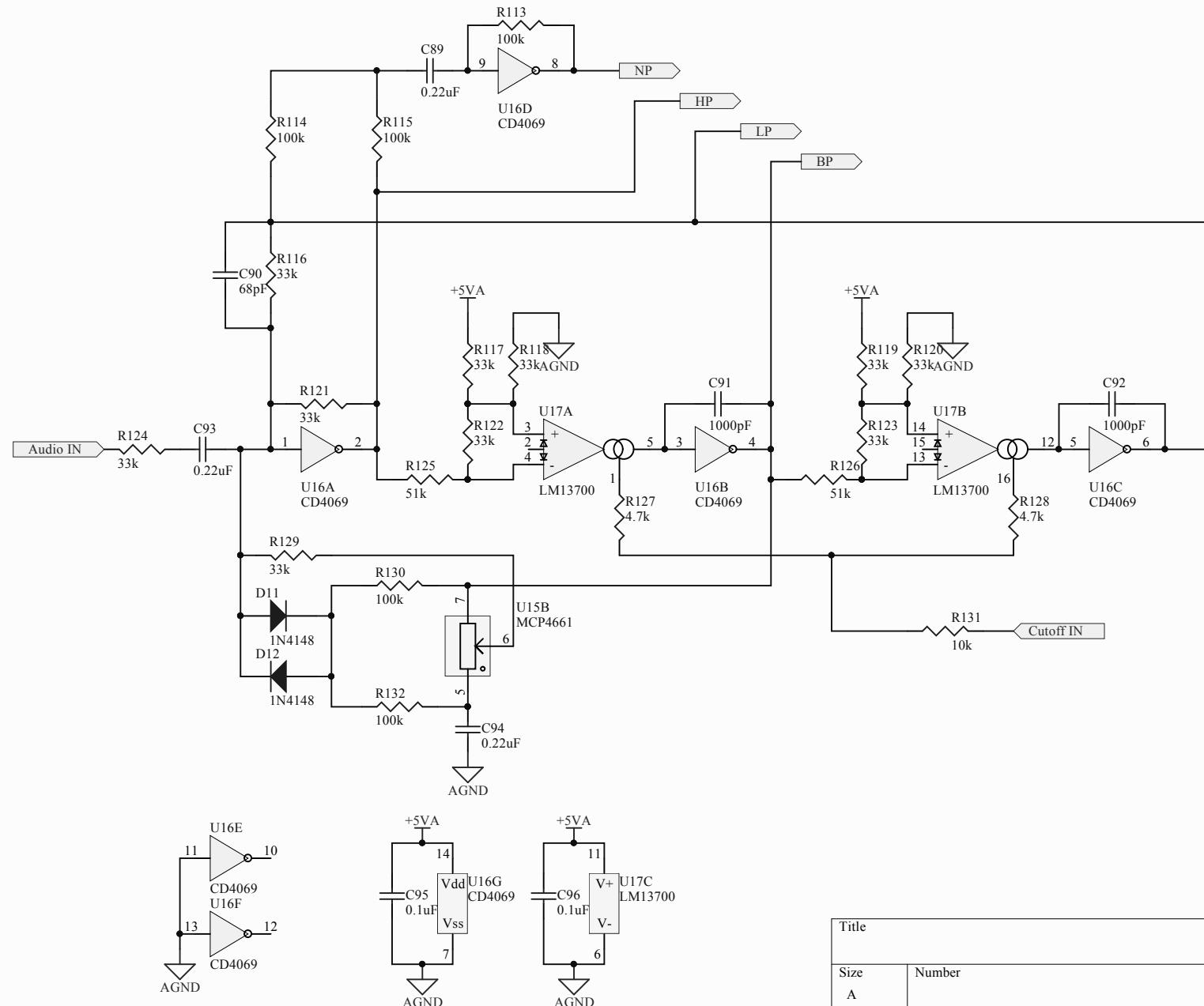
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Date:	3/22/2018	Sheet of		
File:	C:\Users\...\PF4 DSPBypass.SchDoc	Drawn By:		



**Filter A Digi Pot ADDR = 001**

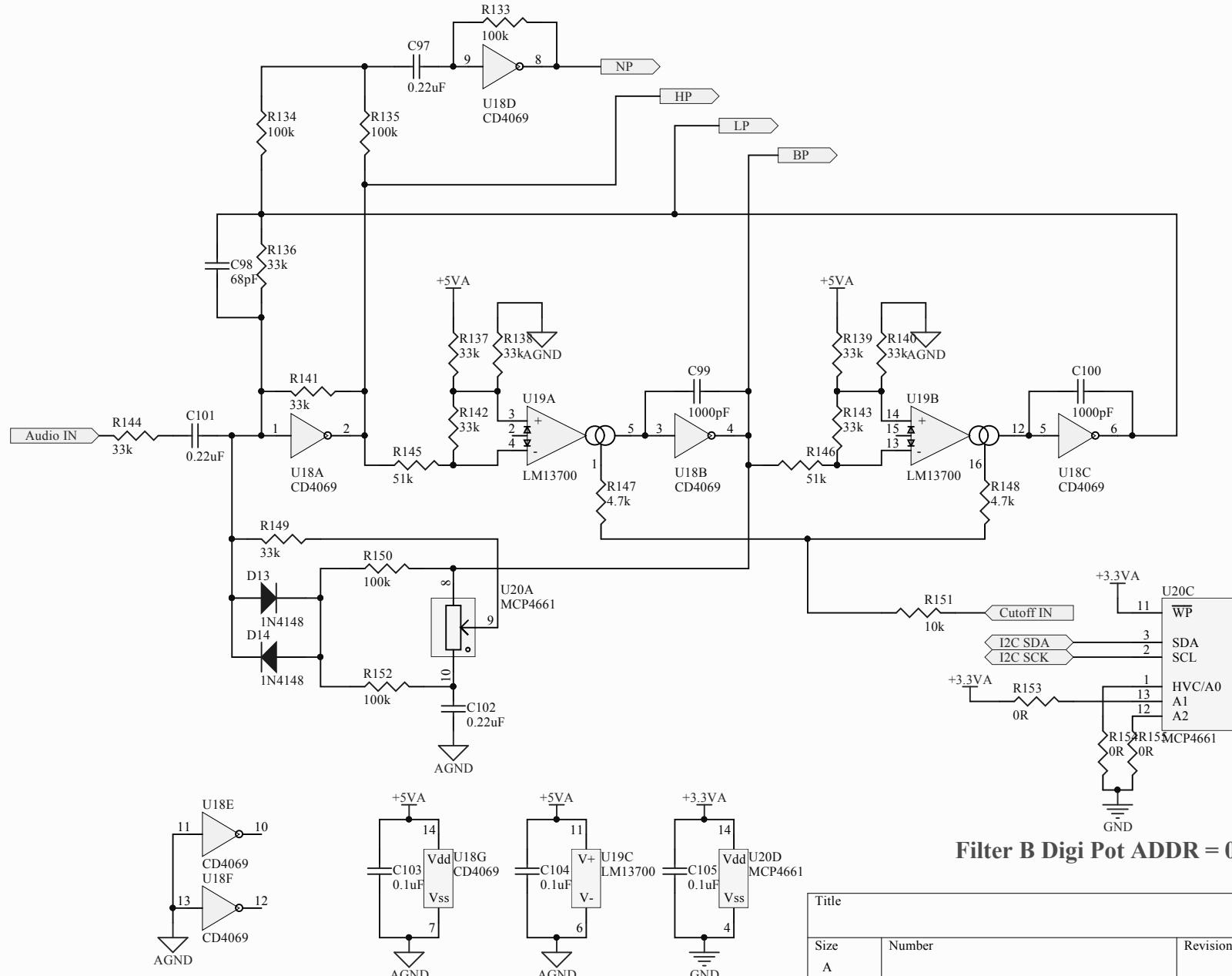
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Size	Number	Revision
A		
Date: 3/22/2018	Sheet of	
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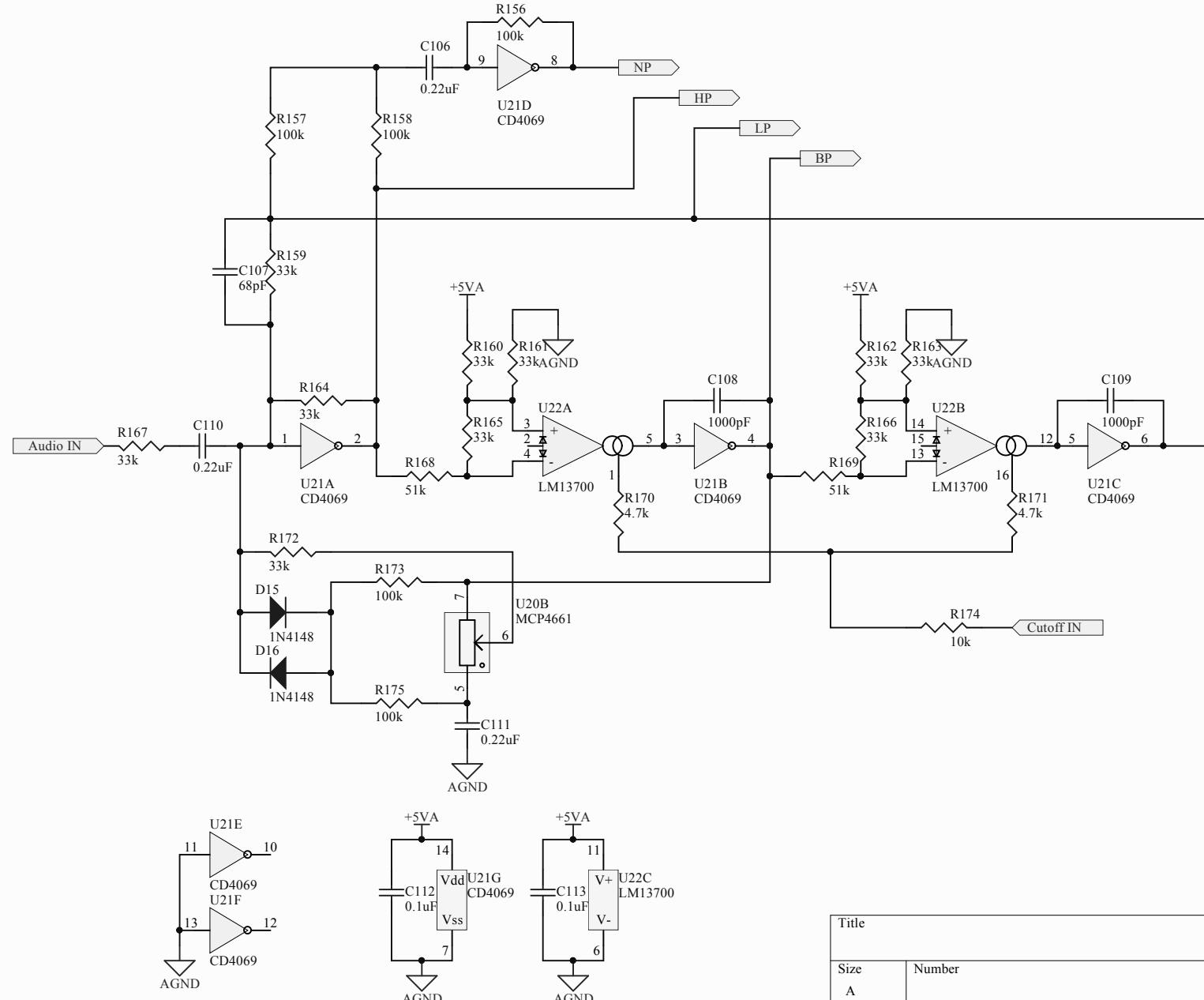
Title

Size	Number	Revision
A		
Date: 3/22/2018	Sheet of	
File: C:\Users\...\PF5b Filter2A.SchDoc		Drawn By:



Title

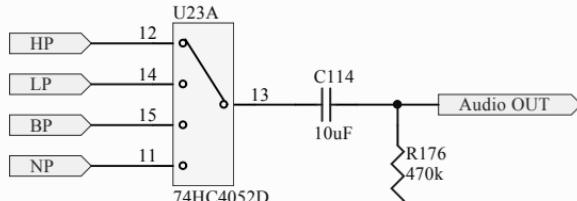
Size	Number	Revision
A		
Date: 3/22/2018	Sheet of	
File: C:\Users\...\PF5c Filter1B.SchDoc		Drawn By:



Title

Size	Number	Revision
A		
Date: 3/22/2018	Sheet of	
File: C:\Users\...\PF5d Filter2B.SchDoc		Drawn By:

A



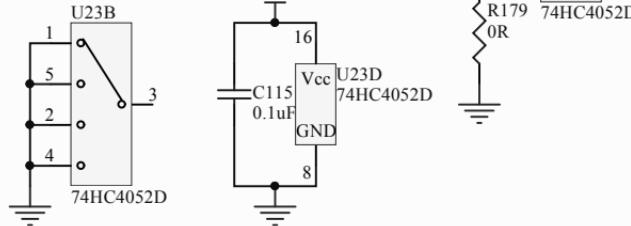
A

B



B

C



C

D

Title

Size Number

Revision

Date: 3/22/2018

Sheet of

File: C:\Users\...\PF6\_FilterMode.SchDoc

Drawn By:

	1A	2A	1B	2B	Out1	Out2
<b>Stereo</b>	In1	In2	1A	2A	1B	2B
<b>Dual Mono</b>	//	1A	In2	1B	2A	//
<b>Single Mono</b>	//	//	//	//	1A	1B

Audio IN 2 From Audio IN 2

Audio IN 1 To 1A

From Audio IN 2 2  
From 1A 3  
To 2A 4

U26A  
FSAL200

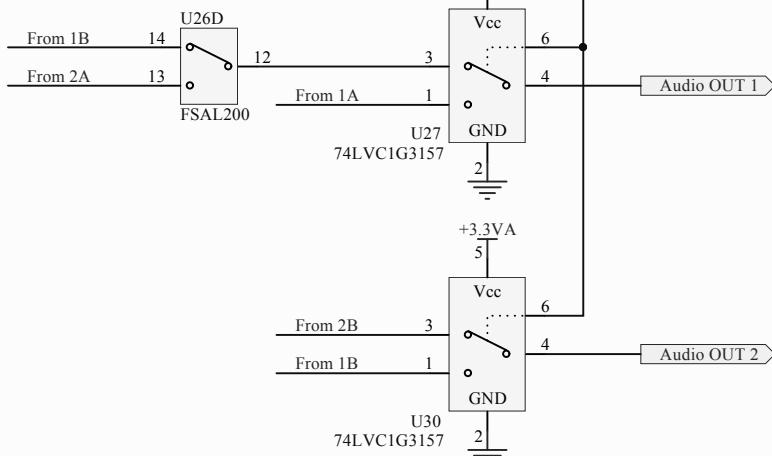
From 1A 5  
From Audio IN 2 6  
To 1B 7

U26B  
FSAL200

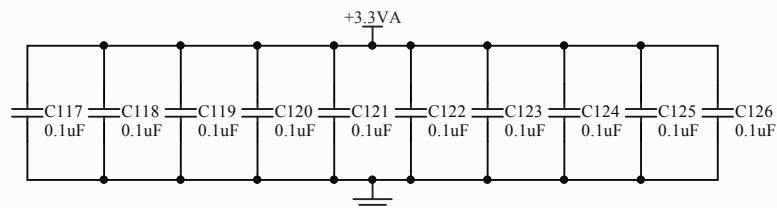
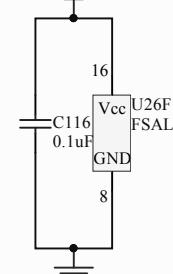
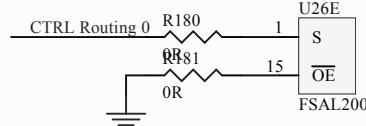
From 2A 11  
From 1B 10  
To 2B 9

U26C  
FSAL200

CTRL Routing [0..1]

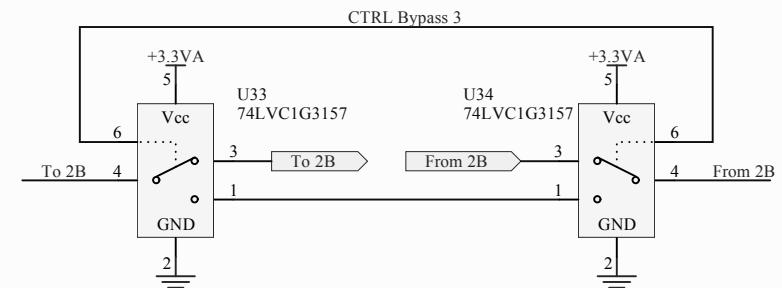
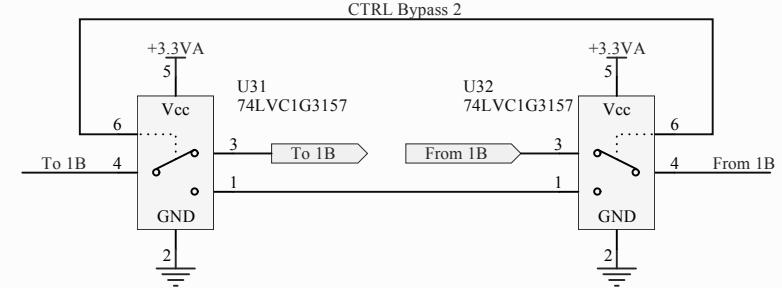
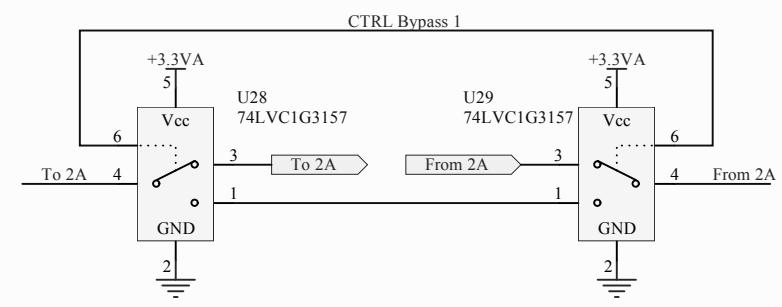
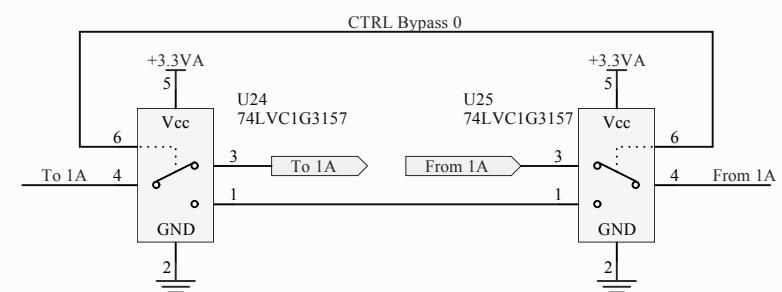


## FILTER ROUTING



## FILTER BYPASS

CTRL Bypass [0..3]



Title

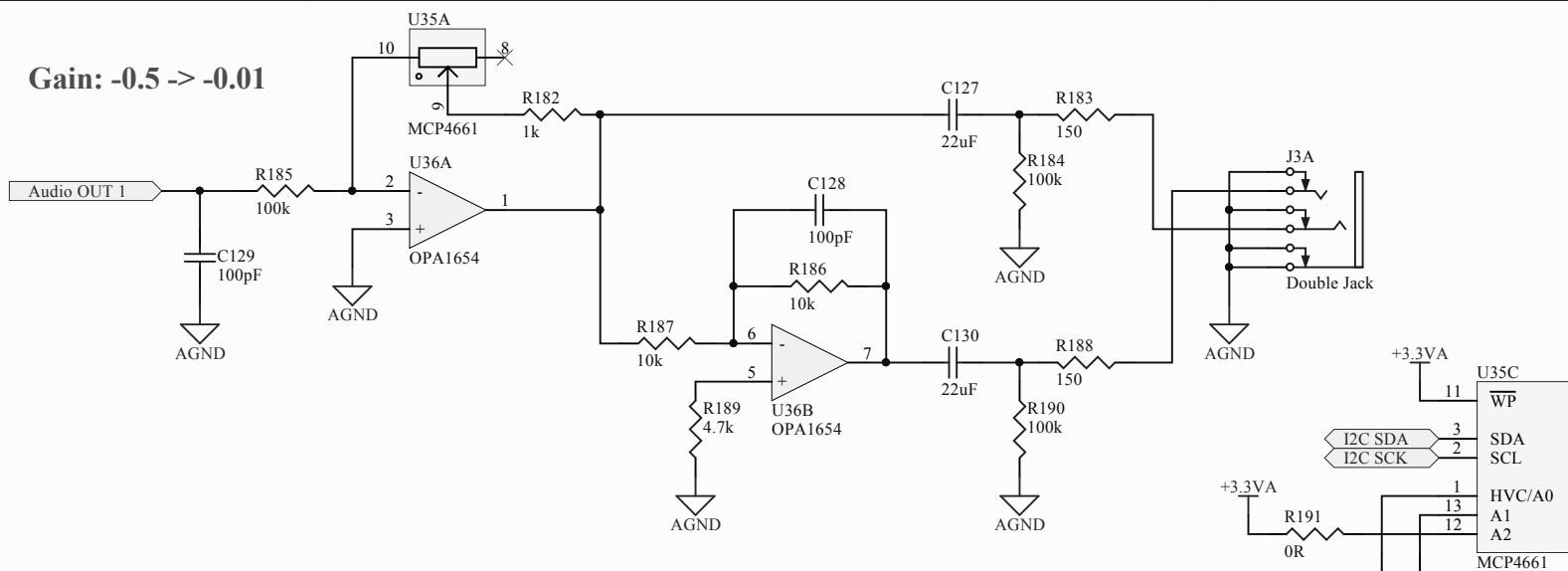
Size	Number
A	

Revision
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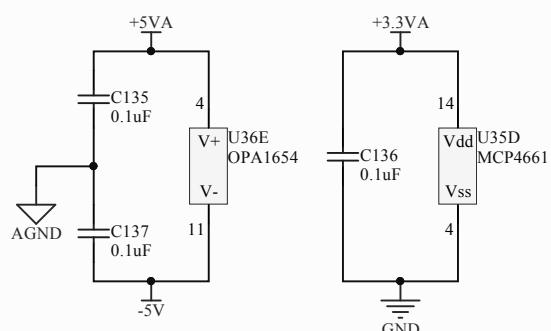
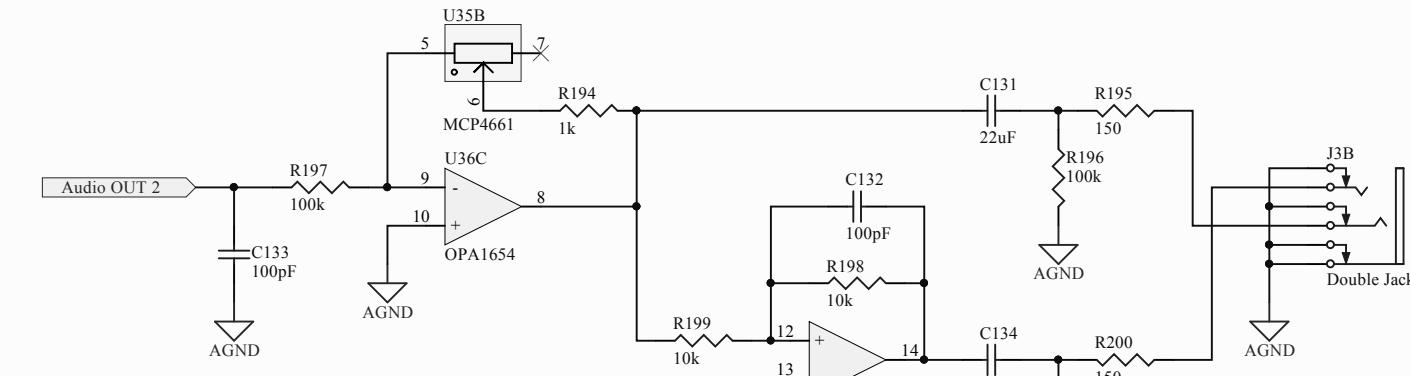
Date: 3/22/2018 Sheet of

File: C:\Users\...\PF7 FilterRoutingBypass.SchDrawn By:

**Gain: -0.5 > -0.01**



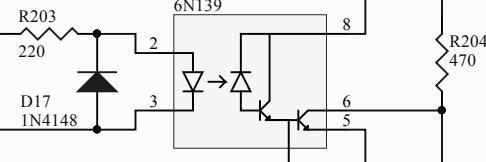
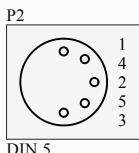
**Output Digi Pot ADDR = 100**



Title

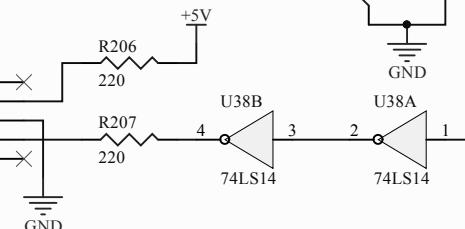
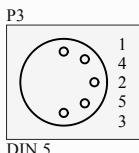
Size	Number	Revision
A		
Date: 3/22/2018	Sheet of	
File: C:\Users\...\PF8_AudioOutput.SchDoc		Drawn By:

A

**MIDI IN**

MIDI

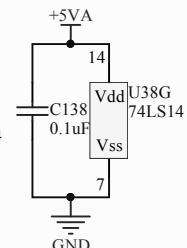
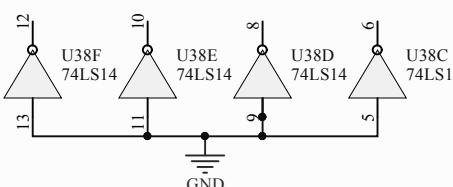
B



GND

**MIDI THRU**

C



D

Title

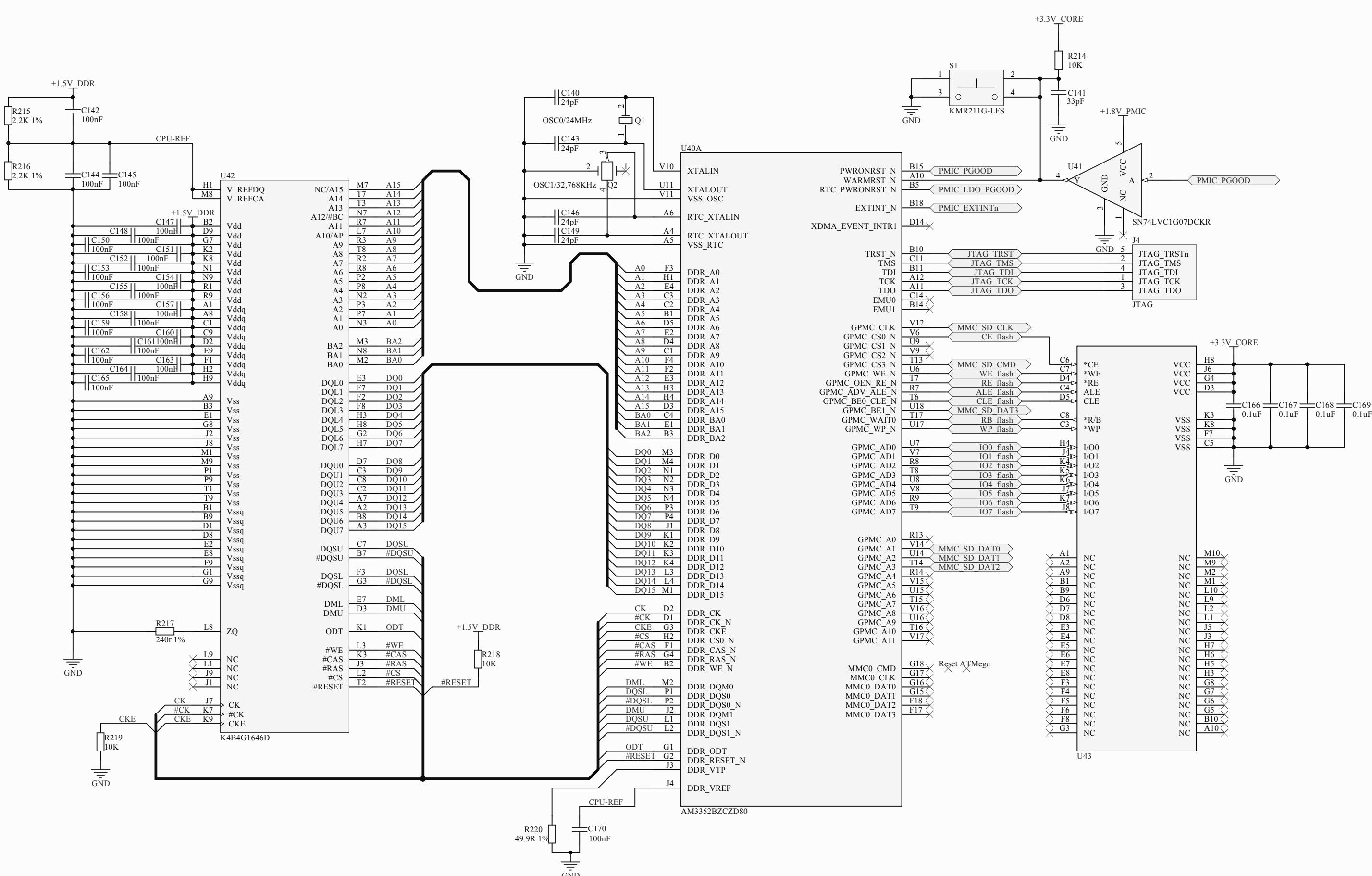
Size A	Number	Revision
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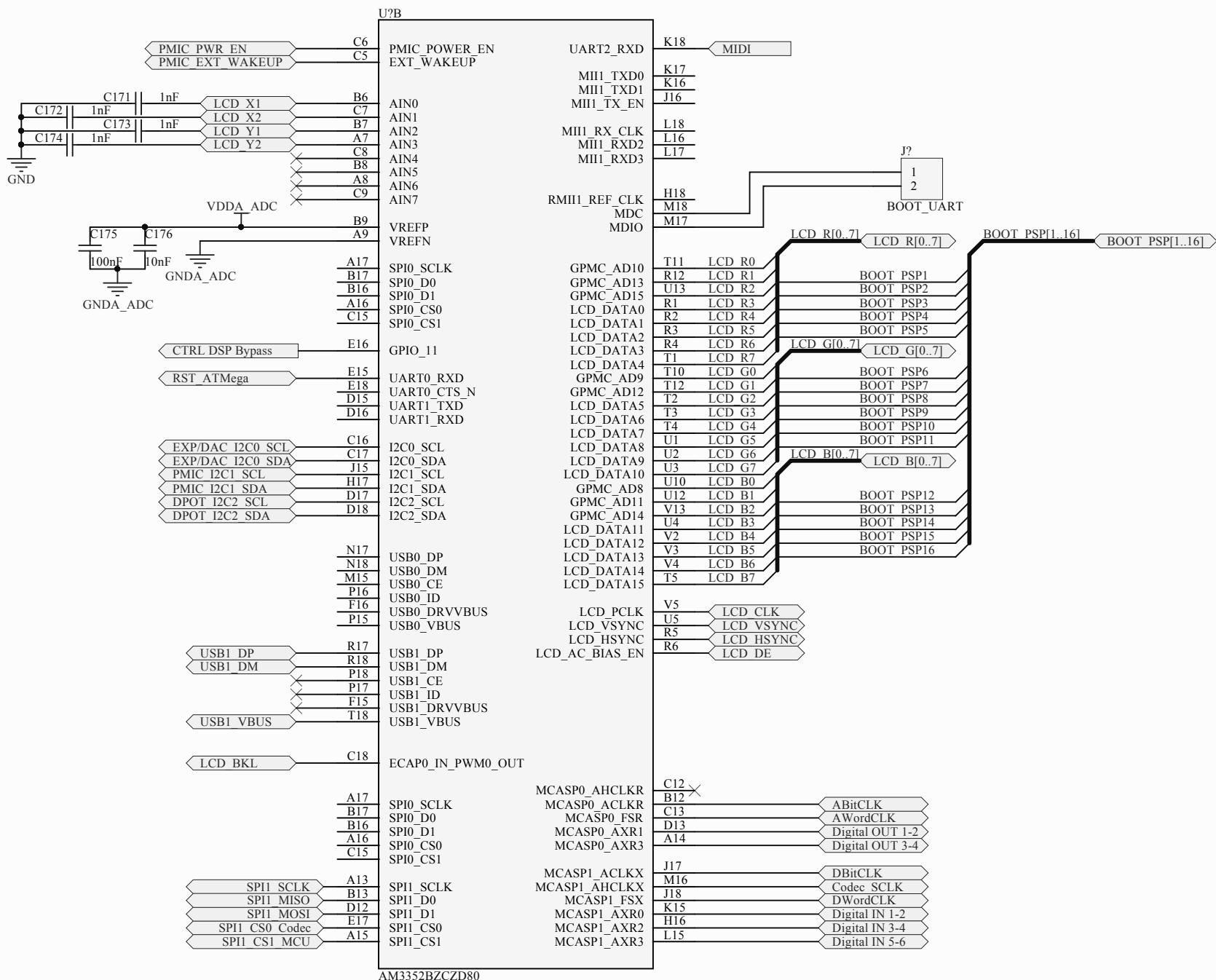
Date: 3/22/2018

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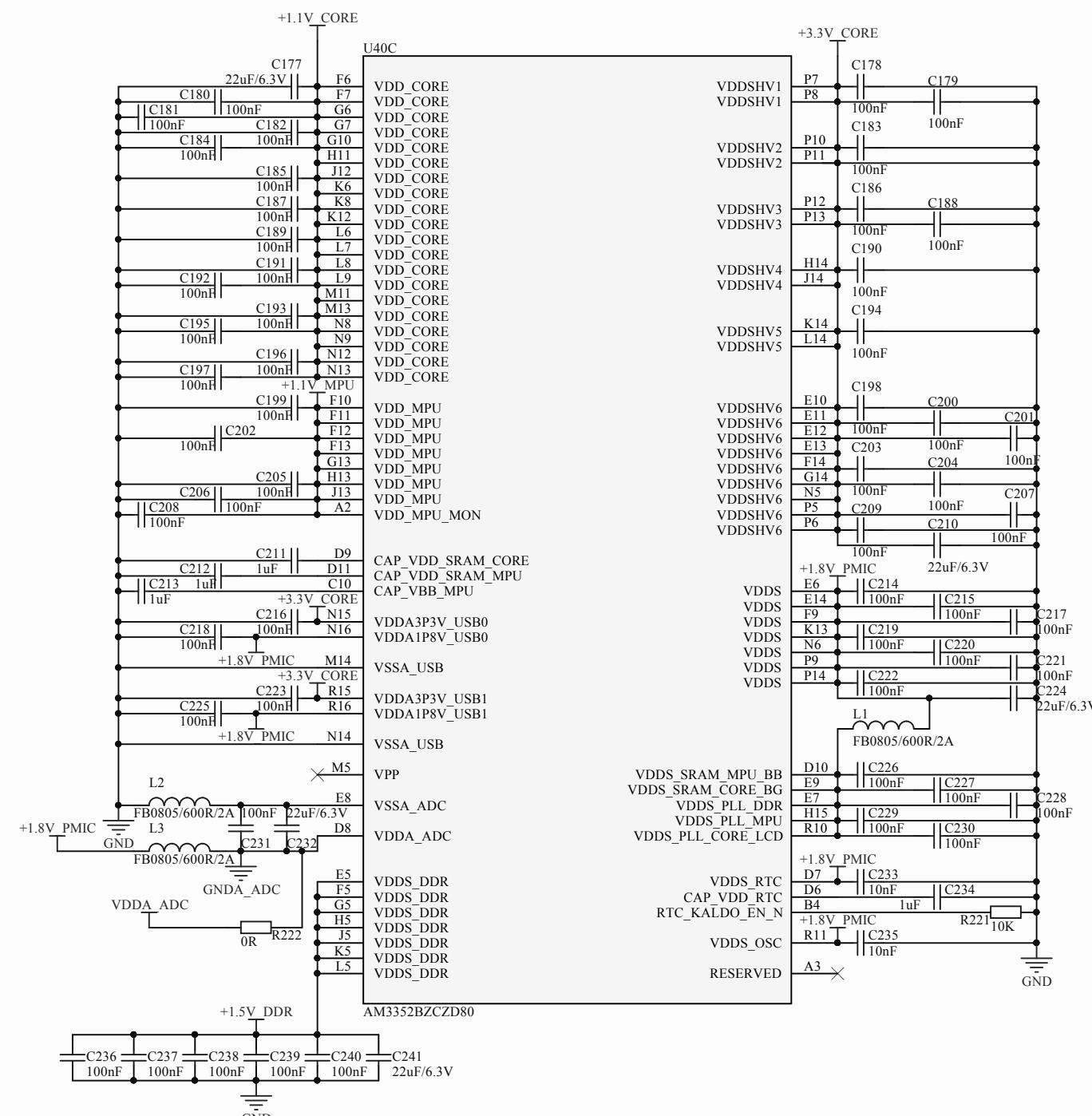


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Size A3	Number	Revision
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Date: 3/22/2018	Sheet of
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Title		
Size	Number	Revision
A3		
Date: 3/22/2018	Sheet of	
File: C:\Users\..\\PP3_ptC_Alim.SchDoc	Drawn By:	

A

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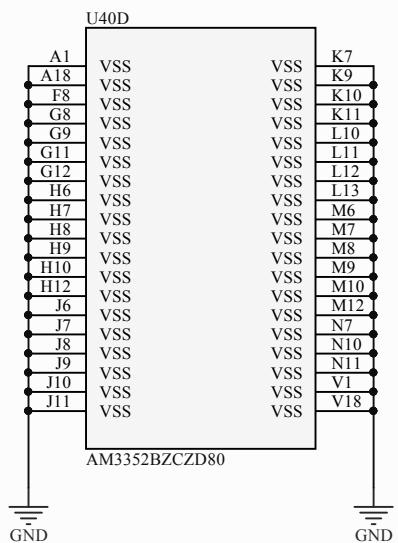
B

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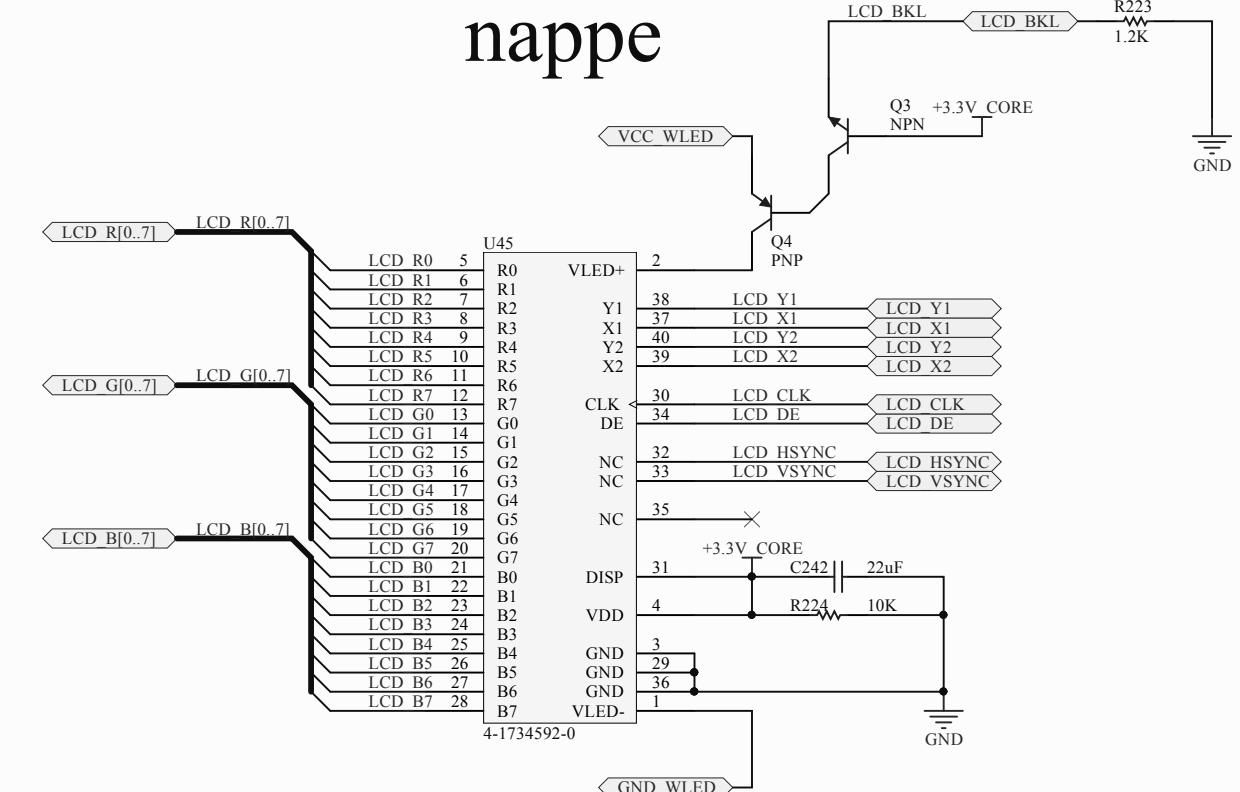
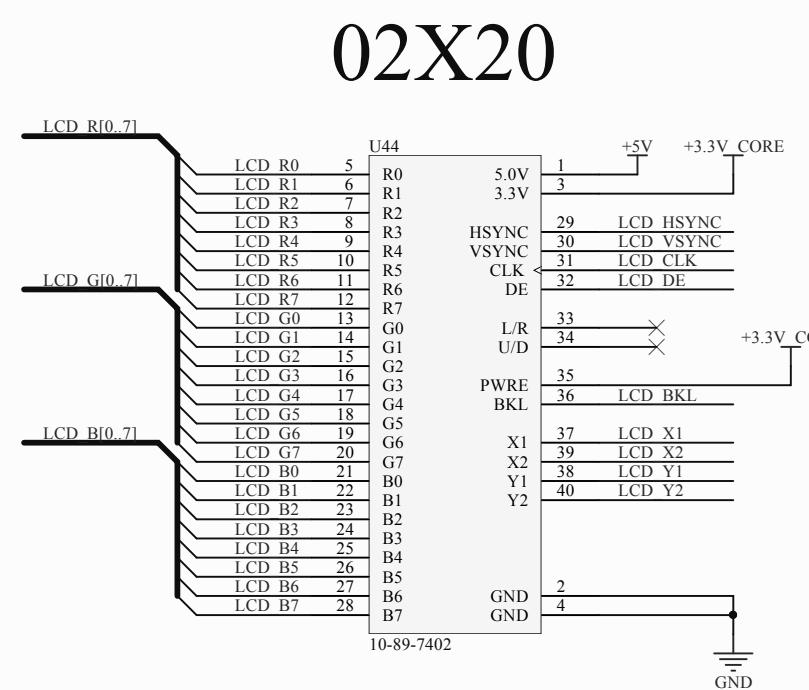
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D

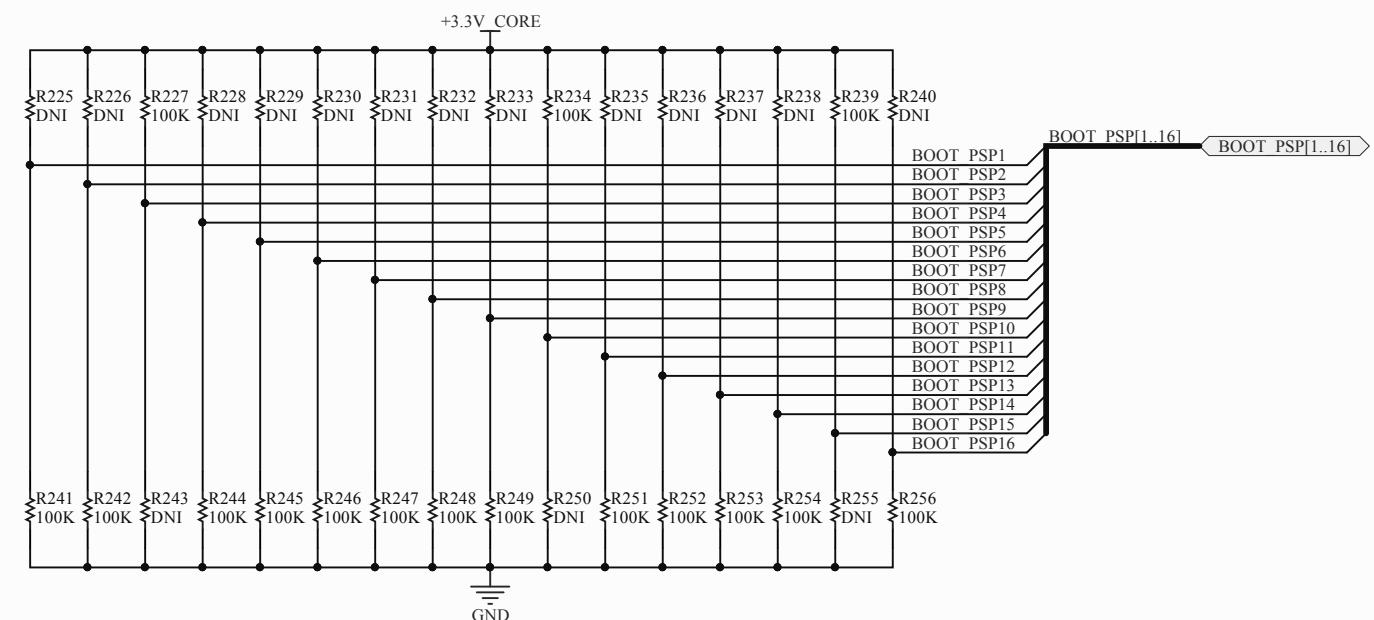


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Date:	3/22/2018	Sheet of
File:	C:\Users\...\PP4.pD	Drawn By:



BOOT



Title		
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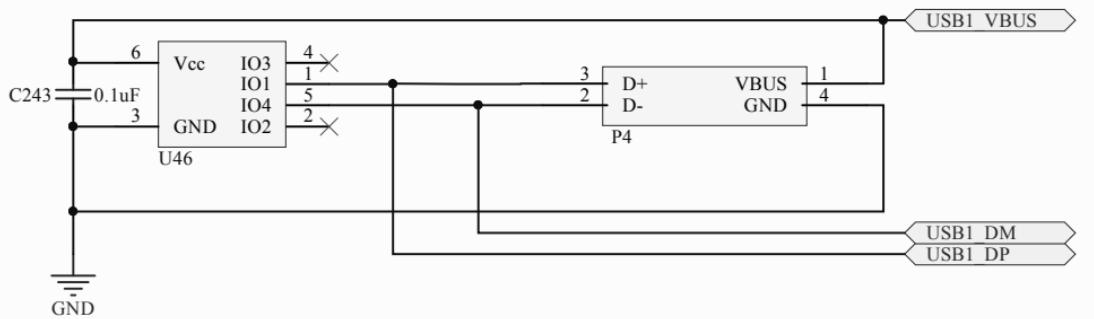
B

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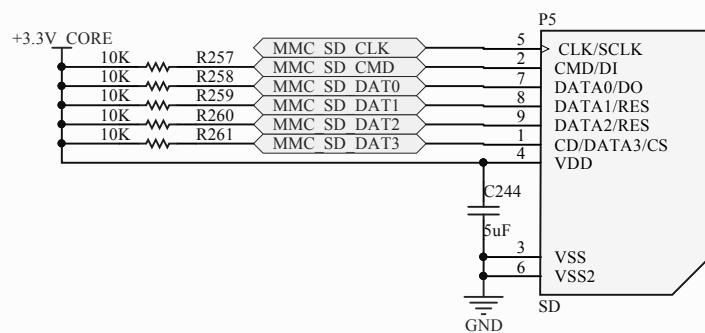
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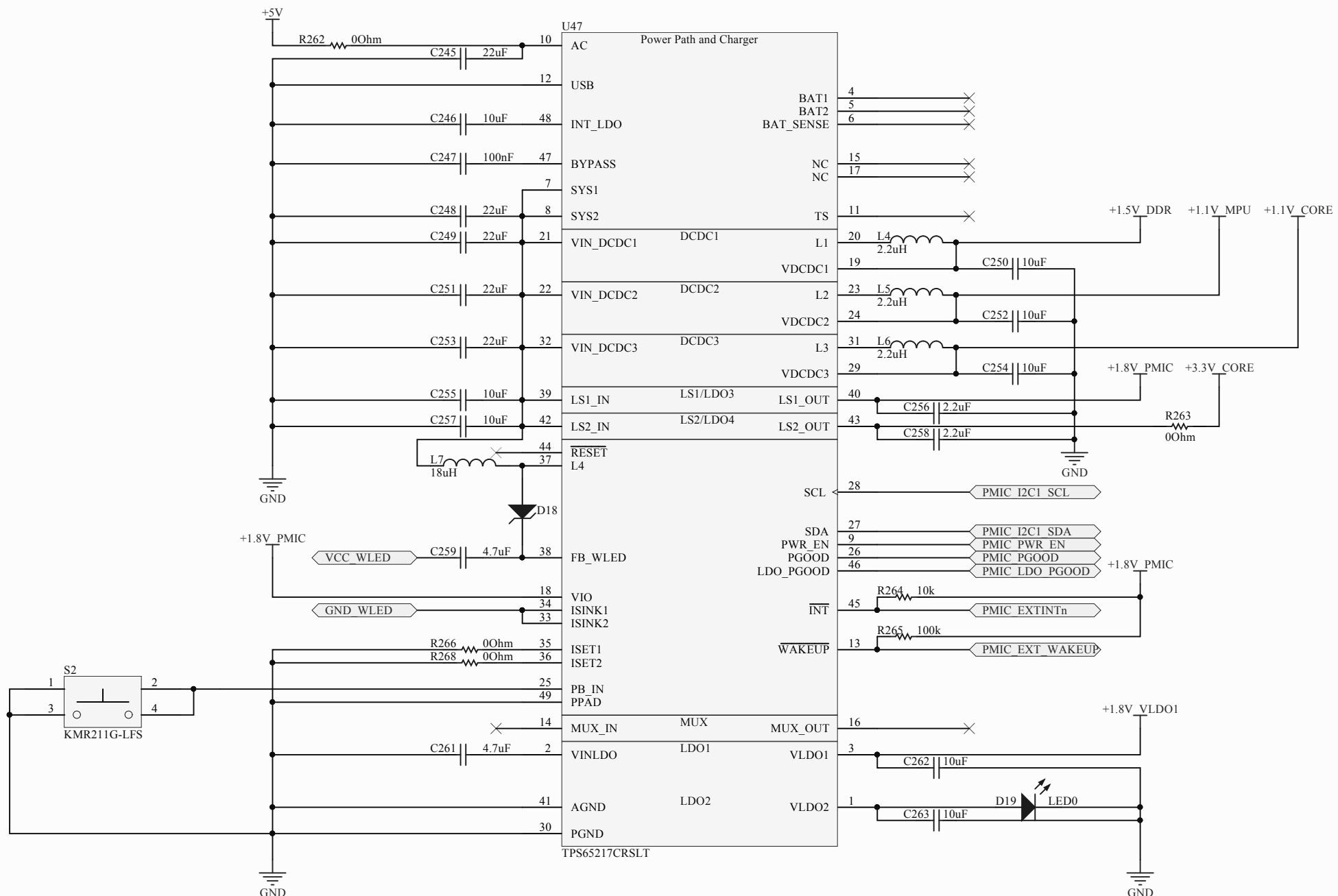


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Date:	3/22/2018	Sheet of
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Title		
Size A3	Number	Revision
Date: 3/22/2018	Sheet of	
File: C:\Users\..\PX_PMIC.SchDoc	Drawn By:	

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