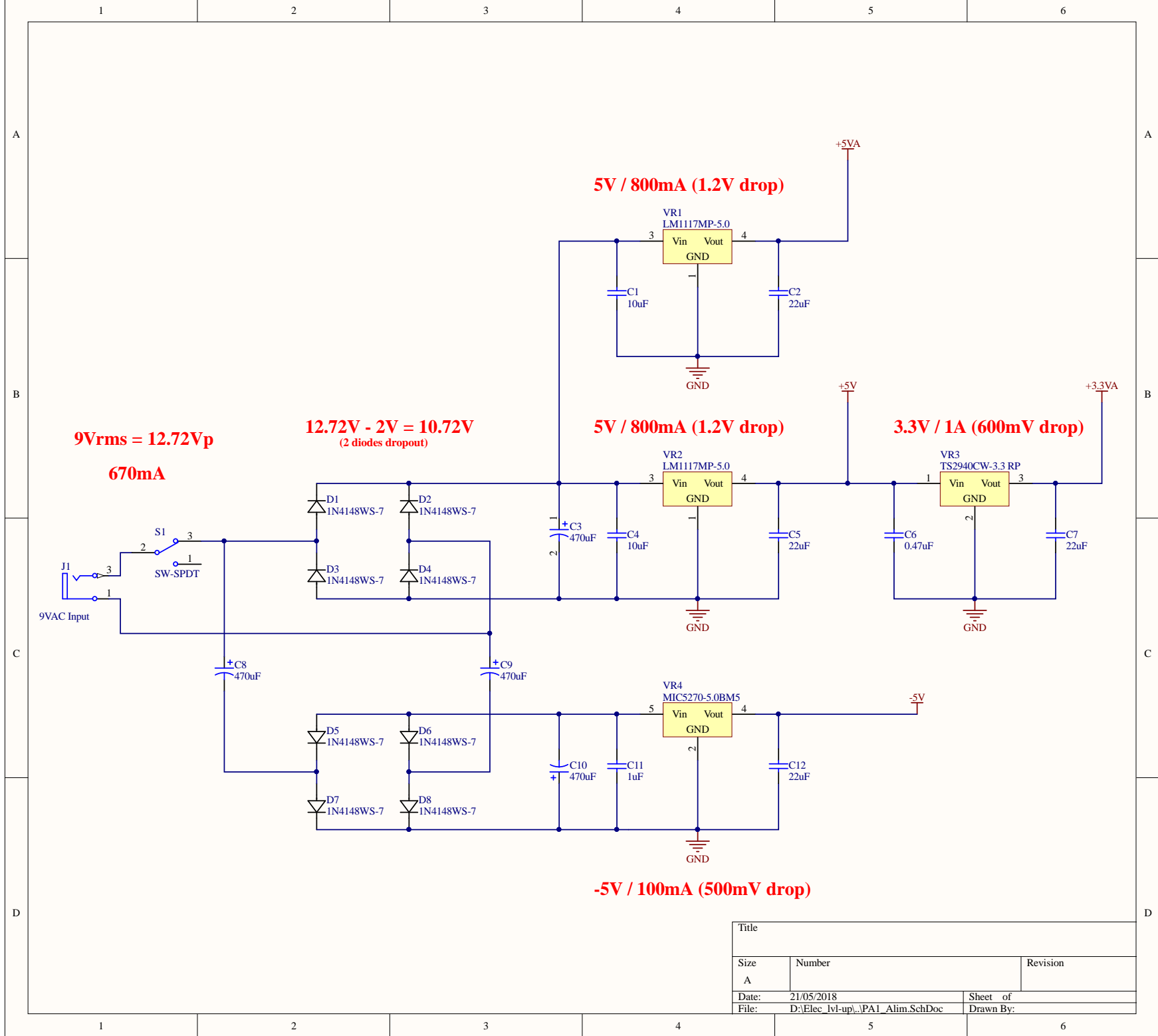
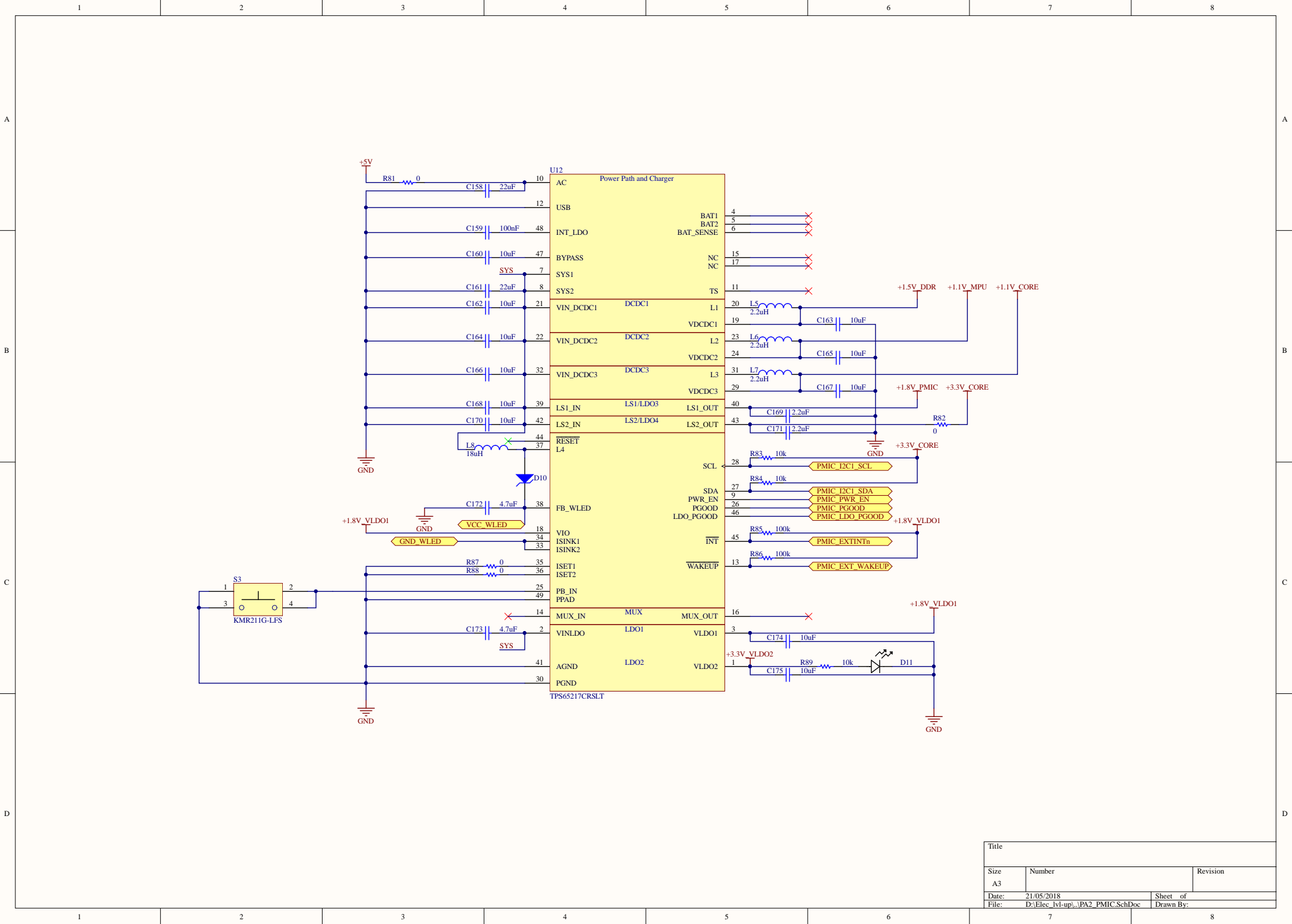


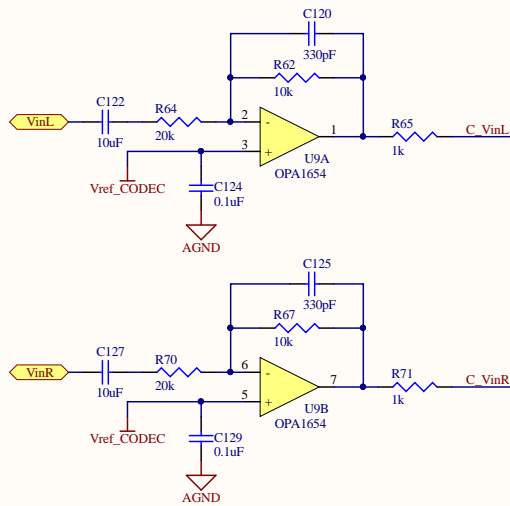
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Size	Number	Revision
A2		
Date:	21/05/2018	Sheet: 01
File:	D:\Files_Av-up_IPD_Main_Connect.SchDoc	Drawn By:



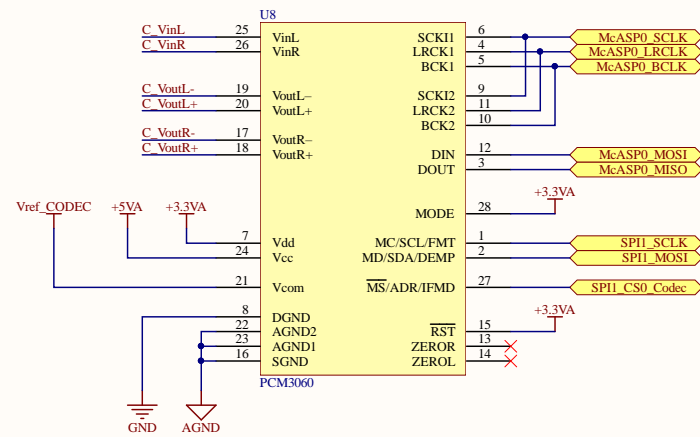


Title		
Size	Number	Revision
A3		
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File:	D:\Elec_lvl-up\...PA2_PMIC.SchDoc	Drawn By:

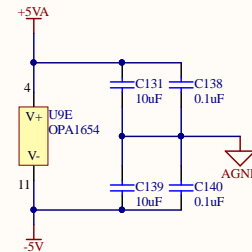
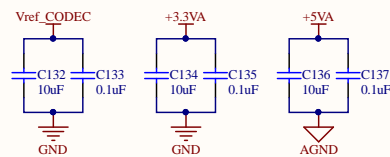
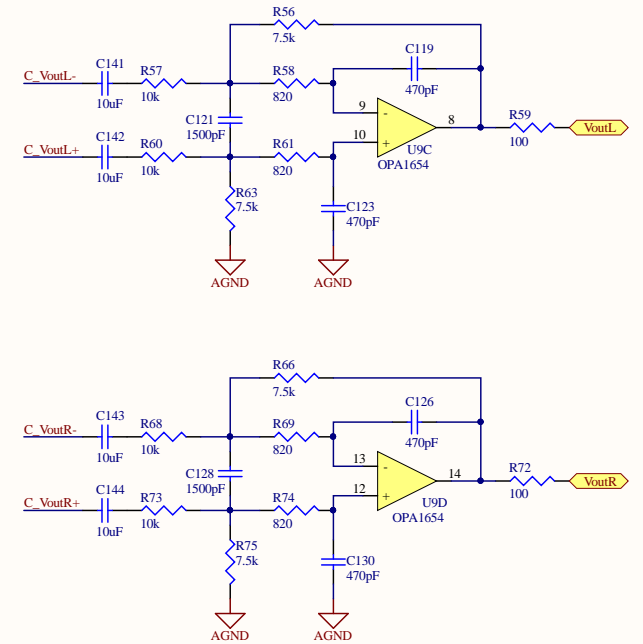
Input



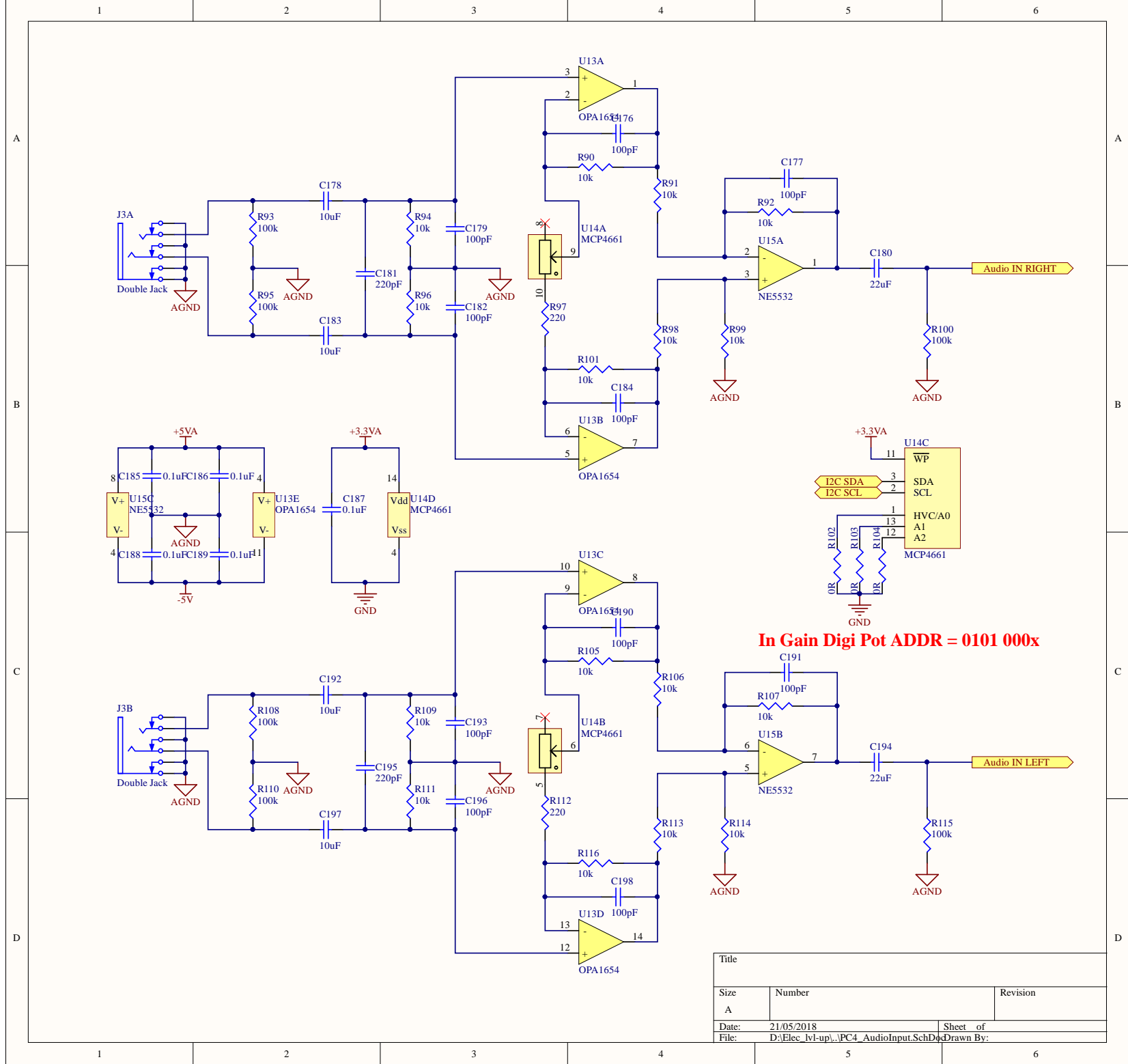
24bit Audio Codec 2 IN, 2 OUT



Output

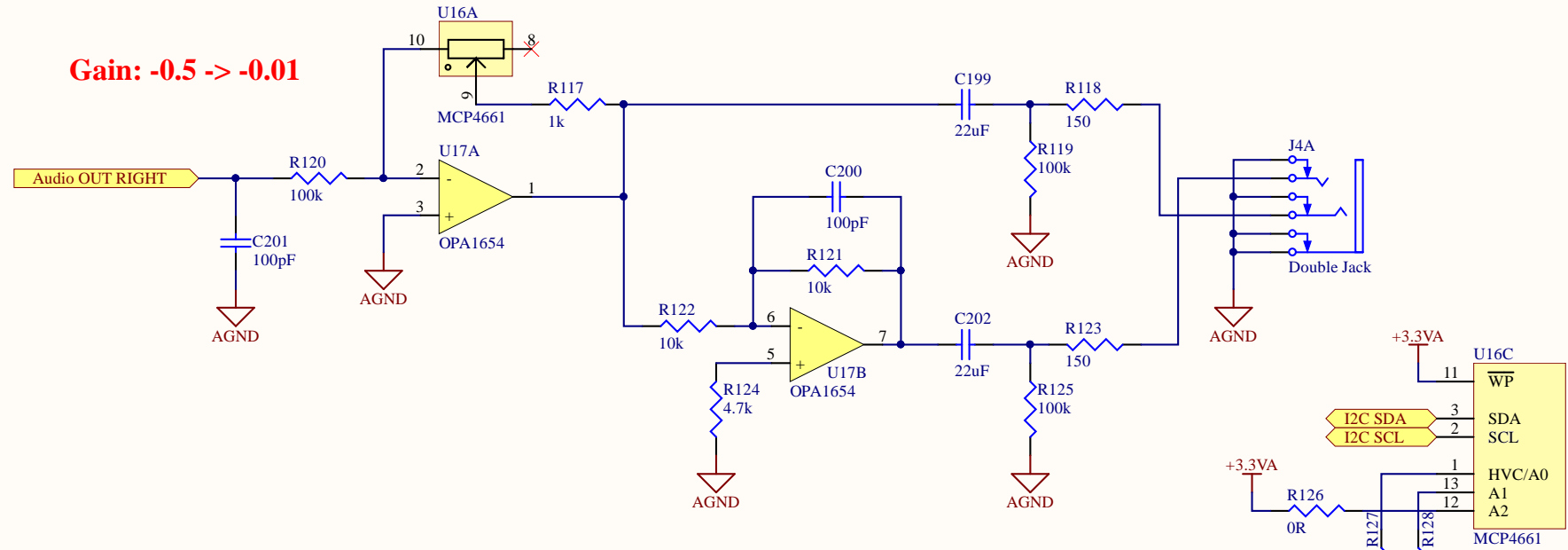


Title		
Size	Number	Revision
A		
Date:	21/05/2018	Sheet of
File:	D:\Elec\jvl-up\PC0_Codec.SchDoc	Drawn By:

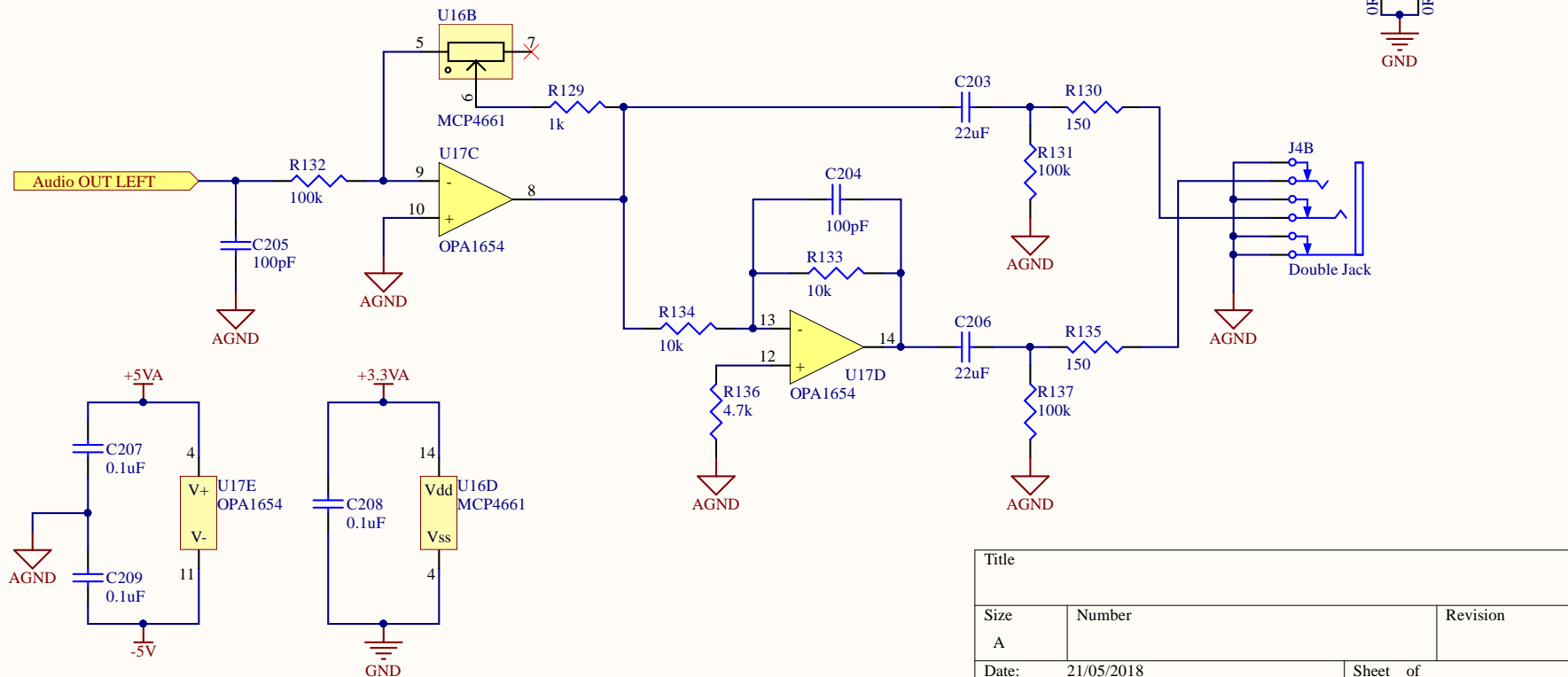


Title		
Size	Number	Revision
A		
Date:	21/05/2018	Sheet of
File:	D:\Elec_lvl-up\...\PC4_AudioInput.SchDoc	Drawn By:

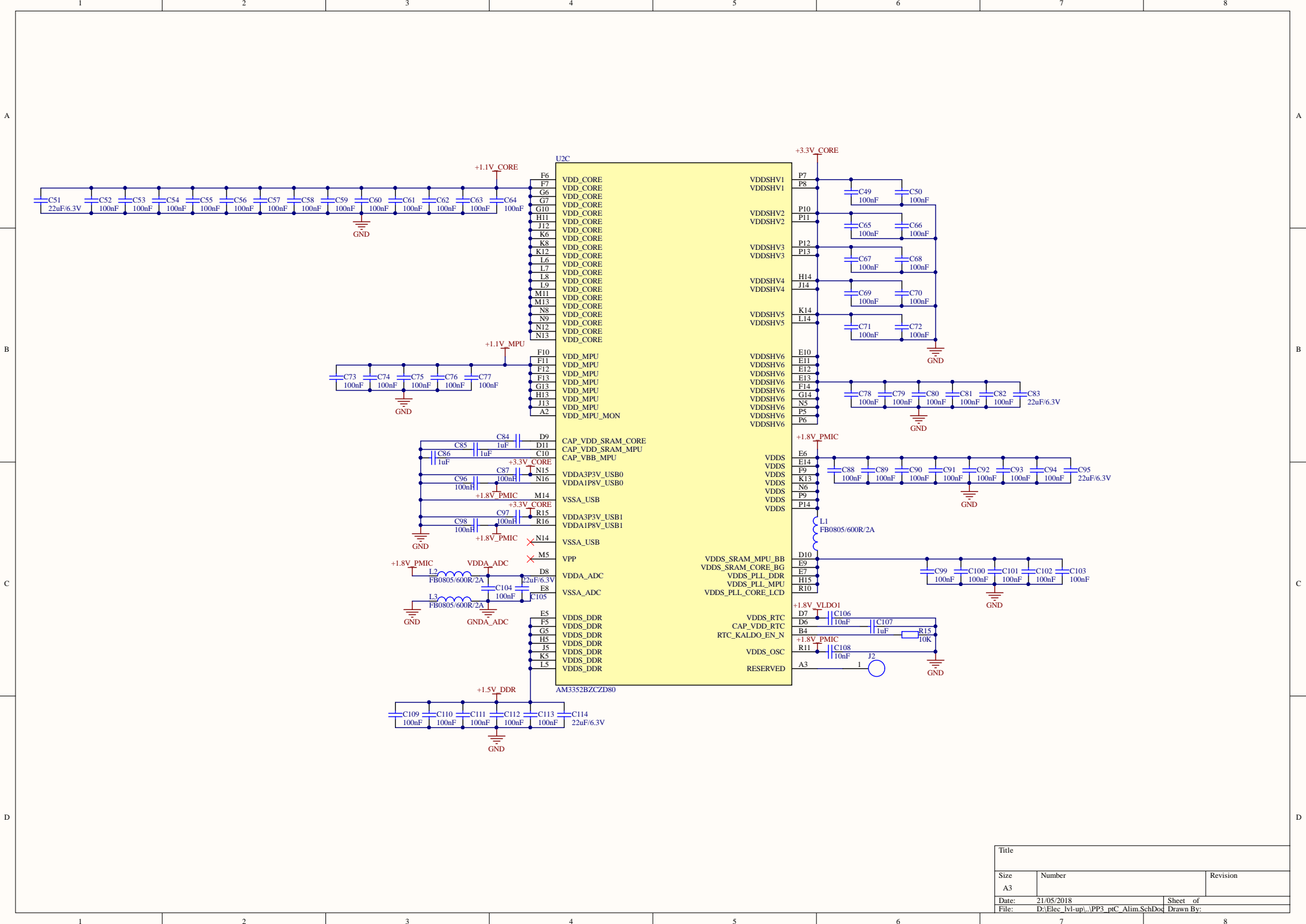
Gain: -0.5 -> -0.01



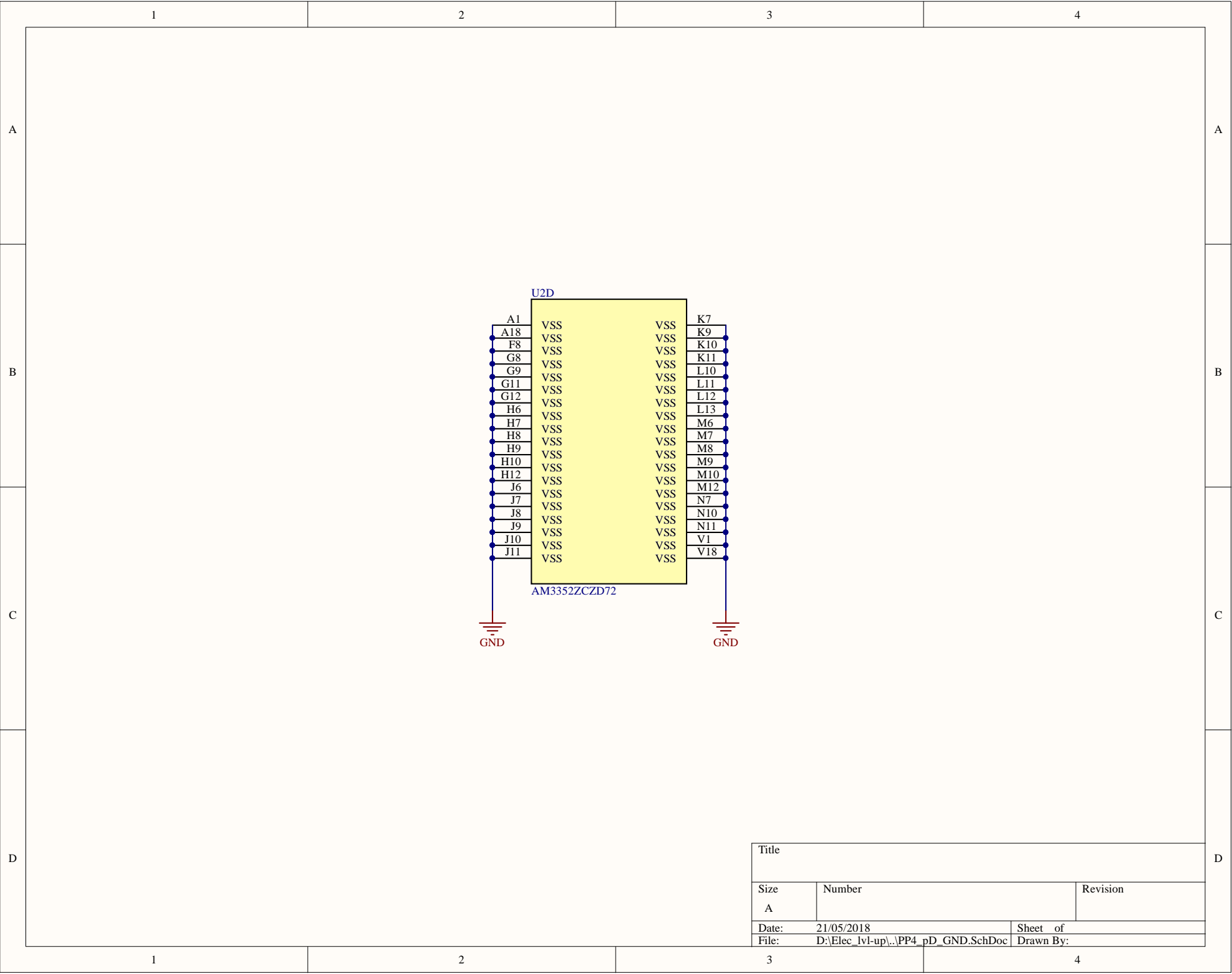
Output Digi Pot ADDR = 0101 100x



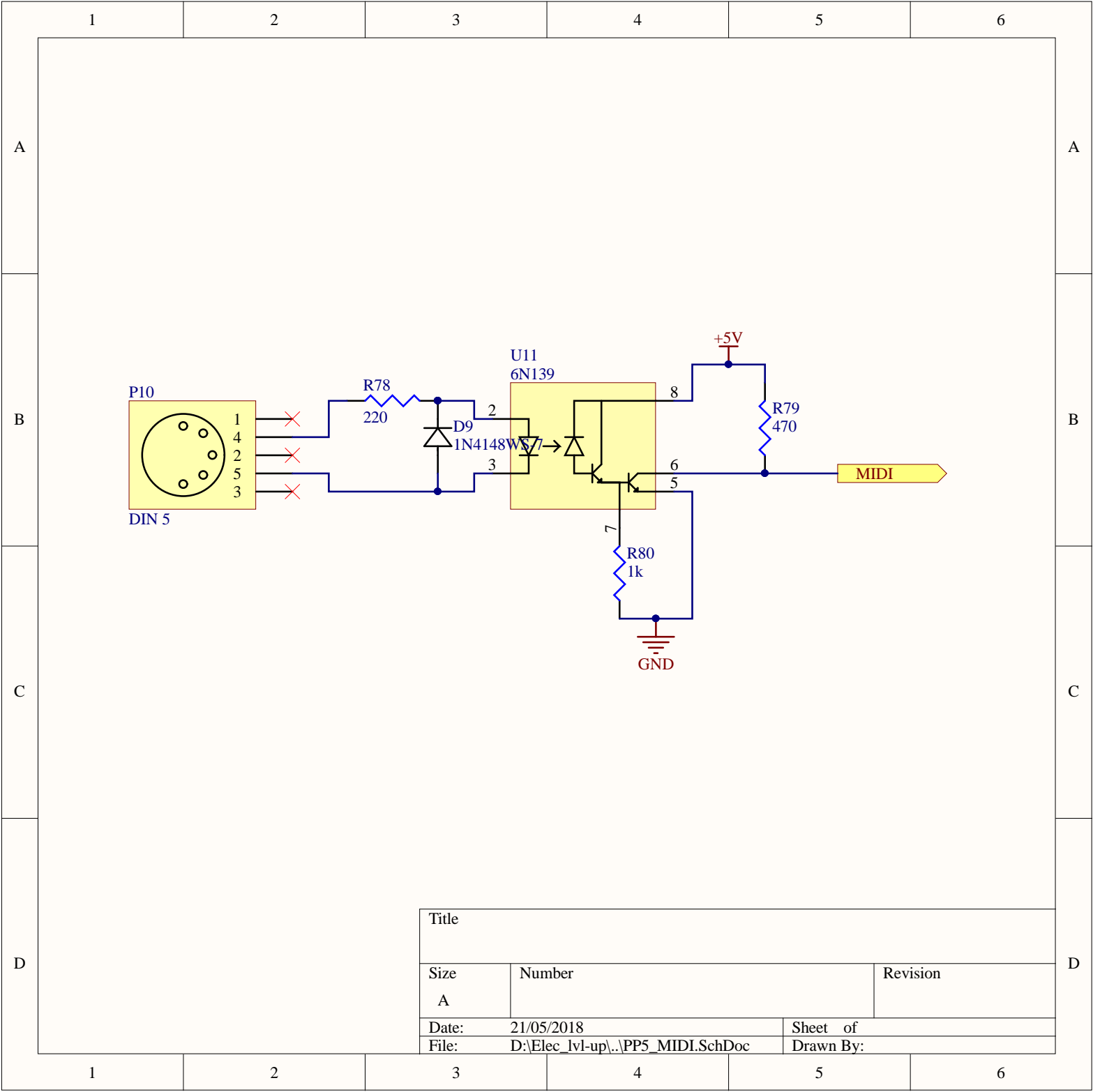
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A		
Date:	21/05/2018	Sheet of
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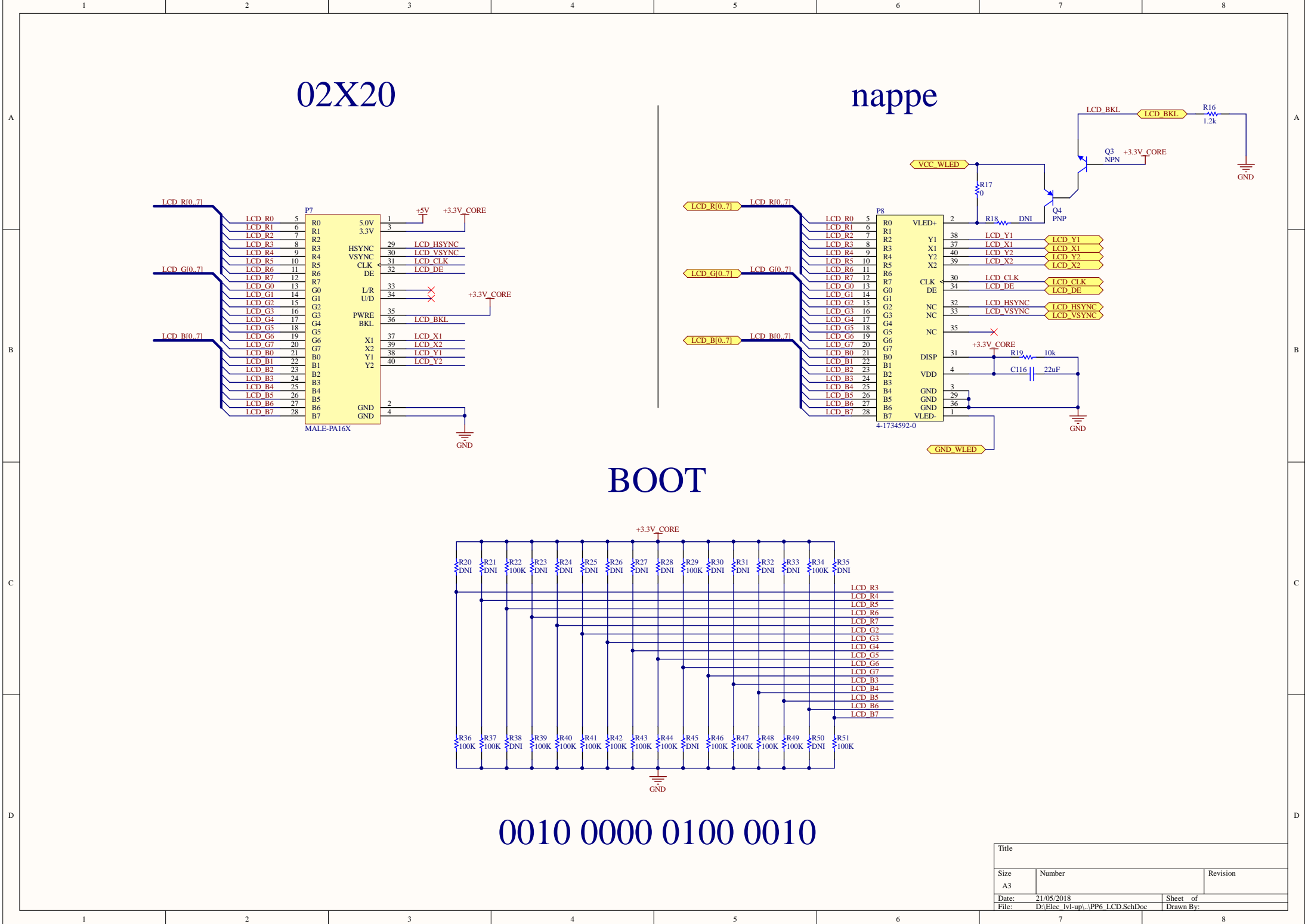


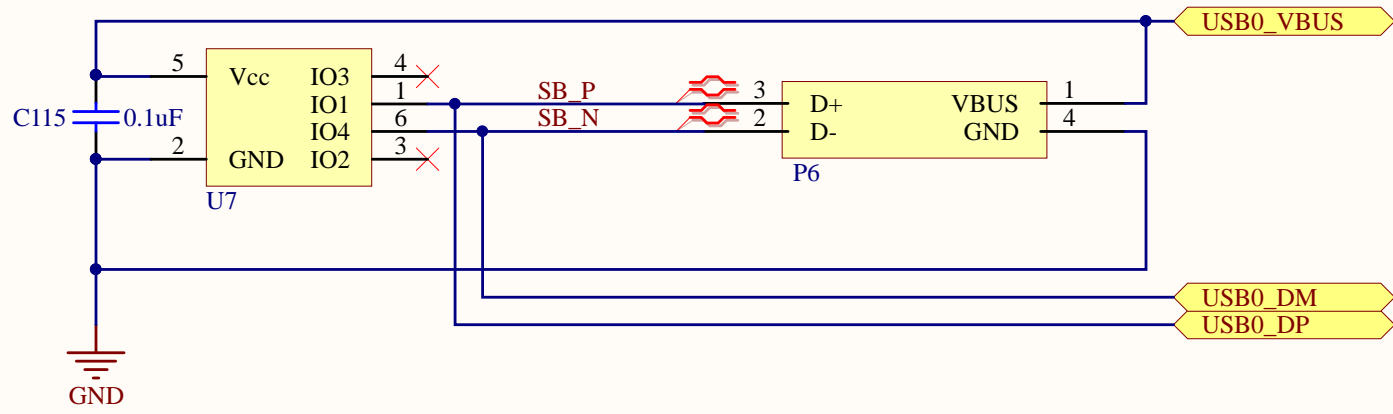
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Size	Number	Revision
A3		
Date:	21/05/2018	Sheet of
File:	D:\Elec_lv1-up\PP3_ptC_Alim.SchDoc	Drawn By:

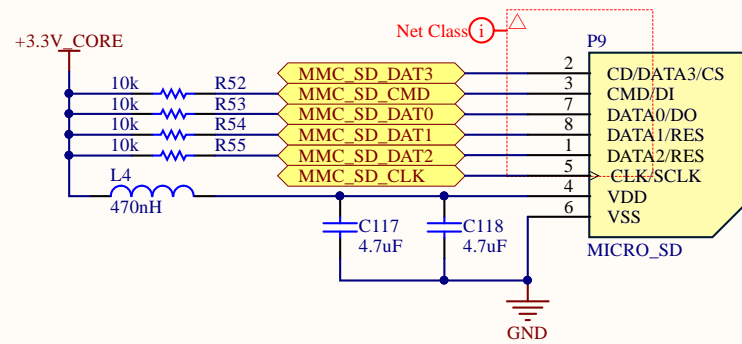


Title			
Size	Number		Revision
A			
Date:	21/05/2018	Sheet of	
File:	D:\Elec_lvl-up\..\PP4_pD_GND.SchDoc	Drawn By:	









Title			
Size	Number		Revision
A			
Date:	21/05/2018	Sheet of	
File:	D:\Elec_lvl-up\..\PP8_MMC.SchDoc	Drawn By:	