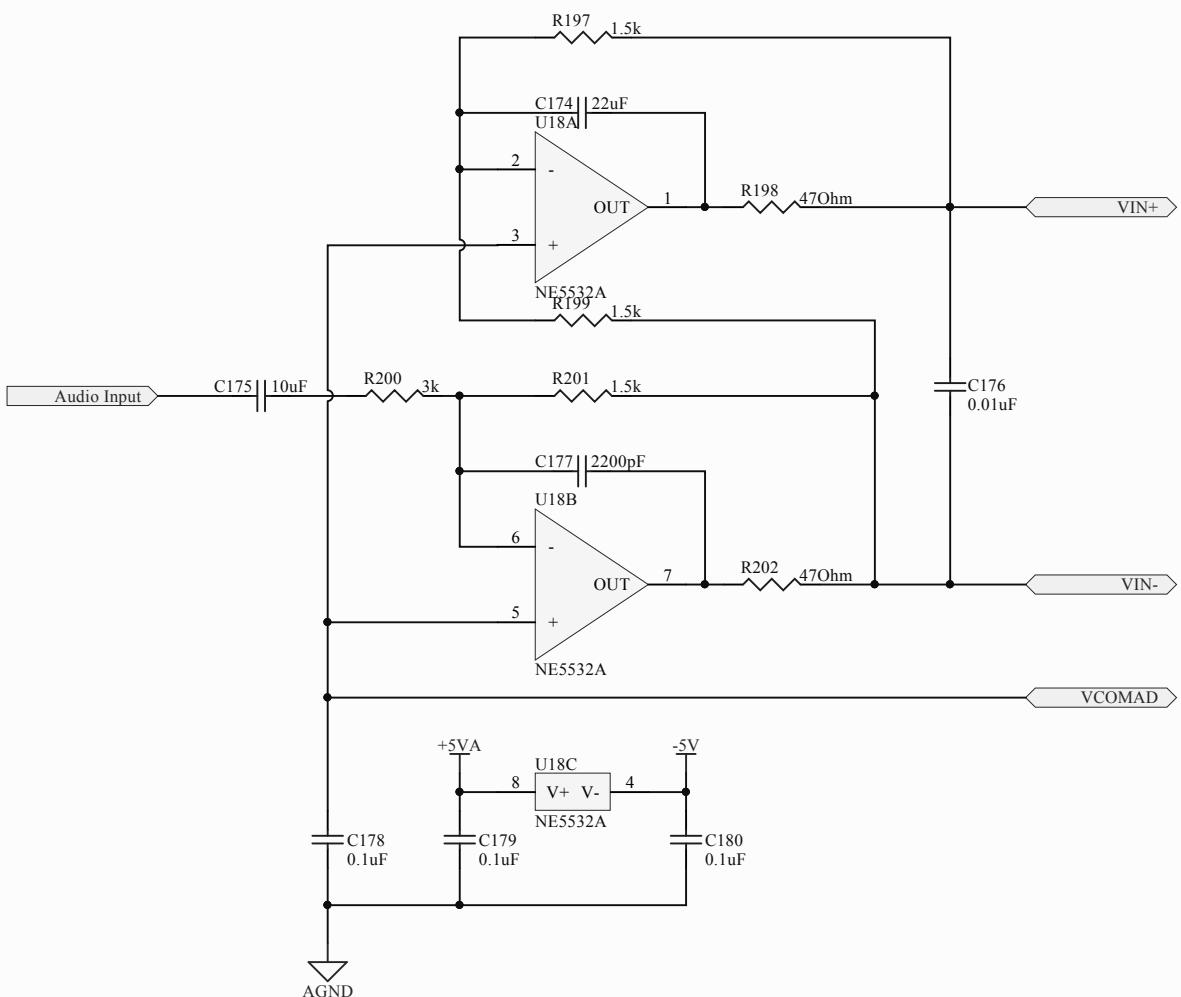
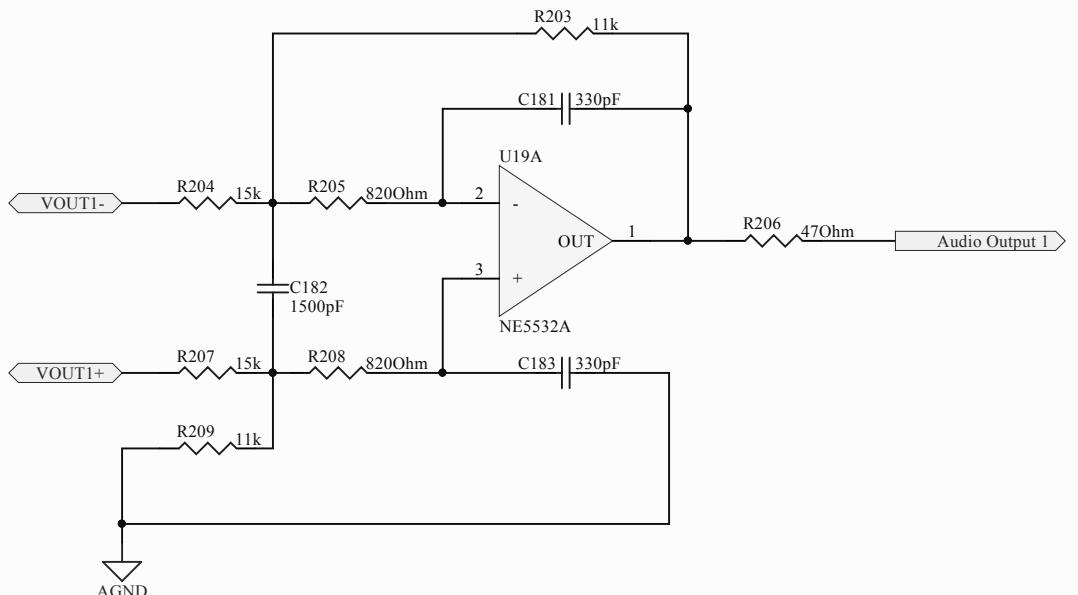


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Date: 4/6/2018	Sheet of	
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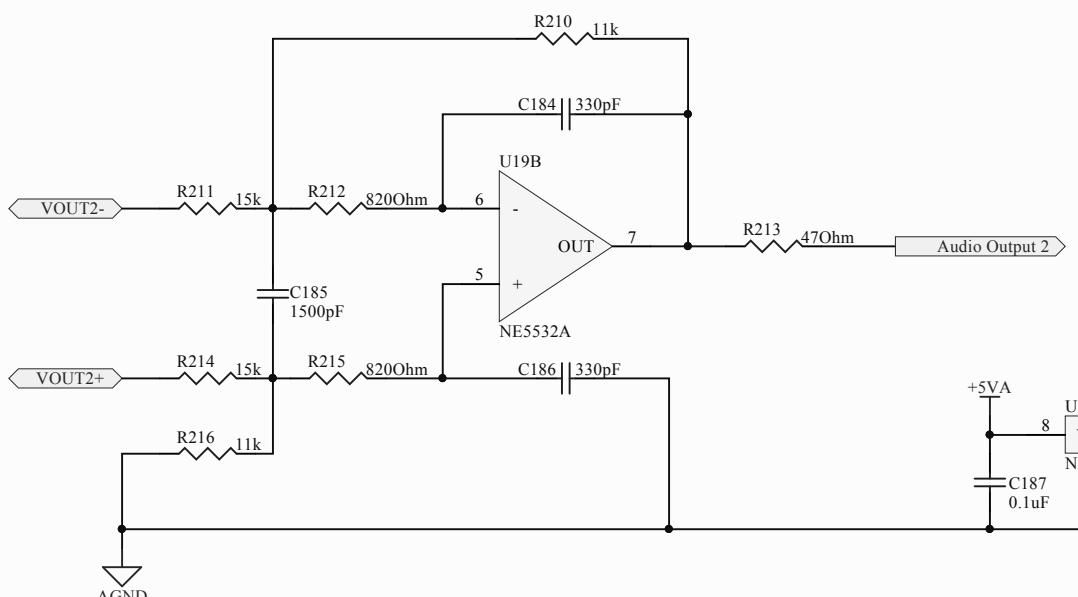
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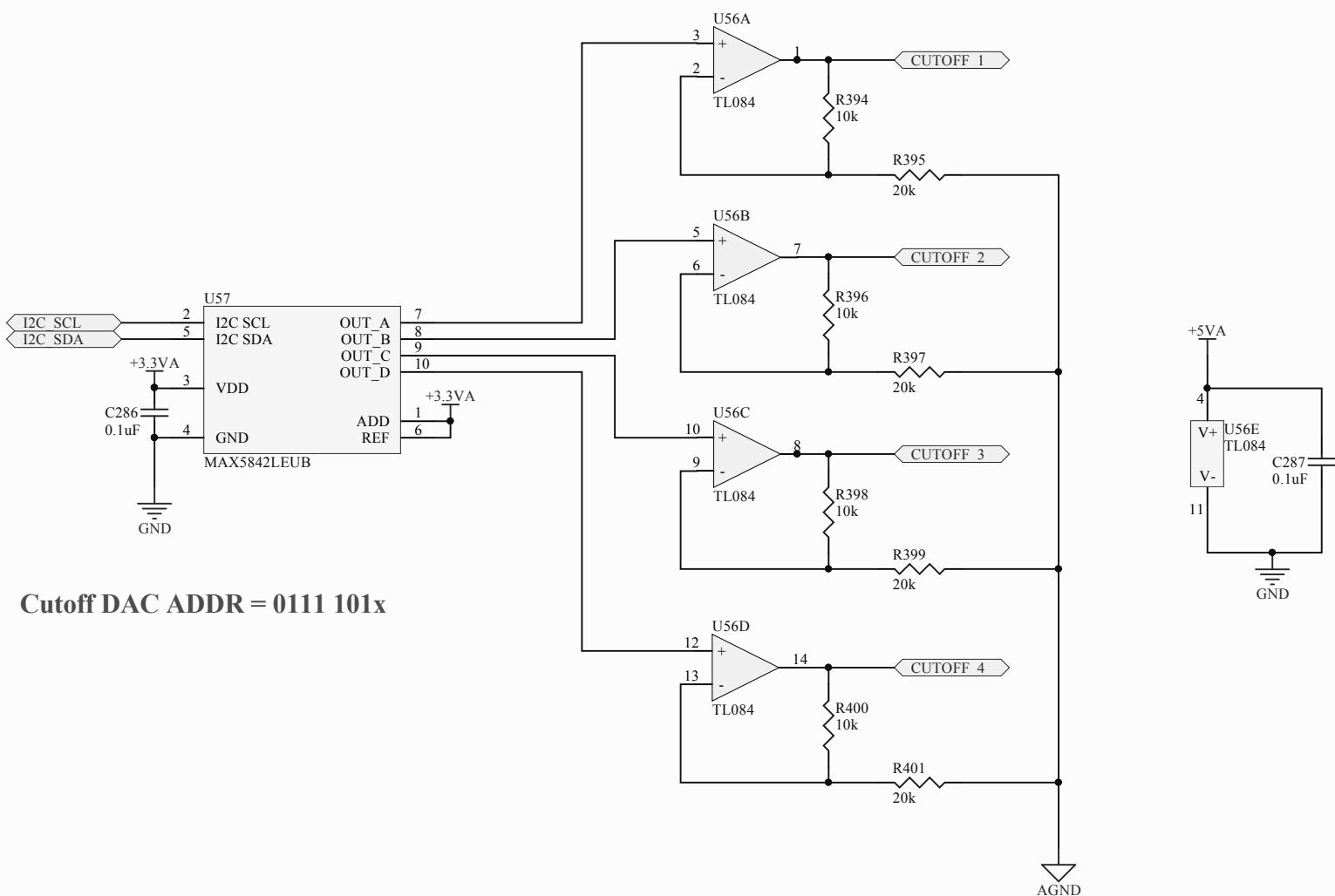
B

C

D

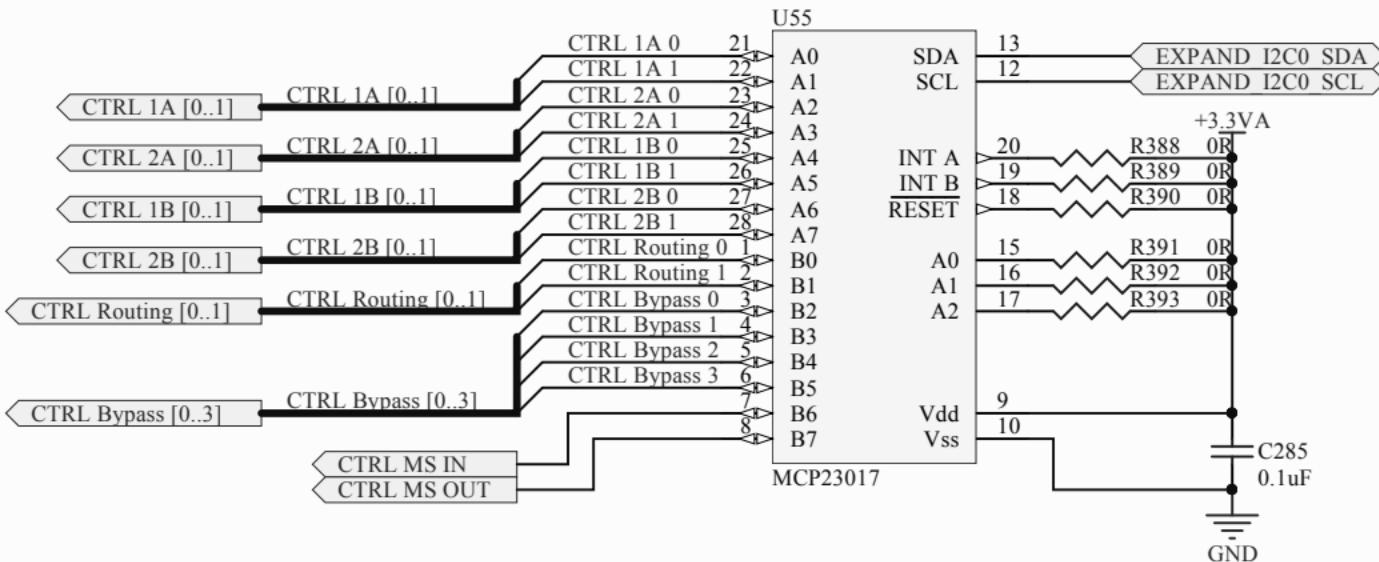


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Size A4	Number	Revision
Date: 4/6/2018		Sheet of
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Title		
Size A4	Number	Revision
Date: 4/6/2018		Sheet of
File: C:\Users\PC4 DAC Cutoff.SchDoc		Drawn By:

# Expander Address 0100111



Title

Size  
A

Number

Revision

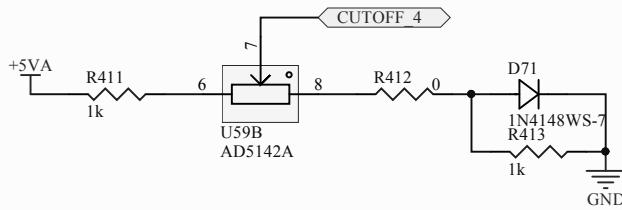
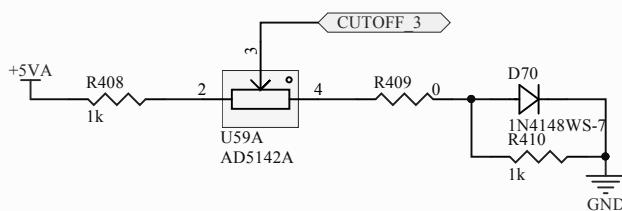
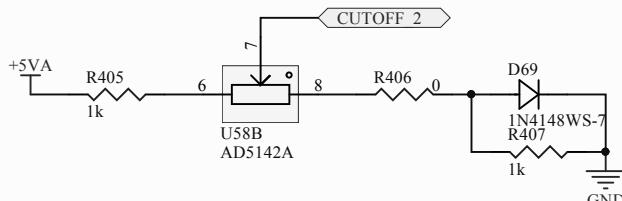
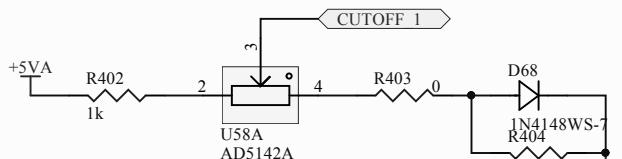
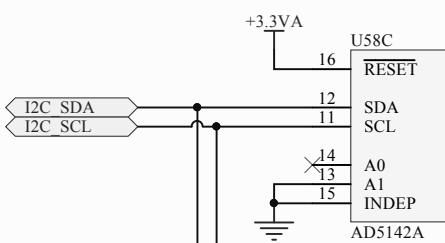
Date: 4/6/2018

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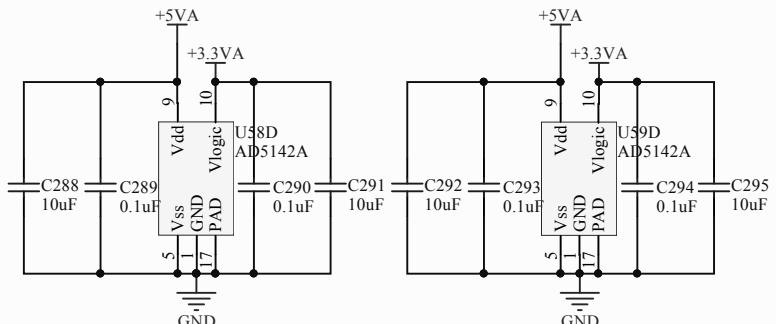
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Drawn By:

## Cutoff 1-2 Digi Pot ADDR = 0101 110x



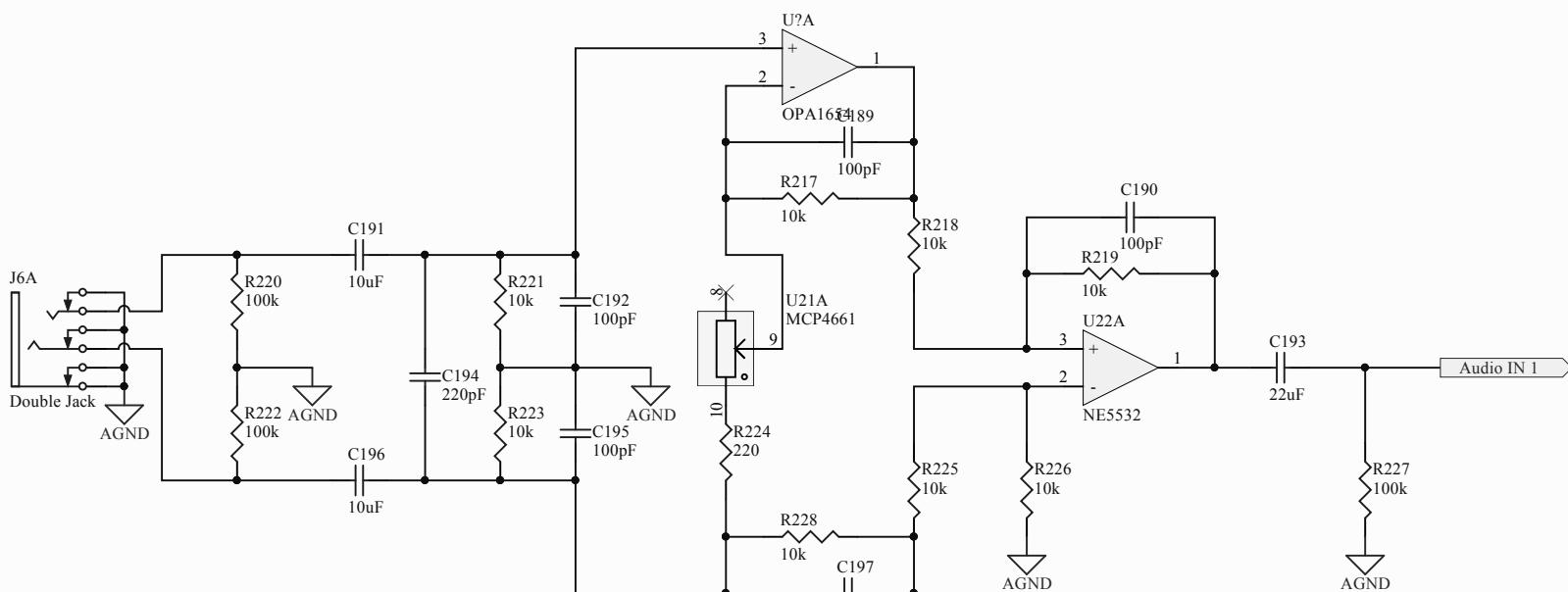
## Cutoff 3-4 Digi Pot ADDR = 0101 111x



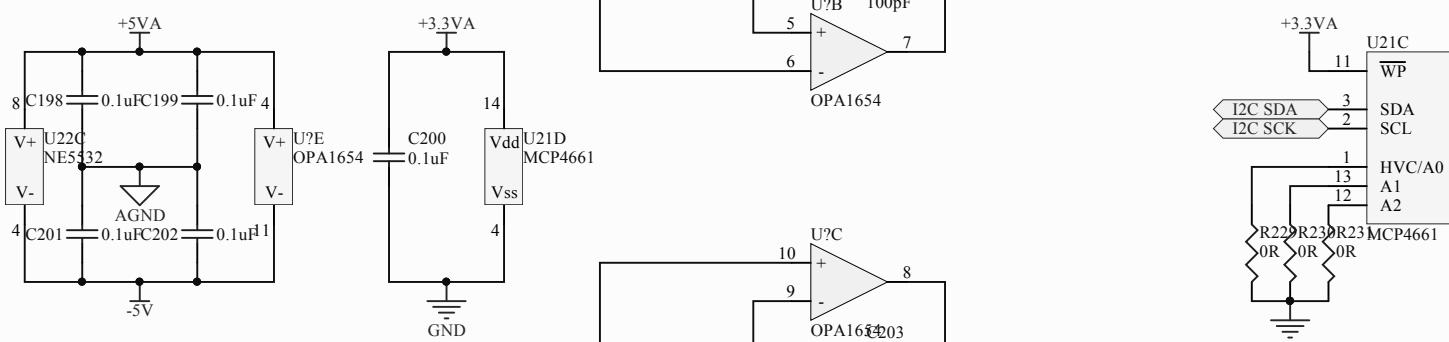
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A		
Date:	4/6/2018	Sheet of
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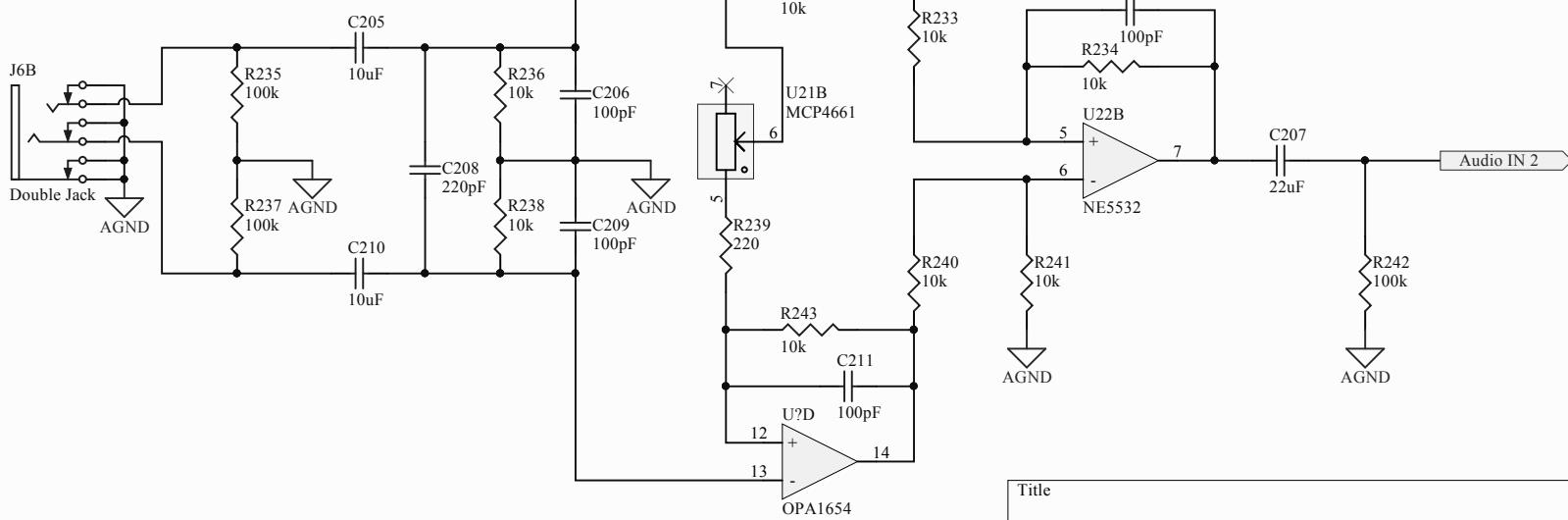
A



B

**In Gain Digi Pot ADDR = 0101 000x**

C

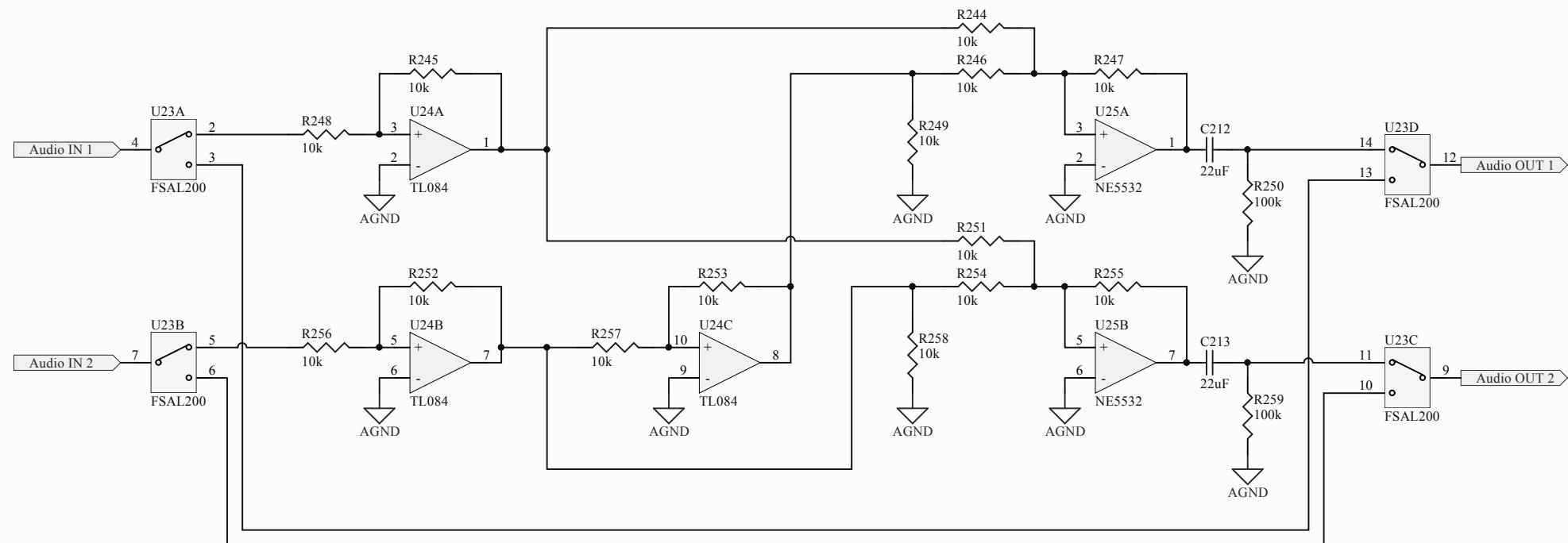


D

Title

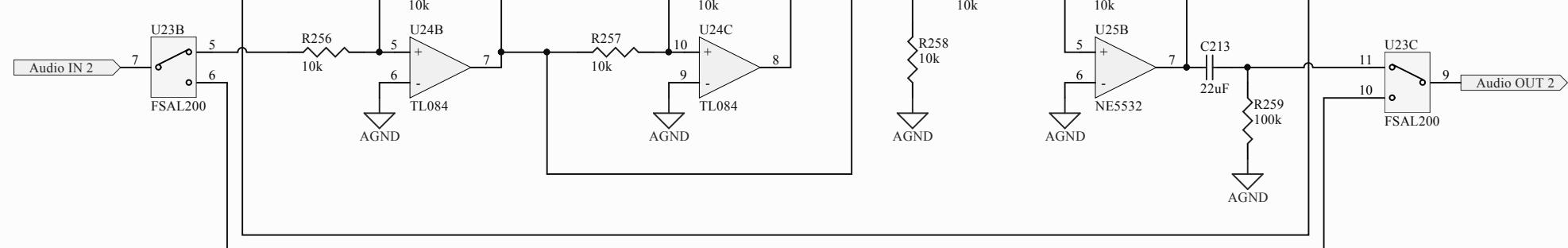
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Date: 4/6/2018	Sheet of	
File: C:\Users\...\PF2_AudioInput.SchDoc		Drawn By:

A



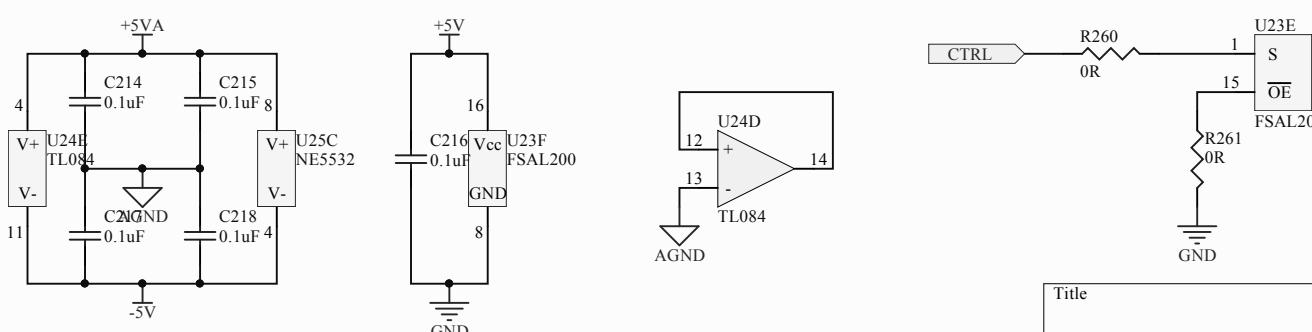
A

B



B

C



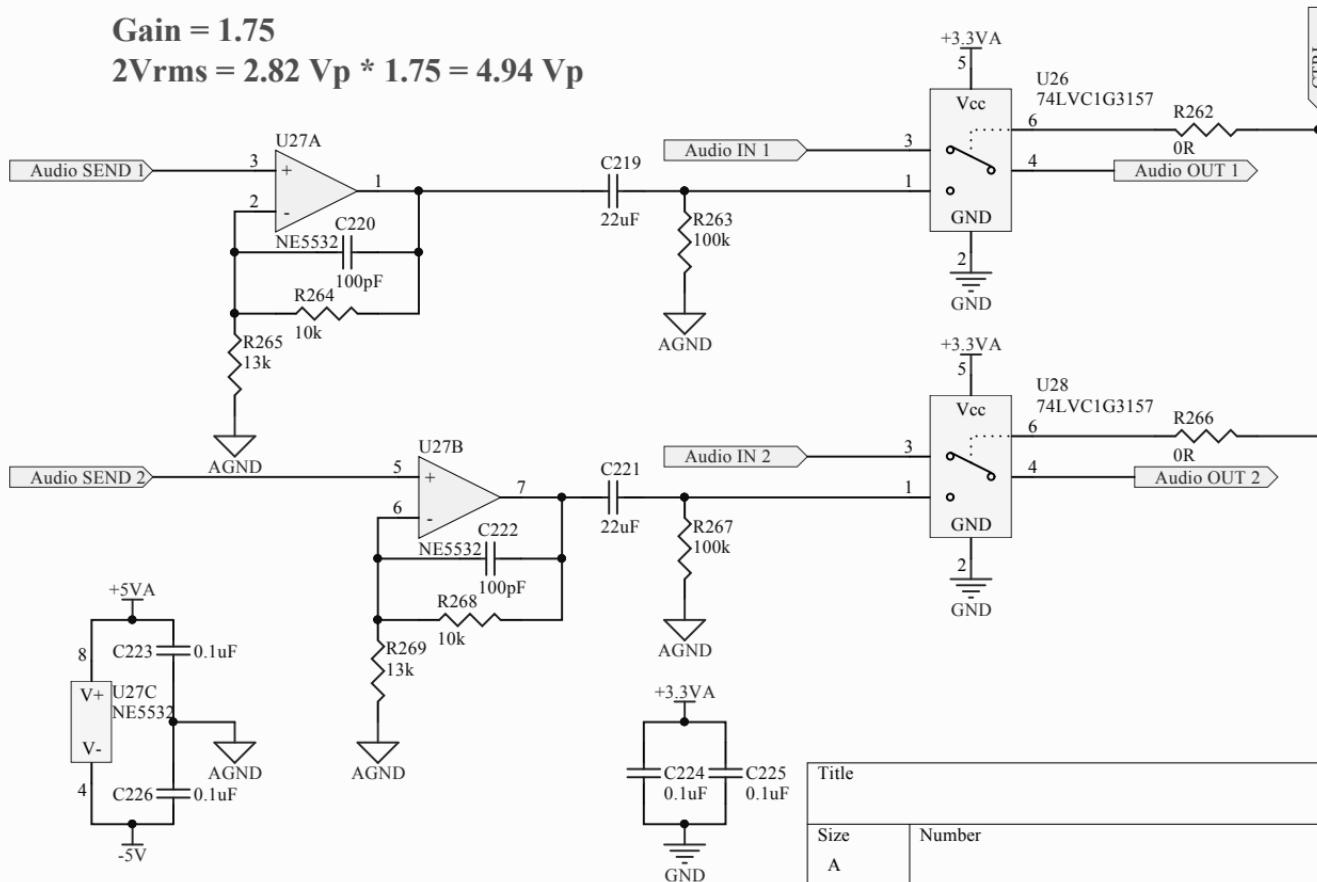
C

D

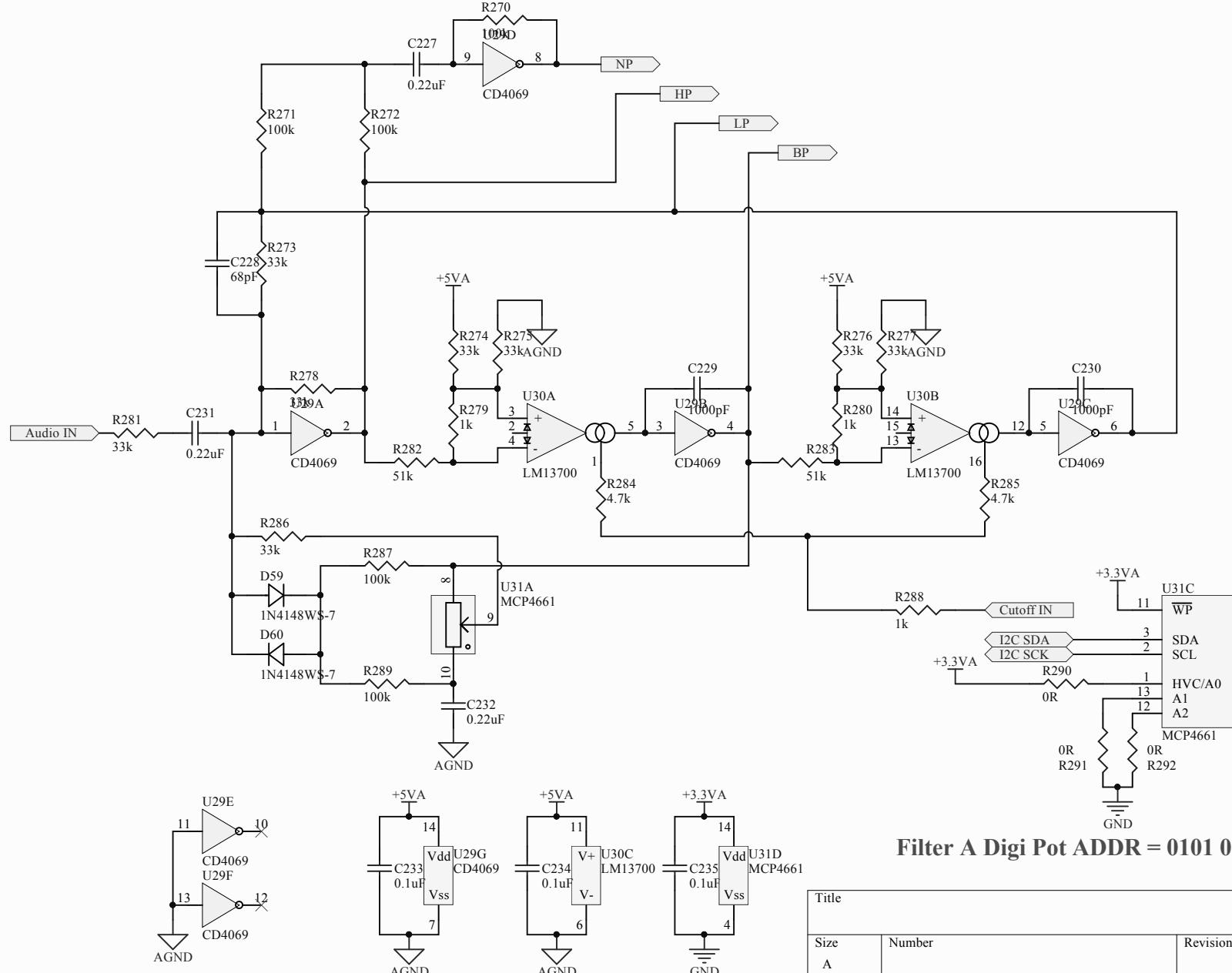
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Size	Number	Revision
A		
Date:	4/6/2018	Sheet of
File:	C:\Users\..\PF3_MS.SchDoc	Drawn By:

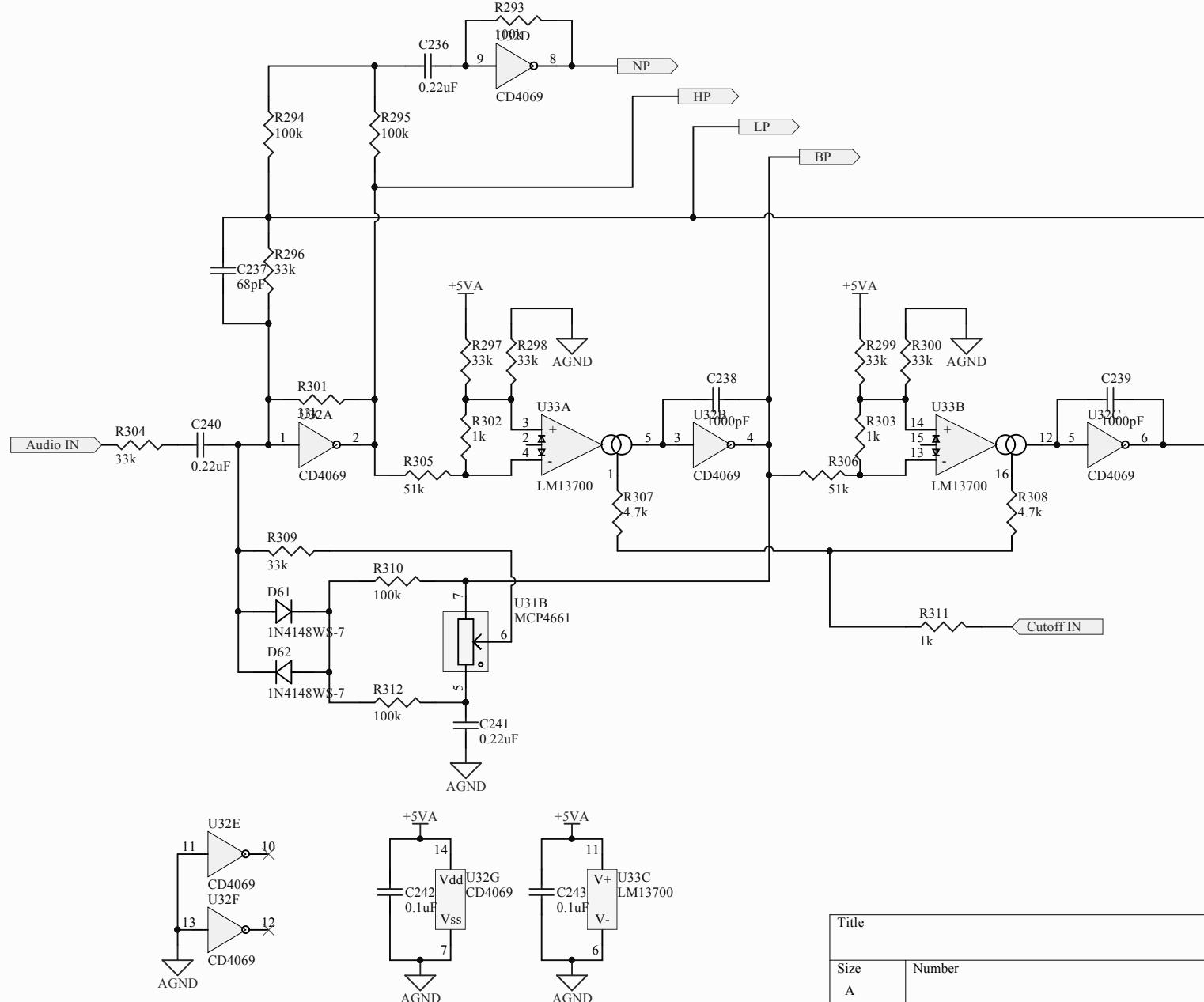
**Gain = 1.75**

$$2V_{rms} = 2.82 \text{ Vp} * 1.75 = 4.94 \text{ Vp}$$



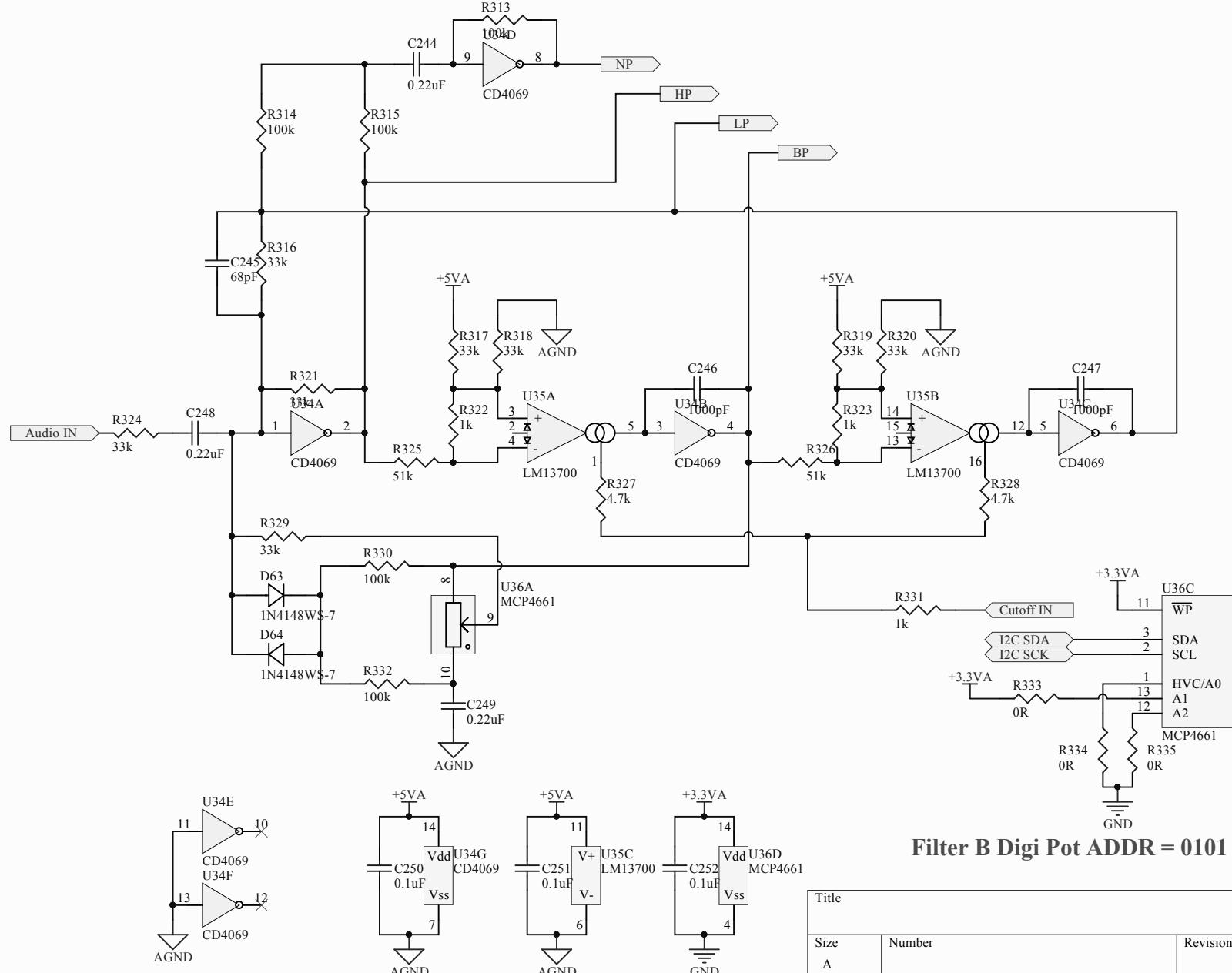
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Size	Number		
A			
Date: 4/6/2018	Sheet of		
File: C:\Users\...\PF4 DSPBypass.SchDoc	Drawn By:		

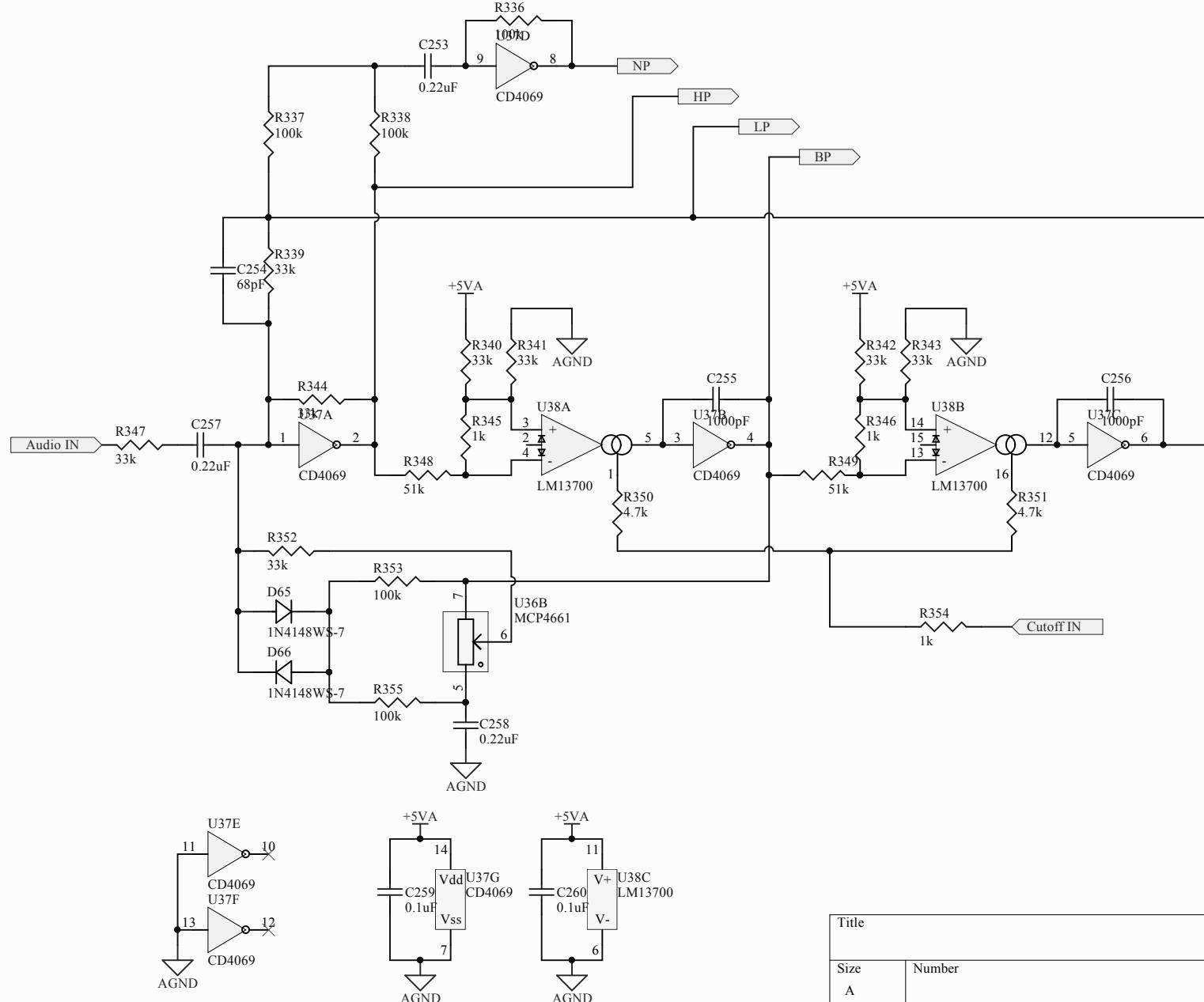




Title

Size	Number	Revision
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Date: 4/6/2018	Sheet of	
File: C:\Users\...\PF5b Filter2A.SchDoc		Drawn By:

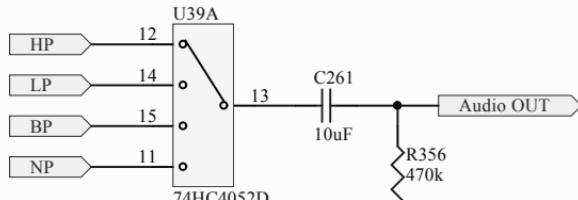




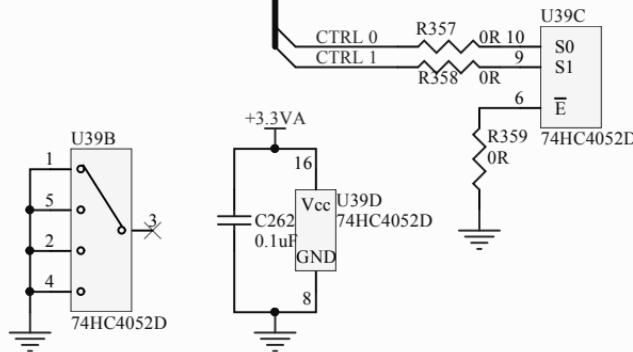
Title

Size	Number	Revision
A		
Date: 4/6/2018	Sheet of	
File: C:\Users\...\PF5d Filter2B.SchDoc		Drawn By:

A



B



C

Title

Size  
A

Number

Revision

Date: 4/6/2018

Sheet of

File: C:\Users\...\PF6\_FilterMode.SchDoc

Drawn By:

A

B

C

D

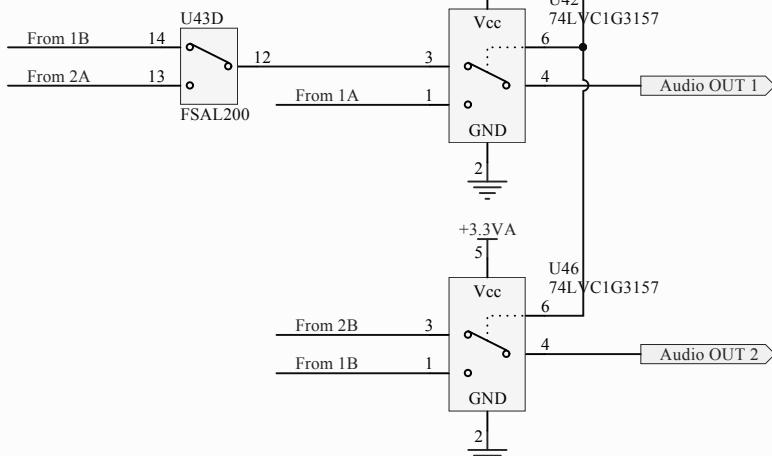
	1A	2A	1B	2B	Out1	Out2
Stereo	In1	In2	1A	2A	1B	2B
Dual Mono	//	1A	In2	1B	2A	//
Single Mono	//	//	//	//	1A	1B

Audio IN 2 From Audio IN 2

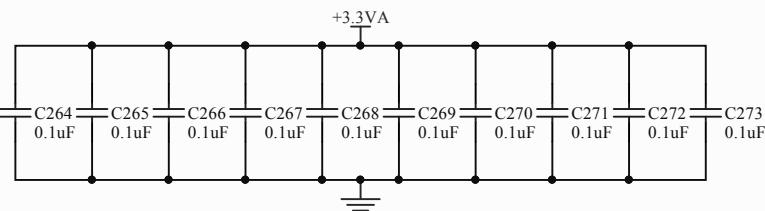
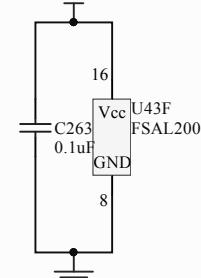
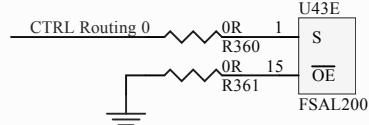
Audio IN 1 To 1A

From Audio IN 2 2 U43A  
From 1A 3 FSAL200  
To 2A 4From 1A 5 U43B  
From Audio IN 2 6 FSAL200  
To 1B 7From 2A 11 U43C  
From 1B 10 FSAL200  
To 2B 9

CTRL Routing [0..1]

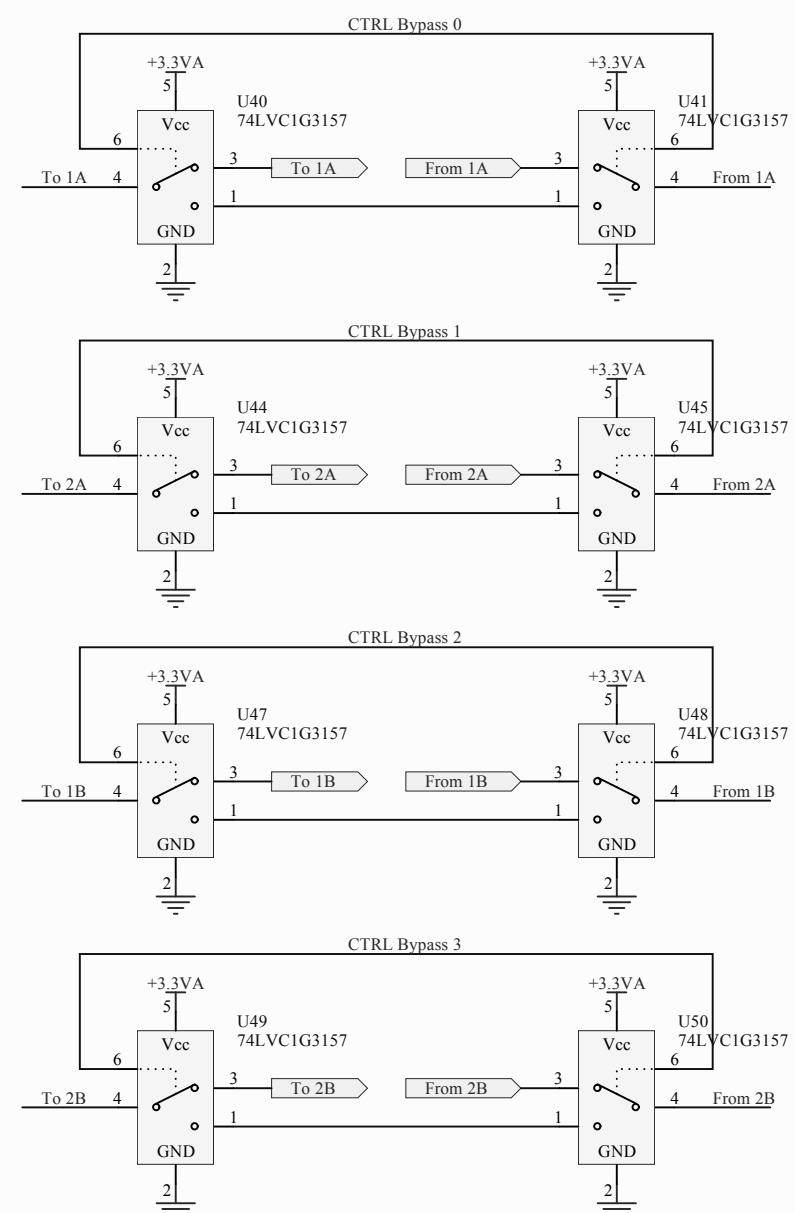


## FILTER ROUTING



## FILTER BYPASS

CTRL Bypass [0..3]



Title

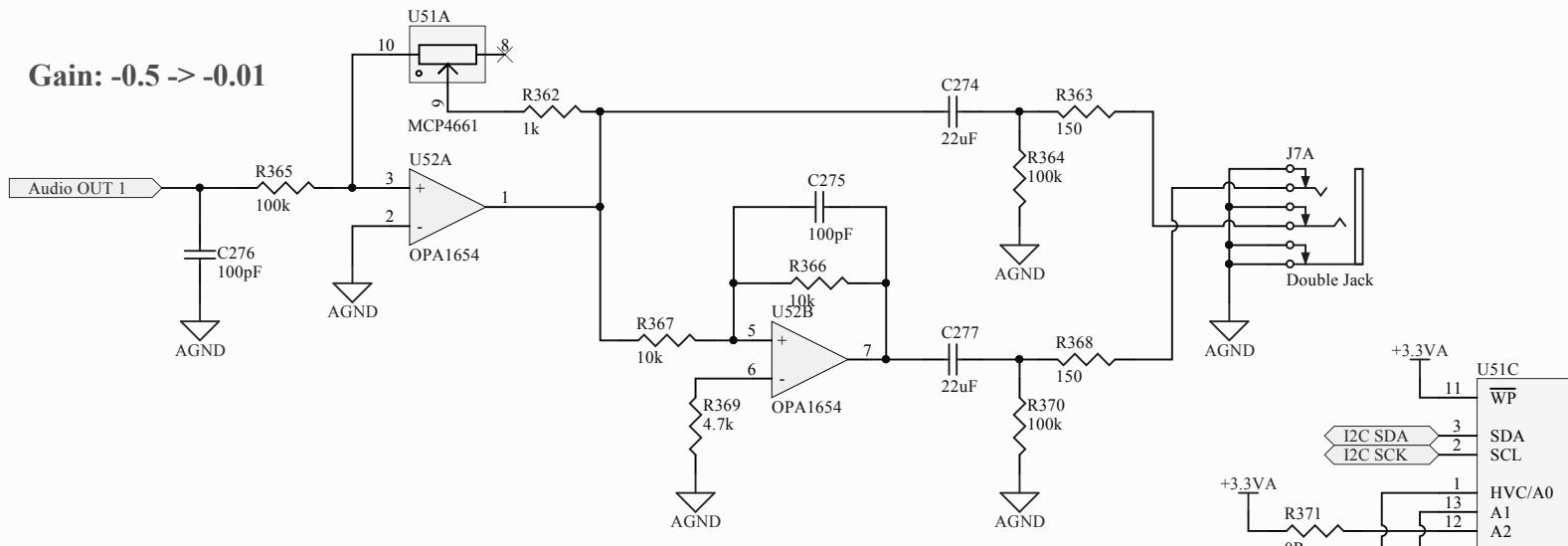
Size  
A

Number

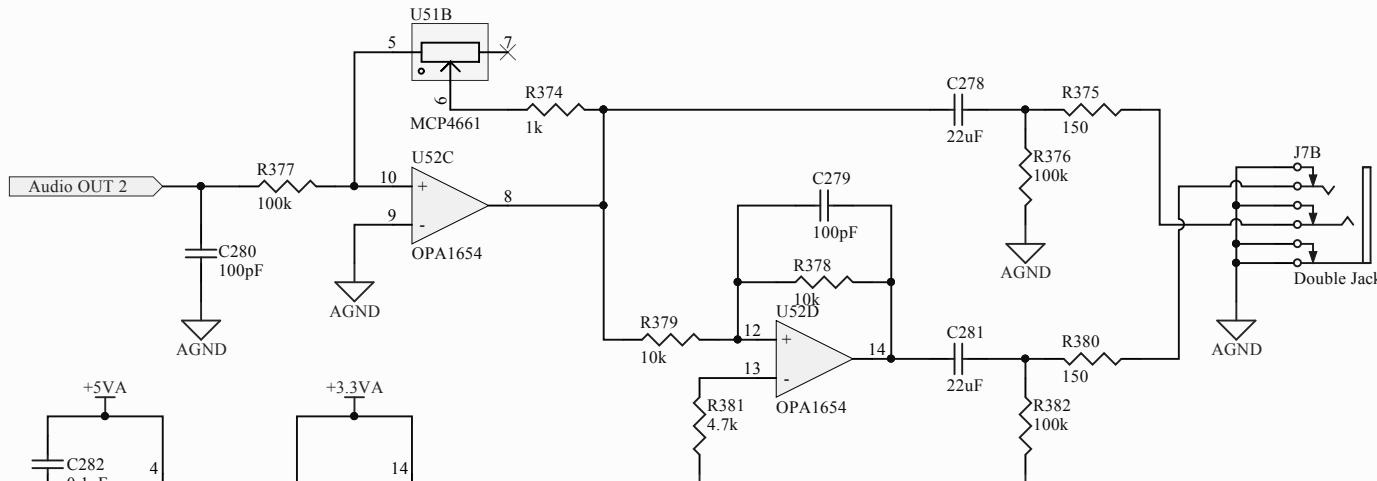
Revision

Date: 4/6/2018 Sheet of  
File: C:\Users\...\PF7 FilterRoutingBypass.SchDrawn By:

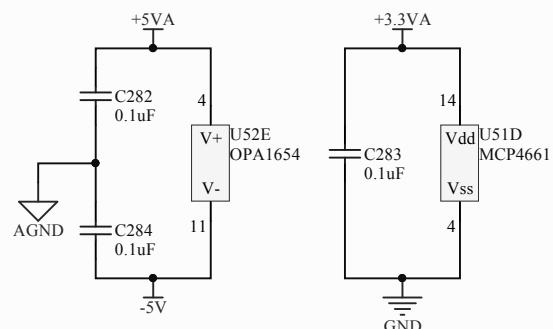
A

**Gain: -0.5 > -0.01**

B

**Output Digi Pot ADDR = 0101 100x**

C

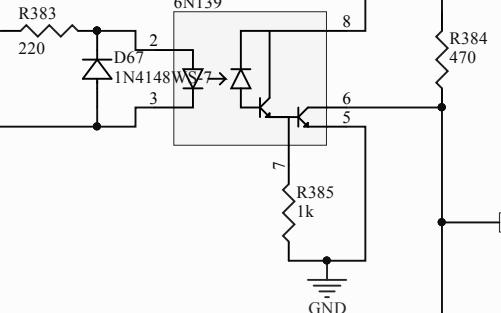
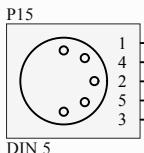


Title

Size	Number	Revision
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Date: 4/6/2018	Sheet of	
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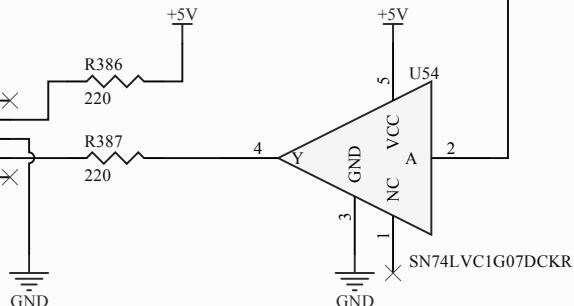
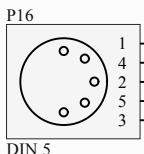
A

A

**MIDI IN**

B

B



C

C

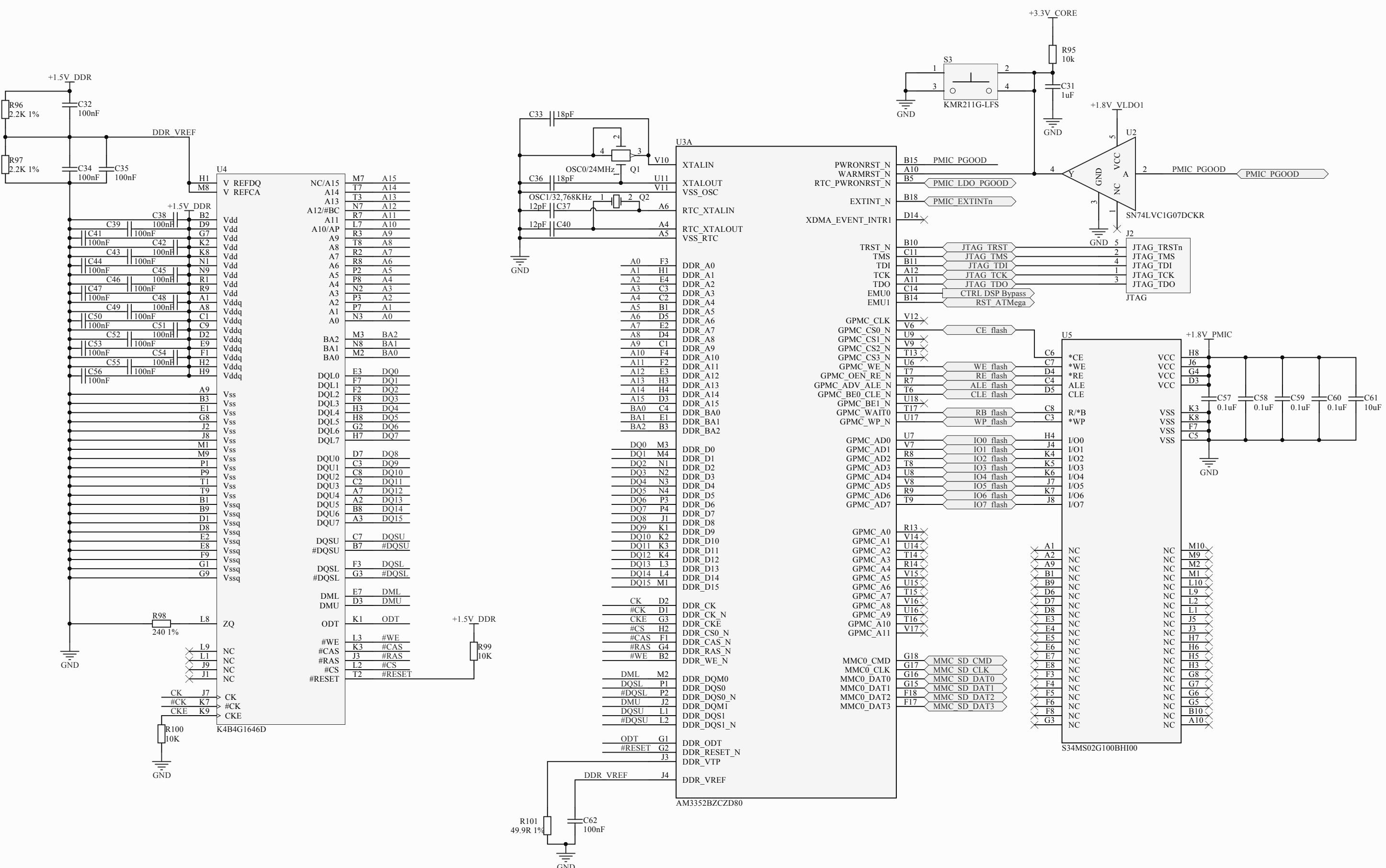
**MIDI THRU**

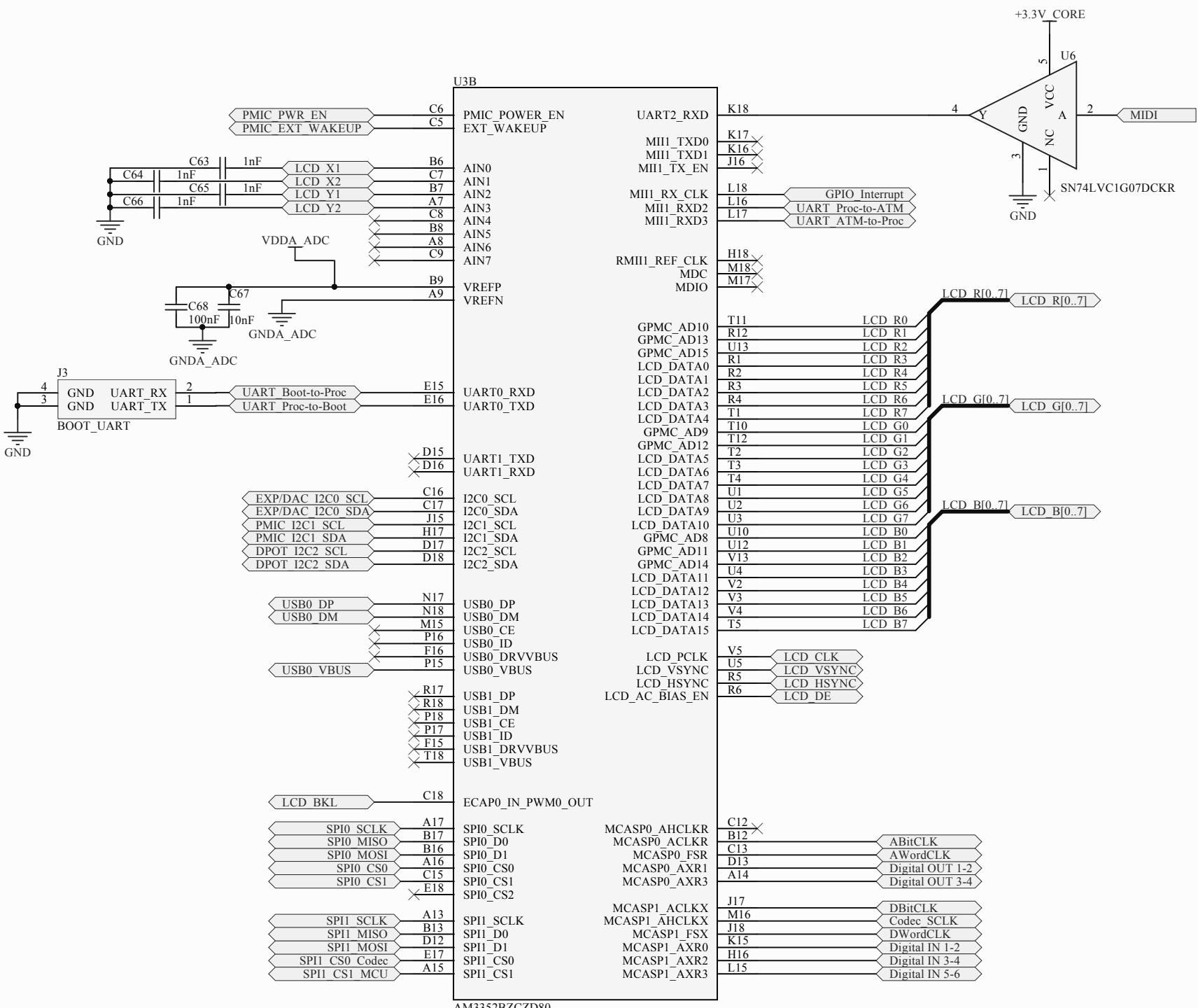
Title

Size	Number	Revision
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Date:	4/6/2018	Sheet of
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D

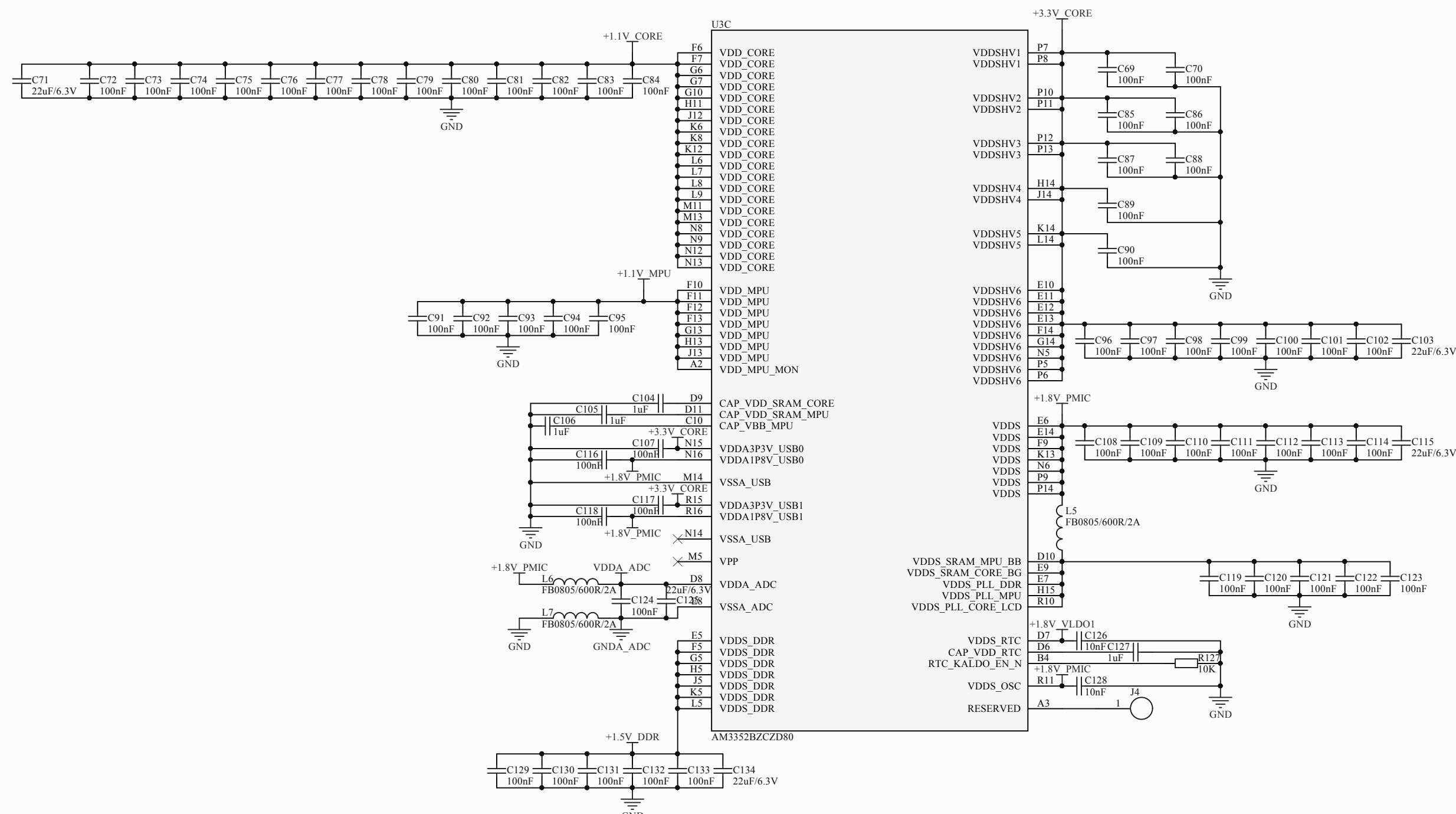
D





Title		
Size	Number	Revision
A3		
Date: 4/6/2018	Sheet of	
File: C:\Users\...\PP2.ptB.SchDoc		Drawn By:

A



Title		
Size	Number	Revision
A3		
Date: 4/6/2018	Sheet of	
File: C:\Users\..\\PP3_ptC_Alim.SchDoc	Drawn By:	

A

A

B

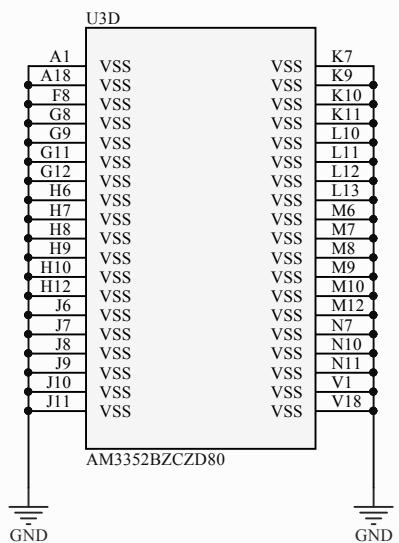
B

C

C

D

D

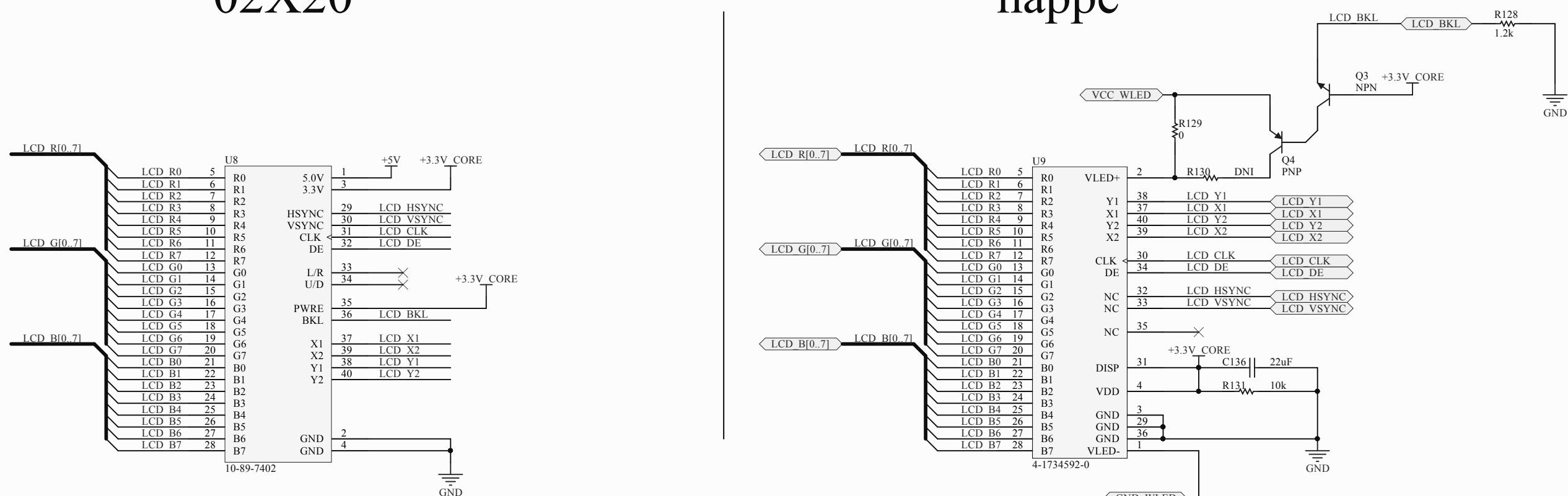


## Title

Size	Number	Revision
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Date:	4/6/2018	Sheet of
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02X20

nappe



1

2

3

4

5

6

A

A

B

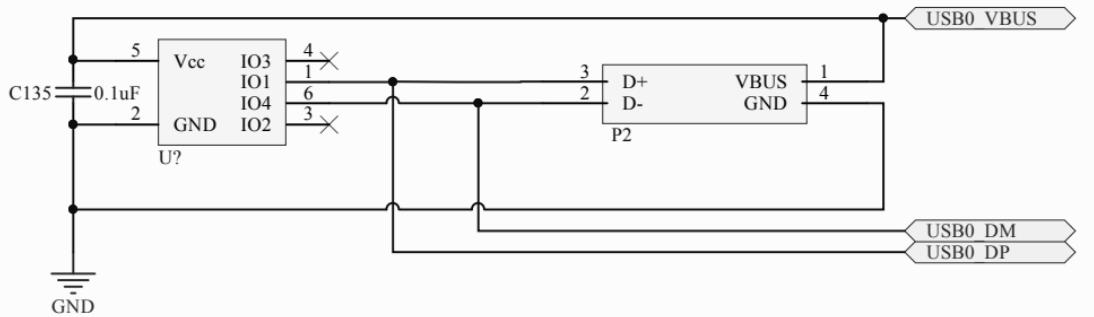
B

C

C

D

D



1

2

3

4

5

6

A

A

B

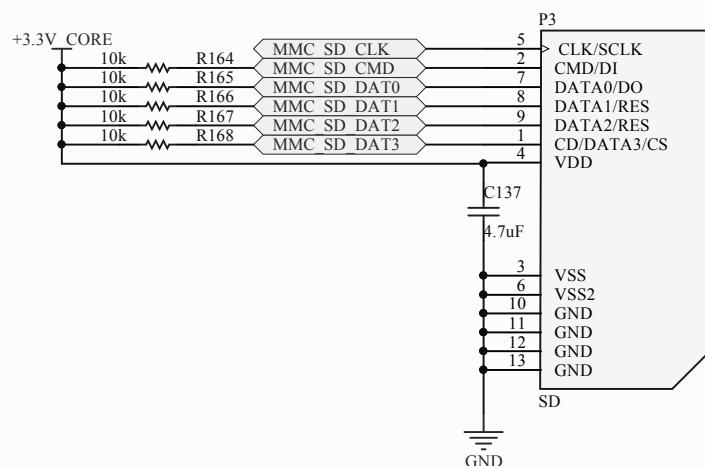
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C

C

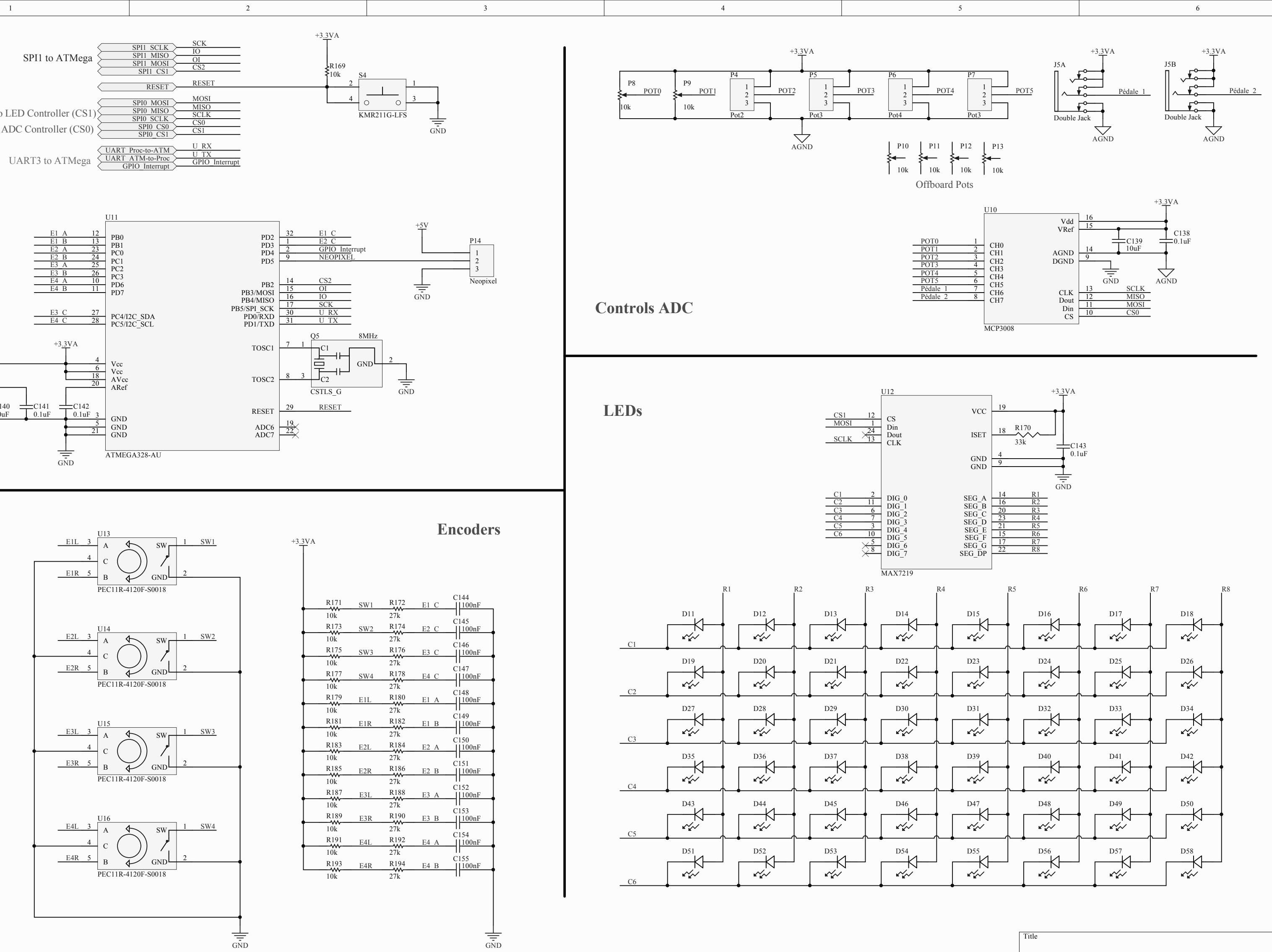
D

D

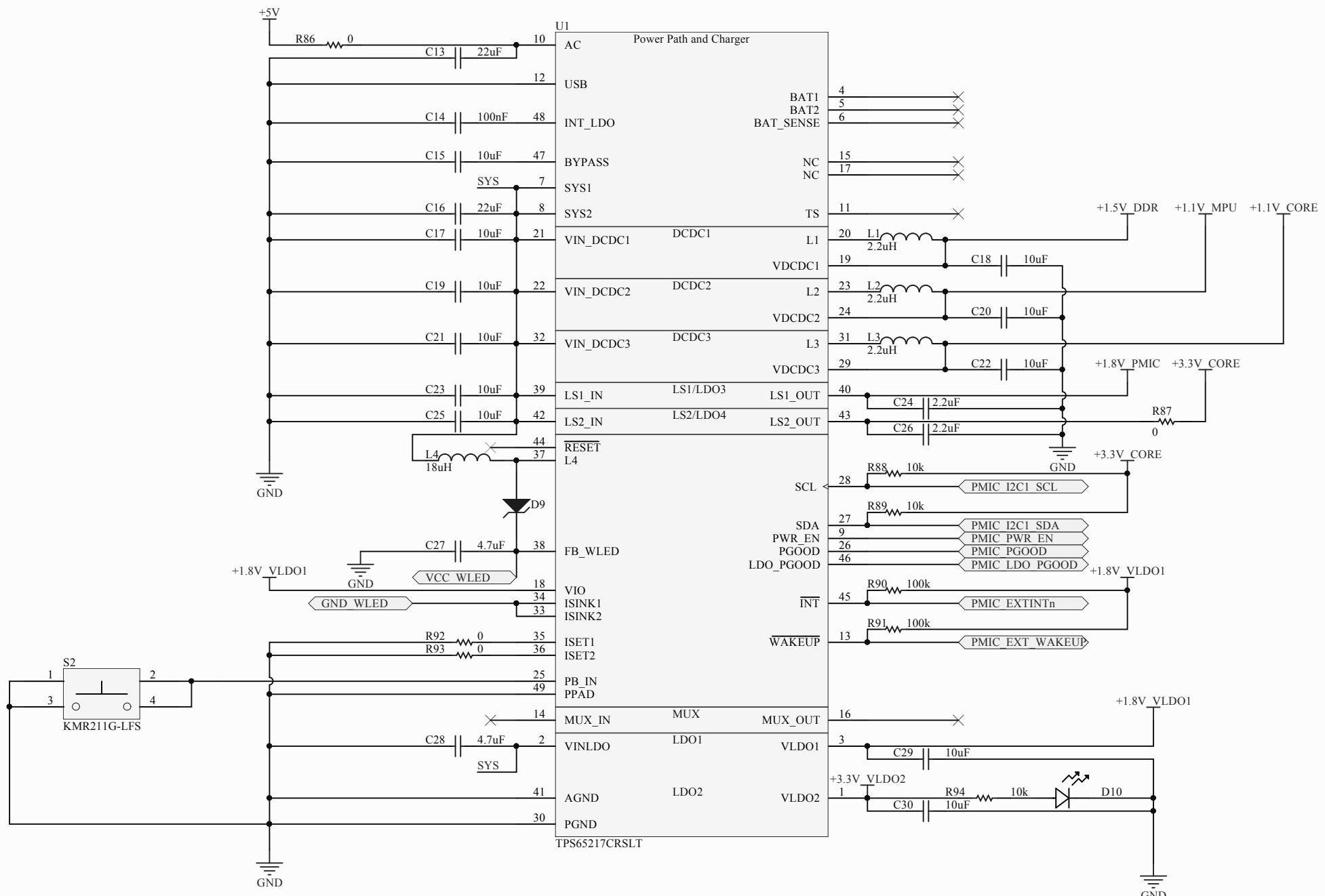


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Date:	4/6/2018	Sheet of
File:	C:\Users\...\PP8 MMC.SchDoc	Drawn By:



A



Title		
Size	Number	Revision
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Date: 4/6/2018	Sheet of	
File: C:\Users\..\PX_PMIC.SchDoc	Drawn By:	