

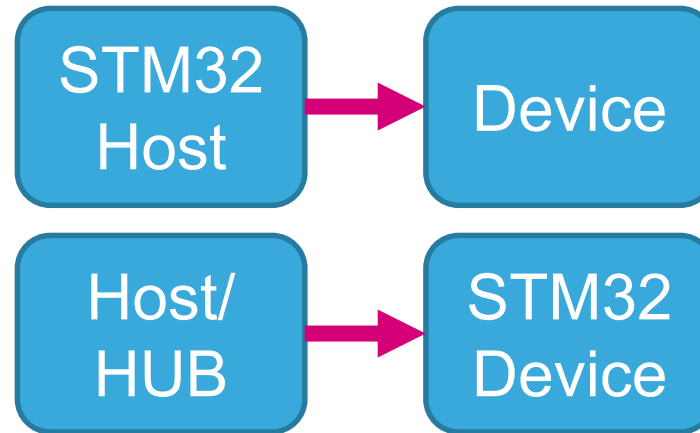
STM32 USB library

USB library overview

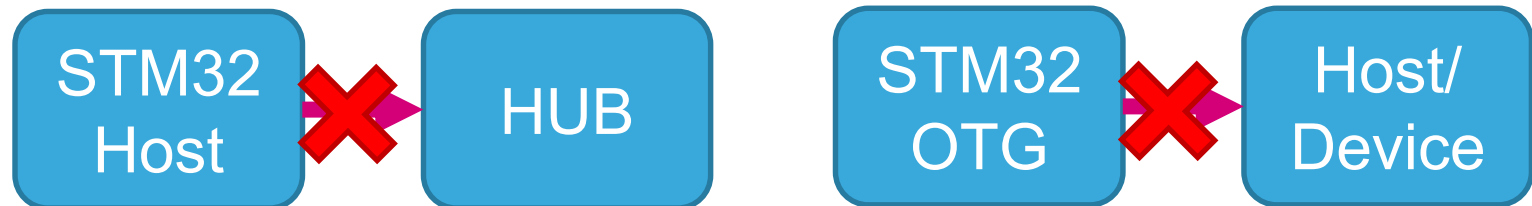
180

- Integration with CubeMX
 - Integration with FatFs when mass-storage host is used

- Supported configurations:

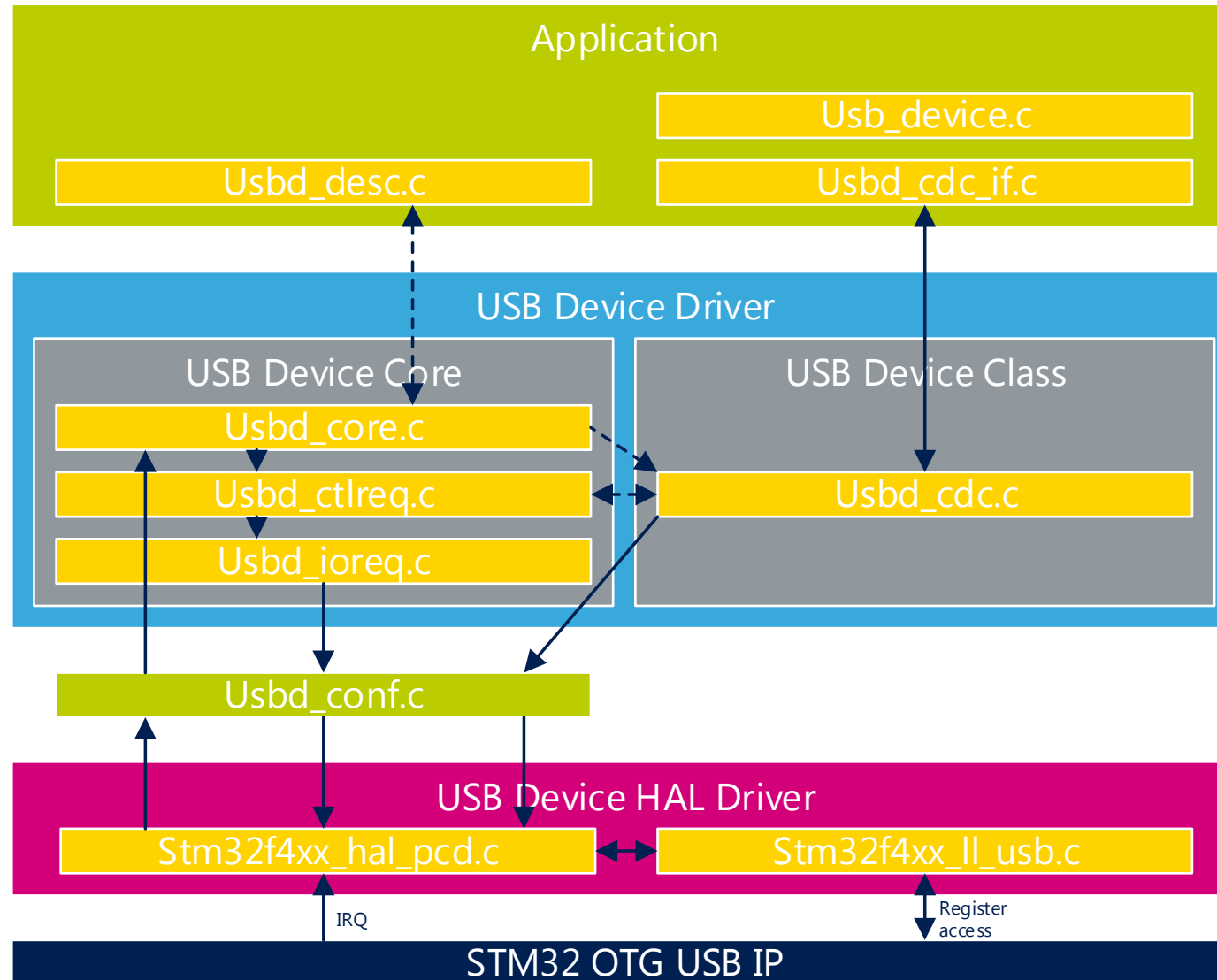


- Unsupported configurations (supported in HW, but not in library):



Device library organization

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USB Device Handle connections

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```
/* USB Device handle structure */
typedef struct _USBD_HandleTypeDef
{
    uint8_t          id;
    uint32_t          dev_config;
    uint32_t          dev_default_config;
    uint32_t          dev_config_status;
    USBD_SpeedTypeDef dev_speed;
    USBD_EndpointTypeDef ep_in[15];
    USBD_EndpointTypeDef ep_out[15];
    uint32_t          ep0_state;
    uint32_t          ep0_data_len;
    uint8_t           dev_state;
    uint8_t           dev_old_state;
    uint8_t           dev_address;
    uint8_t           dev_connection_status;
    uint8_t           dev_test_mode;
    uint32_t          dev_remote_wakeup;

    USBD_SetupReqTypeDef request;
    USBD_DescriptorsTypeDef *pDesc;
    USBD_ClassTypeDef *pClass;
    void *pClassData;
    void *pUserData;
    void *pData;
} USBD_HandleTypeDef;
```

life.augmented

```
/* USB Device descriptors structure */
typedef struct
{
    uint8_t *(*GetDeviceDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetLangIDStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetManufacturerStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetProductStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetSerialStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetConfigurationStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetInterfaceStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    #if (USBD_LPM_ENABLED == 1)
        uint8_t *(*GetBOSDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    #endif
} USBD_DescriptorsTypeDef;
```

```
typedef struct _Device_cb
{
    uint8_t (*Init)          (struct _USBD_HandleTypeDef *pdev , uint8_t cfgidx);
    uint8_t (*DeInit)        (struct _USBD_HandleTypeDef *pdev , uint8_t cfgidx);
    /* Control Endpoints*/
    uint8_t (*Setup)         (struct _USBD_HandleTypeDef *pdev , USBD_SetupReqTypeDef *req);
    uint8_t (*EP0_TxSent)    (struct _USBD_HandleTypeDef *pdev );
    uint8_t (*EP0_RxReady)   (struct _USBD_HandleTypeDef *pdev );
    /* Class Specific Endpoints*/
    uint8_t (*DataIn)        (struct _USBD_HandleTypeDef *pdev , uint8_t epnum);
    uint8_t (*DataOut)       (struct _USBD_HandleTypeDef *pdev , uint8_t epnum);
    uint8_t (*SOF)           (struct _USBD_HandleTypeDef *pdev);
    uint8_t (*IsoINIncomplete) (struct _USBD_HandleTypeDef *pdev , uint8_t epnum);
    uint8_t (*IsoOUTIncomplete) (struct _USBD_HandleTypeDef *pdev , uint8_t epnum);

    uint8_t *(*GetHSConfigDescriptor)(uint16_t *length);
    uint8_t *(*GetFSConfigDescriptor)(uint16_t *length);
    uint8_t *(*GetOtherSpeedConfigDescriptor)(uint16_t *length);
    uint8_t *(*GetDeviceQualifierDescriptor)(uint16_t *length);
    #if (USBD_SUPPORT_USER_STRING == 1)
        uint8_t *(*GetUsrStrDescriptor)(struct _USBD_HandleTypeDef *pdev , uint8_t index, uint16_t
    #endif
} USBD_ClassTypeDef;
```

Connection between descriptors and handle

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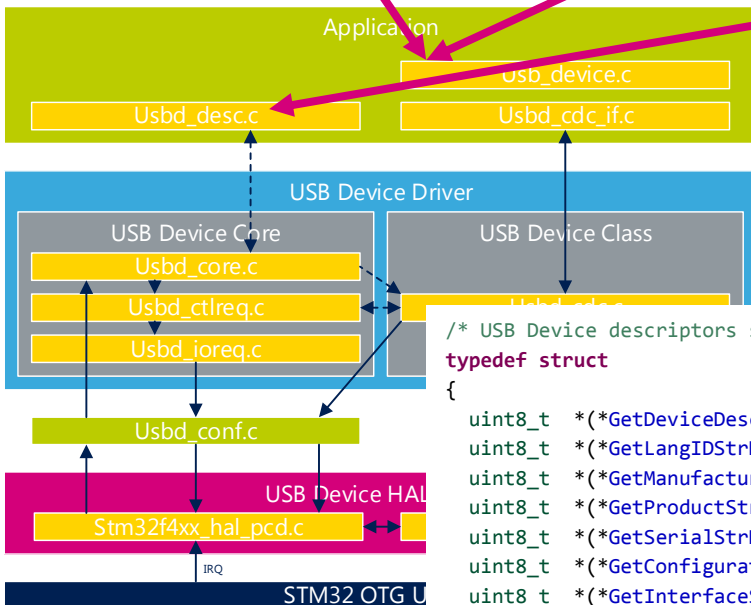
- In usb_device.c is created connection between handle and descriptors from usbd_desc over function:

```
USBD_Init(&hUsbDeviceFS, &FS_Desc, DEVICE_FS);
```

```
/* USB Device handle structure */
typedef struct _USBD_HandleTypeDef
{
    uint8_t            id;
    uint32_t           dev_config;
    uint32_t           dev_default_config;
    uint32_t           dev_config_status;
    USBD_SpeedTypeDef  dev_speed;
    USBD_EndpointTypeDef ep_in[15];
    USBD_EndpointTypeDef ep_out[15];
    uint32_t           ep0_state;
    uint32_t           ep0_data_len;
    uint8_t            dev_state;
    uint8_t            dev_old_state;
    uint8_t            dev_address;
    uint8_t            dev_connection_status;
    uint8_t            dev_test_mode;
    uint32_t           dev_remote_wakeup;

    USBD_SetupReqTypeDef request;
    USBD_DescriptorsTypeDef *pDesc;
    USBD_ClassTypeDef *pClass;
    void *pClassData;
    void *pUserData;
    void *pData;
} USBD_HandleTypeDef;
```

```
/* USB Device descriptors structure */
typedef struct
{
    uint8_t *(*GetDeviceDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetLangIDStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetManufacturerStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetProductStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetSerialStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetConfigurationStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    uint8_t *(*GetInterfaceStrDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    #if (USBD_LPM_ENABLED == 1)
    uint8_t *(*GetBOSDescriptor)( USBD_SpeedTypeDef speed , uint16_t *length);
    #endif
} USBD_DescriptorsTypeDef;
```



Connection between handle class

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- In usb_device.c is created connection between handle and class functions from usbd_cdc.c(or different class) over function:

```
USBD_RegisterClass(&hUsbDeviceFS, &USBD_CDC);
```

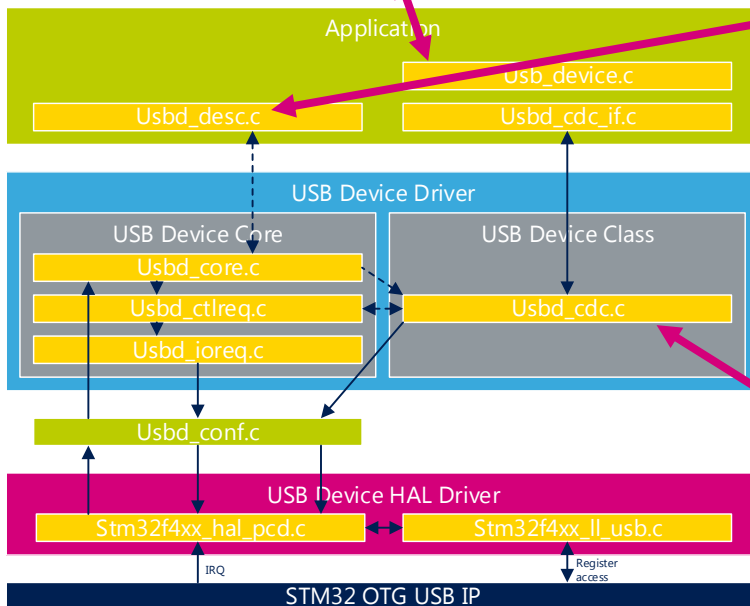
```
/* USB Device handle structure */
typedef struct _USBD_HandleTypeDef
{
    uint8_t id;
    uint32_t dev_config;
    uint32_t dev_default_config;
    uint32_t dev_config_status;
    USBD_SpeedTypeDef dev_speed;
    USBD_EndpointTypeDef ep_in[15];
    USBD_EndpointTypeDef ep_out[15];
    uint32_t ep0_state;
    uint32_t ep0_data_len;
    uint8_t dev_state;
    uint8_t dev_old_state;
    uint8_t dev_address;
    uint8_t dev_connection_status;
    uint8_t dev_test_mode;
    uint32_t dev_remote_wakeup;

    USBD_SetupReqTypeDef request;
    USBD_DescriptorsTypeDef *pDesc;
    USBD_ClassTypeDef *pClass;
    void *pClassData;
    void *pUserData;
    void *pData;
} USBD_HandleTypeDef;
```

```
typedef struct _DeviceCb
{
    uint8_t (*Init)(struct _USBD_HandleType
    uint8_t (*DeInit)(struct _USBD_HandleType
    /* Control Endpoints*/
    uint8_t (*Setup)(struct _USBD_HandleType
    uint8_t (*EP0_TxSent)(struct _USBD_HandleType
    uint8_t (*EP0_RxReady)(struct _USBD_HandleType
    /* Class Specific Endpoints*/
    uint8_t (*DataIn)(struct _USBD_HandleType
    uint8_t (*DataOut)(struct _USBD_HandleType
    uint8_t (*SOF)(struct _USBD_HandleType
    uint8_t (*IsoINIncomplete)(struct _USBD_HandleType
    uint8_t (*IsoOUTIncomplete)(struct _USBD_HandleType

    uint8_t (*GetHSCfgDescriptor)(uint16_t *length)
    uint8_t (*GetFSCfgDescriptor)(uint16_t *length)
    uint8_t (*GetOtherSpeedCfgDescriptor)(uint16_t *length)
    uint8_t (*GetDeviceQualifierDescriptor)(uint16_t *length)

    #if (USBD_SUPPORT_USER_STRING == 1)
    uint8_t (*GetUsrStrDescriptor)(struct _USBD_HandleTypeDef *pdev, uint8_t index, uint16_t *length);
    #endif
}
```

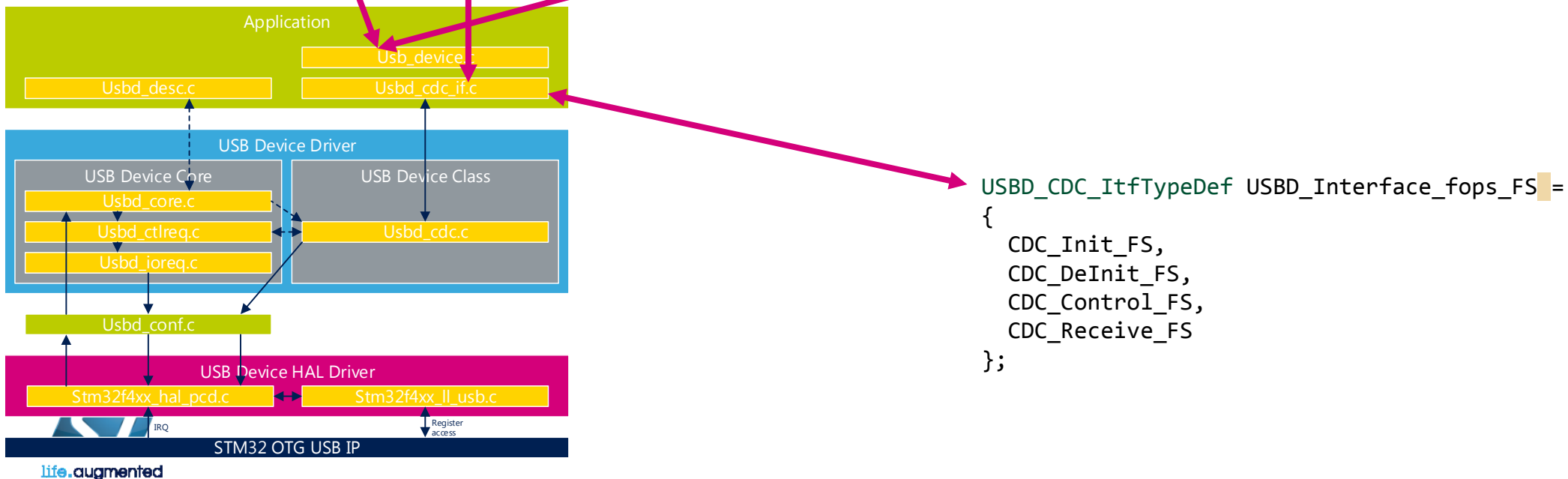


Connection between class and their api interface (cdc)

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- In usb_device.c is created connection between class and class api functions from usbd_cdc_if.c(or different class) over function:

```
USBD_CDC_RegisterInterface(&hUsbDeviceFS,  
&USBD_Interface_fops_FS);
```

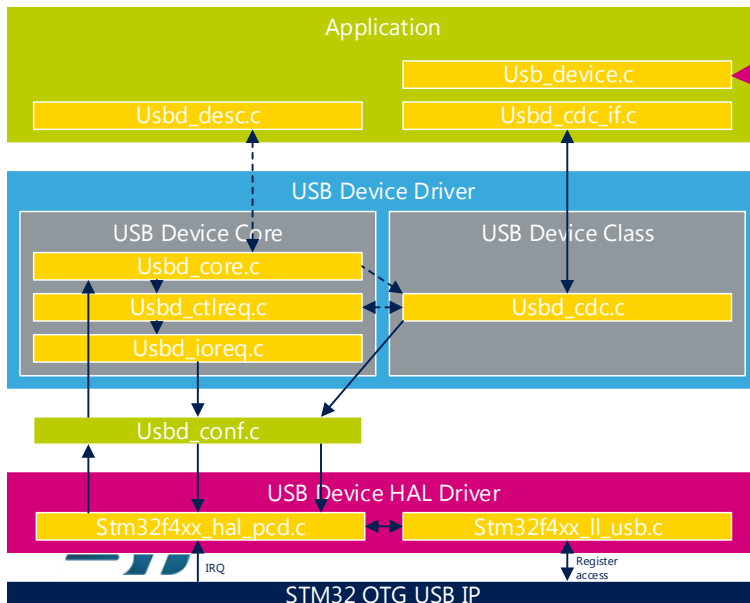


Start the USB device library

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- The library function is started by function USBD_Start with handle as parameter.
- Function USBD_stop will stop the USB.
- USBD_Start/Stop is defined in usbd_core.c file

`USBD_Start(&hUsbDeviceFS);`



Device parameters

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```
USBD_StatusTypeDef  USBD_LL_Init (USBD_HandleTypeDef *pdev)
{
    /* Init USB IP */
    if (pdev->id == DEVICE_FS) {
        /* Link The driver to the stack */
        hpcd_USB_OTG_FS.pData = pdev;
        pdev->pData = &hpcd_USB_OTG_FS;

        hpcd_USB_OTG_FS.Instance = USB_OTG_FS;
        hpcd_USB_OTG_FS.Init.dev_endpoints = 6;
        hpcd_USB_OTG_FS.Init.speed = PCD_SPEED_FULL;
        hpcd_USB_OTG_FS.Init.dma_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.ep0_mps = DEP0CTL_MPS_64;
        hpcd_USB_OTG_FS.Init.phy_itface = PCD_PHY_EMBEDDED;
        hpcd_USB_OTG_FS.Init.Sof_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.low_power_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.lpm_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.vbus_sensing_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.use_dedicated_ep1 = DISABLE;
        if (HAL_PCD_Init(&hpcd_USB_OTG_FS) != HAL_OK)
        {
            _Error_Handler(__FILE__, __LINE__);
        }

        HAL_PCDEx_SetRxFiFo(&hpcd_USB_OTG_FS, 0x80);
        HAL_PCDEx_SetTxFiFo(&hpcd_USB_OTG_FS, 0, 0x40);
        HAL_PCDEx_SetTxFiFo(&hpcd_USB_OTG_FS, 1, 0x80);
    }
    return USBD_OK;
}
```

Created by CubeMX in
usbd_conf.c

IP selection HS IP or FS
IP

Number of used
endpoints.

DMA used to endpoint
TX/RX

Speed in device mode
LS/FS/HS

EP0 max packet size



Device parameters

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```
USBD_StatusTypeDef  USBD_LL_Init (USBD_HandleTypeDef *pdev)
{
    /* Init USB IP */
    if (pdev->id == DEVICE_FS) {
        /* Link The driver to the stack */
        hpcd_USB_OTG_FS.pData = pdev;
        pdev->pData = &hpcd_USB_OTG_FS;

        hpcd_USB_OTG_FS.Instance = USB_OTG_FS;
        hpcd_USB_OTG_FS.Init.dev_endpoints = 6;
        hpcd_USB_OTG_FS.Init.speed = PCD_SPEED_FULL;
        hpcd_USB_OTG_FS.Init.dma_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.ep0_mps = DEP0CTL_MPS_64;
        hpcd_USB_OTG_FS.Init.phy_itface = PCD_PHY_EMBEDDED;
        hpcd_USB_OTG_FS.Init.Sof_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.low_power_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.lpm_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.vbus_sensing_enable = DISABLE;
        hpcd_USB_OTG_FS.Init.use_dedicated_ep1 = DISABLE;
        if (HAL_PCD_Init(&hpcd_USB_OTG_FS) != HAL_OK)
        {
            _Error_Handler(__FILE__, __LINE__);
        }

        HAL_PCDEx_SetRxFiFo(&hpcd_USB_OTG_FS, 0x80);
        HAL_PCDEx_SetTxFiFo(&hpcd_USB_OTG_FS, 0, 0x40);
        HAL_PCDEx_SetTxFiFo(&hpcd_USB_OTG_FS, 1, 0x80);
    }
    return USBD_OK;
}
```

SOF interrupt generation
(each 1ms/125us)

VBUS sensing feature
used

Used specific EP1
interrupts (HS only)



Device parameters – FIFO allocation

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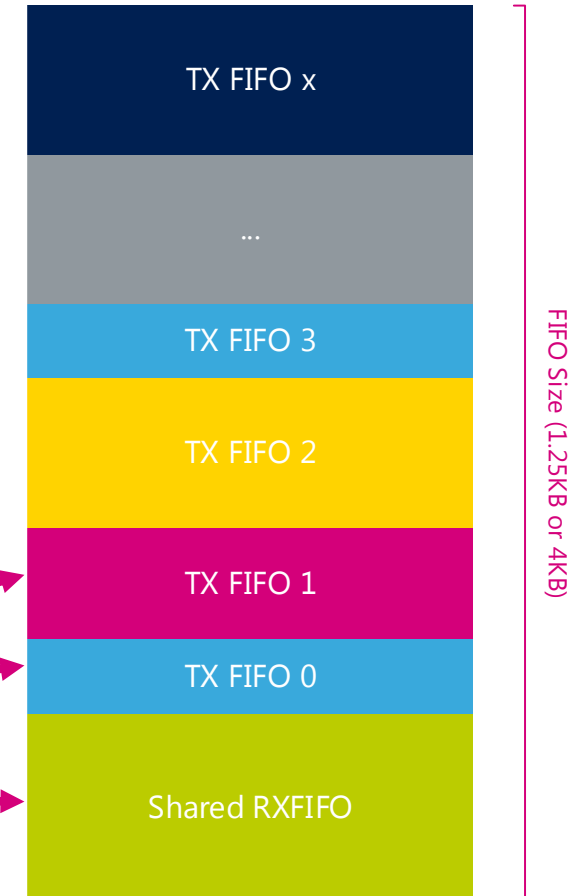
```
USBD_StatusTypeDef  USBD_LL_Init (USBD_HandleTypeDef *pdev)
{
    /* Init USB IP */
    if (pdev->id == DEVICE_FS) {
        /* Link The driver to the stack */
        hpcd_USB_OTG_FS.pData = pdev;
        pdev->pData = &hpcd_USB_OTG_FS;
    }
}
```

Bigger value will provide bigger transfer speed and less interrupts when big amount of data is sent/received

The sum of all fifo sizes must be 1.25KB or 4KB
Argument in words – 32 bits!!

```
HAL_PCD_Init(&hpcd_USB_OTG_FS) = HAL_OK)
{
    _Error_Handler(__FILE__, __LINE__);
}

HAL_PCDEx_SetRxFiFo(&hpcd_USB_OTG_FS, 0x80);
HAL_PCDEx_SetTxFiFo(&hpcd_USB_OTG_FS, 0, 0x40);
HAL_PCDEx_SetTxFiFo(&hpcd_USB_OTG_FS, 1, 0x80);
}
return USBD_OK;
}
```

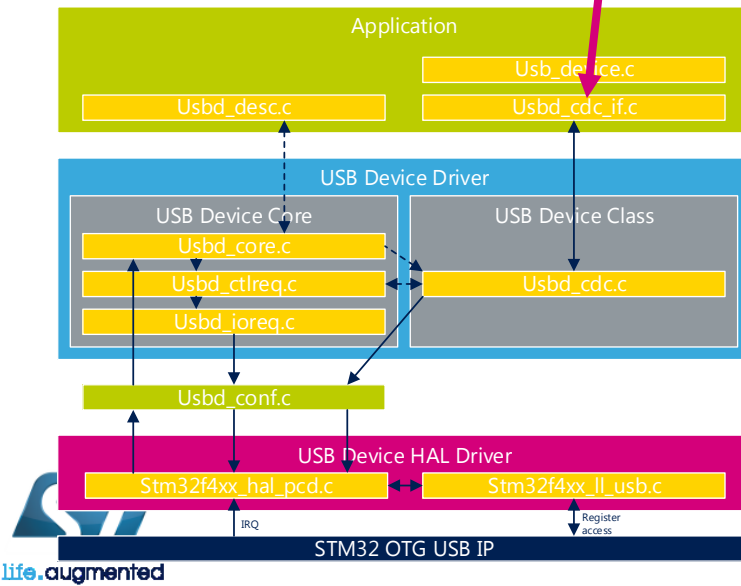


EP number



```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

Function can be called from main application

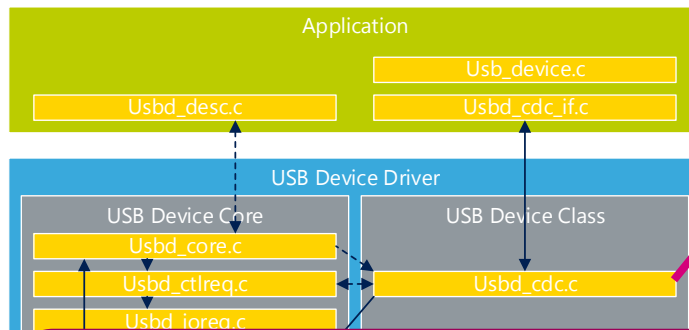


```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

First the CDC structure is used to store buffer source for TX

And data size

```
USBDCDC_SetTxBuffer(&hUsbDeviceFS, Buf, Len);
```



Structure is pointed from main USB handle it is dynamically allocated (from HEAP) during enumeration

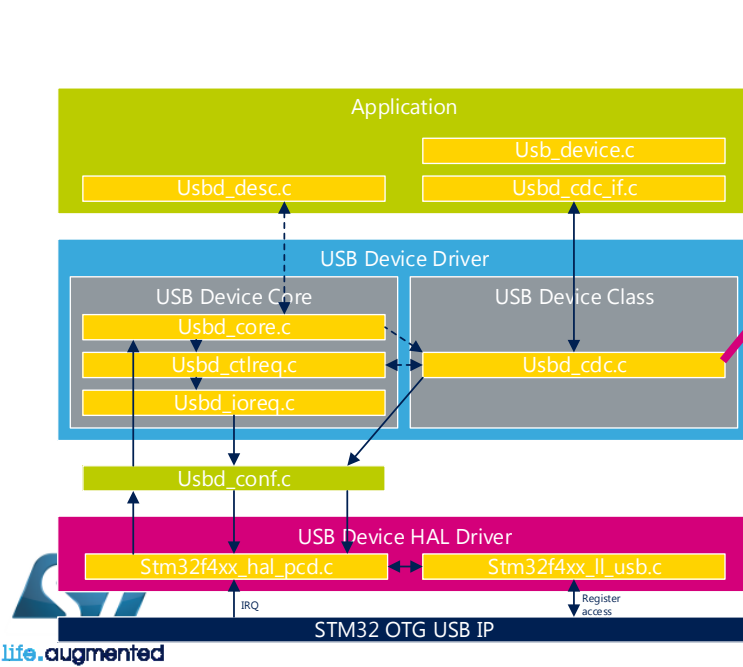
```
typedef struct
{
    uint32_t data[CDC_DATA_HS_MAX_PACKET_SIZE/4]; /* Force 32bits alignment */
    uint8_t CmdOpCode;
    uint8_t CmdLength;
    uint8_t *RxBuffer;
    uint8_t *TxBuffer;
    uint32_t RxLength;
    uint32_t TxLength;

    __IO uint32_t TxState;
    __IO uint32_t RxState;
}
USBDCDC_HandleTypeDef;
```

```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

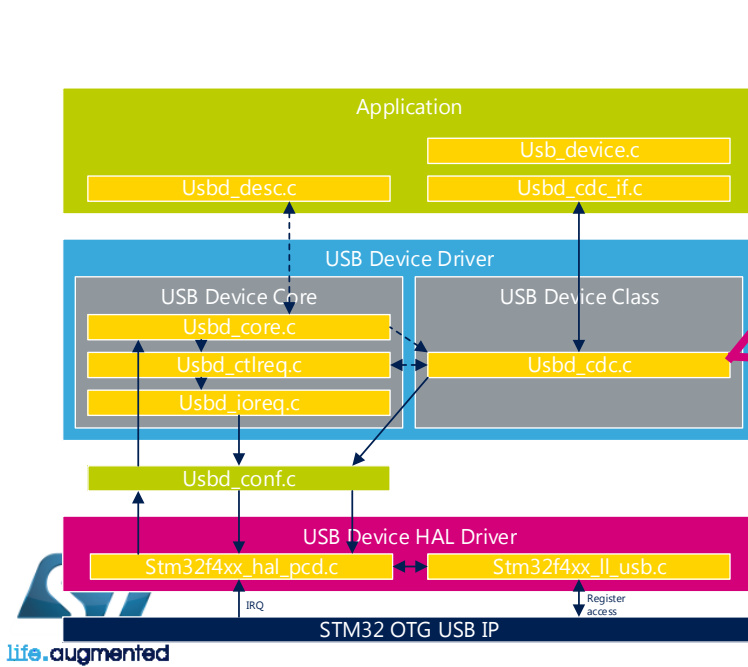
Transmit function is called to pass our data to EP

```
uint8_t USBDCDC_TransmitPacket(USB_HandleTypeDef *pdev)
```



```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

```
uint8_t USBDCDC_TransmitPacket(USBDCDC_HandleTypeDef *pdev)
```



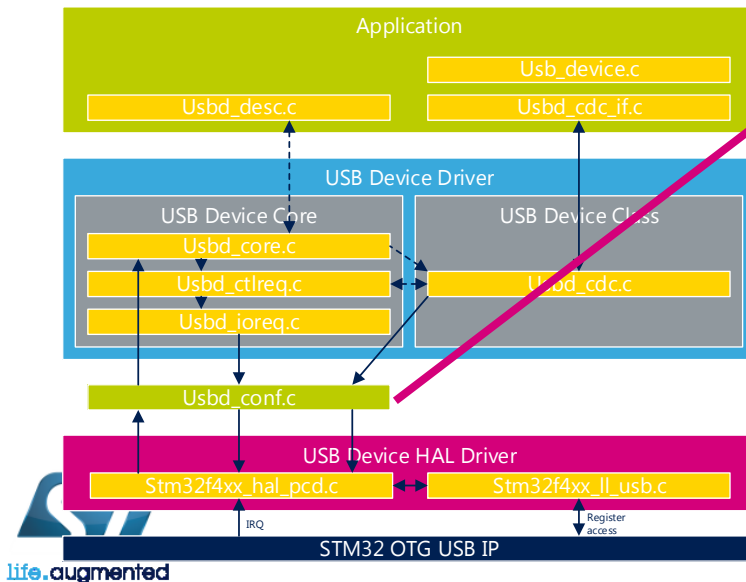
```
typedef struct
{
    uint32_t data[CDC_DATA_HS_MAX_PACKET_SIZE/4]; /* Force 32bits alignment */
    uint8_t CmdOpCode;
    uint8_t CmdLength;
    uint8_t *RxBuffer;
    uint8_t *TxBuffer;
    uint32_t RxLength;
    uint32_t TxLength;
    __IO uint32_t TxState;
    __IO uint32_t RxState;
}
USBDCDC_HandleTypeDef;
```

TxState is set to 1 marking outgoing TX, !buffer now cannot be changed by Application!

```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

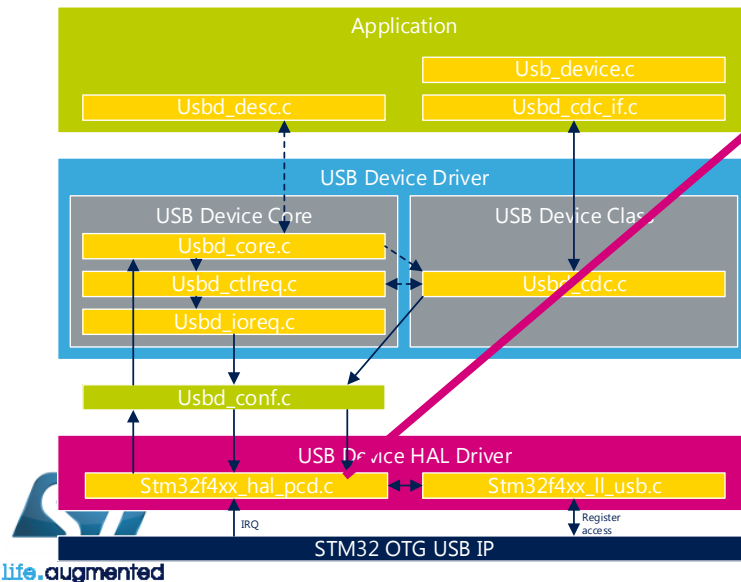
```
USB_StatusTypeDef USB_LL_Transmit(USB_HandleTypeDef *pdev,  
    uint8_t ep_addr,  
    uint8_t *pbuf,  
    uint16_t size)
```

USB_LL_Transmit is called to send data




```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

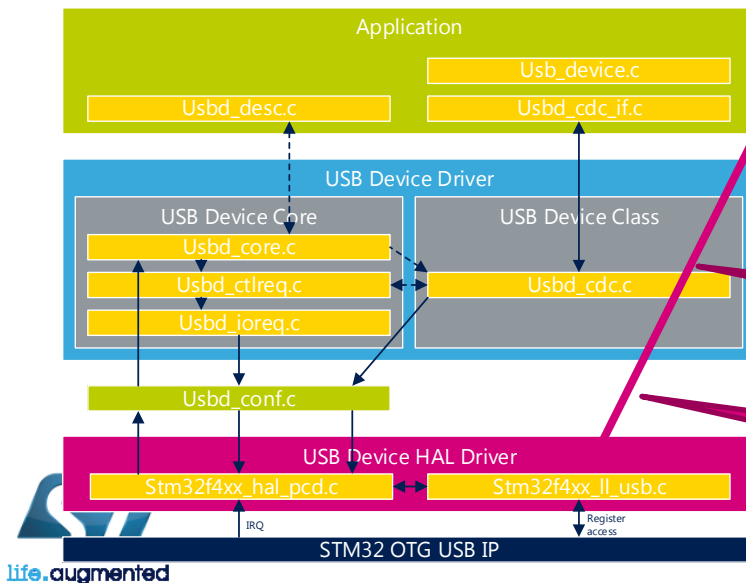
```
HAL_StatusTypeDef HAL_PCD_EP_Transmit(PCD_HandleTypeDef *hpcd,  
uint8_t ep_addr, uint8_t *pBuf, uint32_t len)
```



This calls HAL_PCD_EP_Transmit

```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

```
HAL_StatusTypeDef USB_EPStartXfer(USB_OTG_GlobalTypeDef *USBx
, USB_OTG_EPTypeDef *ep, uint8_t dma)
```



At the end is called USB_EPStartXfer

This function set EP registers, how much bytes will be sent, how many packets...

Then EP Fifo empty interrupt is enabled

Now EP is enabled

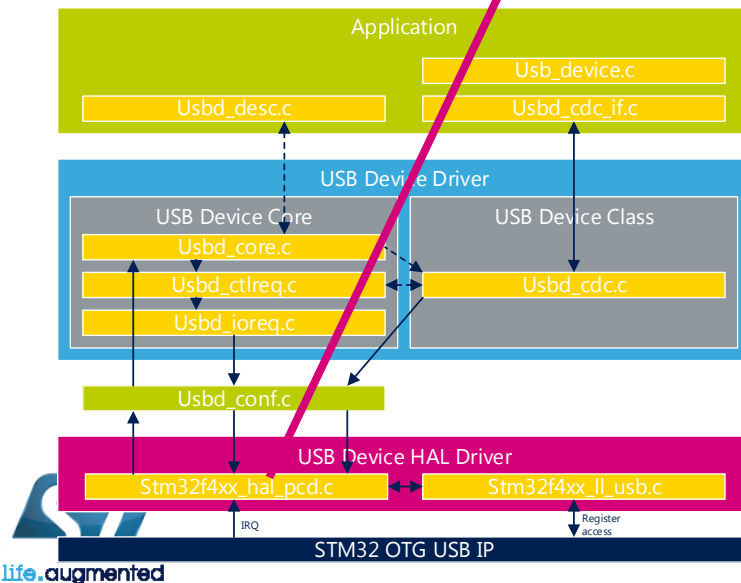
```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

Empty fifo interrupt is handled by
HAL_PCD_IRQHandler

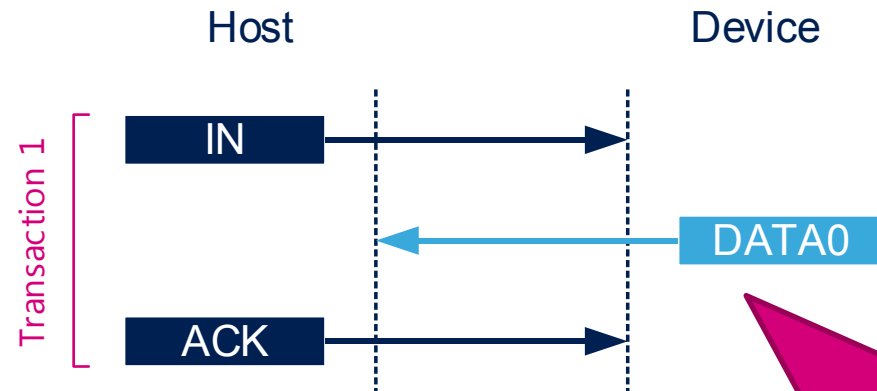
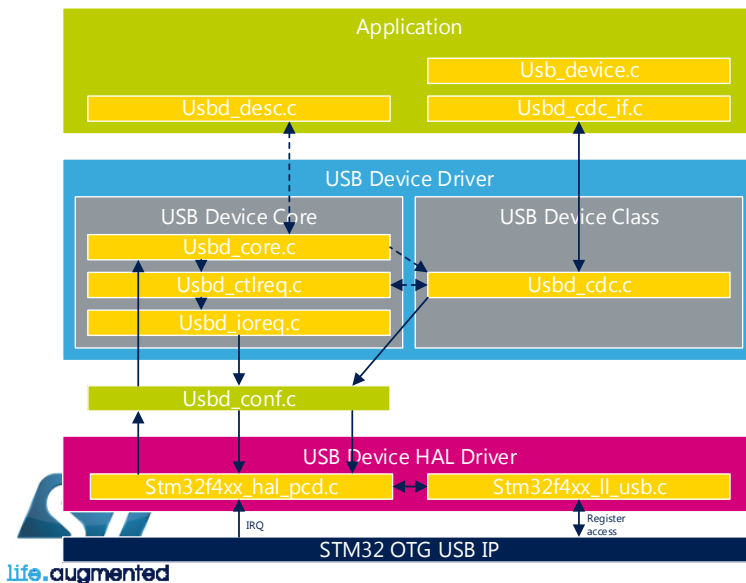
```
void HAL_PCD_IRQHandler(PCD_HandleTypeDef *hpcd)
```

This function calls
PCD_WriteEmptyTxFifo which transfers
data from buffer into EP FIFO

```
static HAL_StatusTypeDef  
PCD_WriteEmptyTxFifo(PCD_HandleTypeDef *hpcd, uint32_t epnum)
```



```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```



When data are in fifo then the device answer on IN by DATA0/DATA1 packet instead of NAK

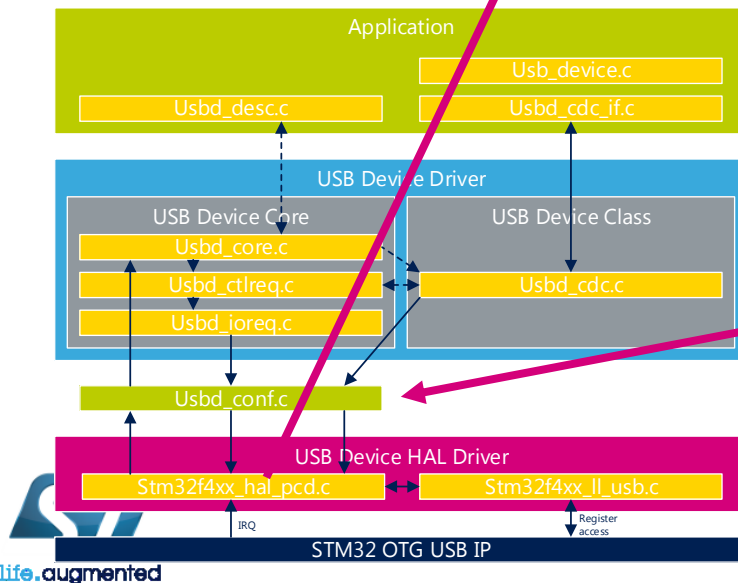
```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

When all bytes are transferred (previously set in register) Tx complete interrupt is triggered

```
void HAL_PCD_IRQHandler(PCD_HandleTypeDef *hpcd)
```

HAL_PCD_DataInStageCallback is called

```
void HAL_PCD_DataInStageCallback(PCD_HandleTypeDef *hpcd, uint8_t epnum)
```

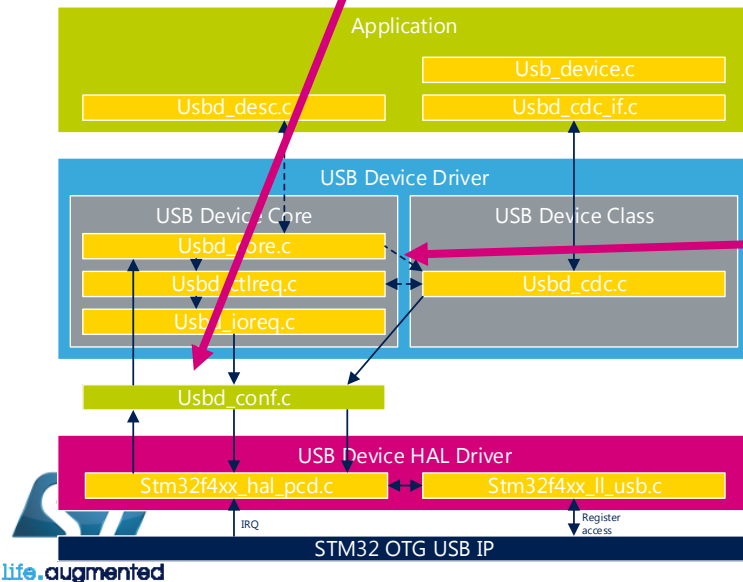


```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

```
void HAL_PCD_DataInStageCallback(PCD_HandleTypeDef *hpcd,
uint8_t epnum)
```

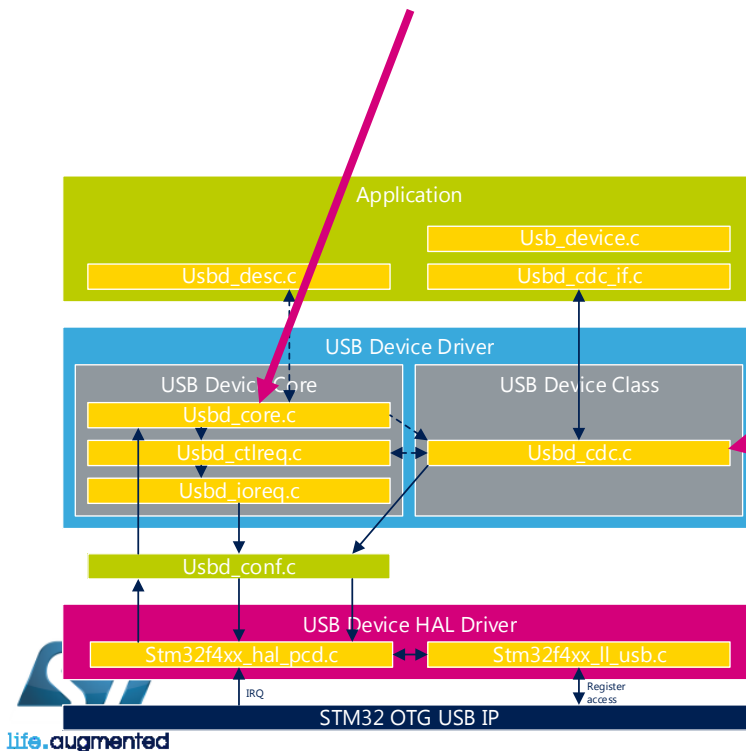
This calls USBD_LL_DataInStage

```
USB_StatusTypeDef
USB_LL_DataInStage(USB_HandleTypeDef
Def *pdev ,uint8_t epnum, uint8_t
*pdata)
```



```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

```
USB_StatusTypeDef USB_LL_DataInStage(USB_HandleTypeDef *pdev,
uint8_t epnum, uint8_t *pdata)
```



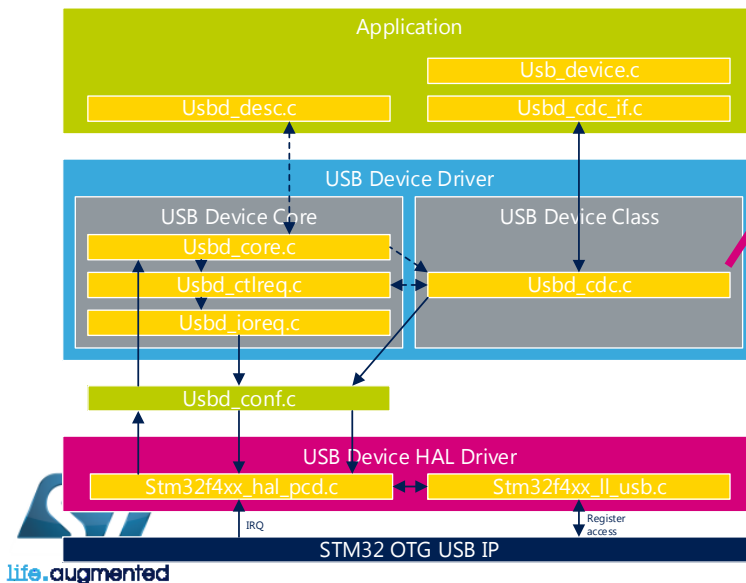
```
typedef struct _Device_cb
{
    uint8_t (*Init)          (struct _USB_D_HANDLETypeDef *pdev , uint8_t cfgidx);
    uint8_t (*DeInit)        (struct _USB_D_HANDLETypeDef *pdev , uint8_t cfgidx);
    /* Control Endpoints*/
    uint8_t (*Setup)          (struct _USB_D_HANDLETypeDef *pdev , USB_SetupReqTypeDef *req);
    uint8_t (*EP0_TxSent)     (struct _USB_D_HANDLETypeDef *pdev );
    uint8_t (*EP0_RxReady)    (struct _USB_D_HANDLETypeDef *pdev );
    /* Class Specific Endpoints*/
    uint8_t (*DataIn)         (struct _USB_D_HANDLETypeDef *pdev , uint8_t epnum);
    uint8_t (*DataOut)        (struct _USB_D_HANDLETypeDef *pdev , uint8_t epnum);
    uint8_t (*SOF)            (struct _USB_D_HANDLETypeDef *pdev );
    uint8_t (*IsoINIncomplete) (struct _USB_D_HANDLETypeDef *pdev , uint8_t epnum);
    uint8_t (*IsoOUTIncomplete) (struct _USB_D_HANDLETypeDef *pdev , uint8_t epnum);
} USB_ClassTypeDef;

uint8_t (*GetUSBStrDescriptor)(struct _USB_D_HANDLETypeDef *pdev ,uint8_t index, uint16_t *length);
#endif
} USB_ClassTypeDef;
```

This function calls out pointer in DataIn function defined our class structure

```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
```

```
static uint8_t USBDCDC_DataIn (USB_HandleTypeDef *pdev, uint8_t  
epnum)
```

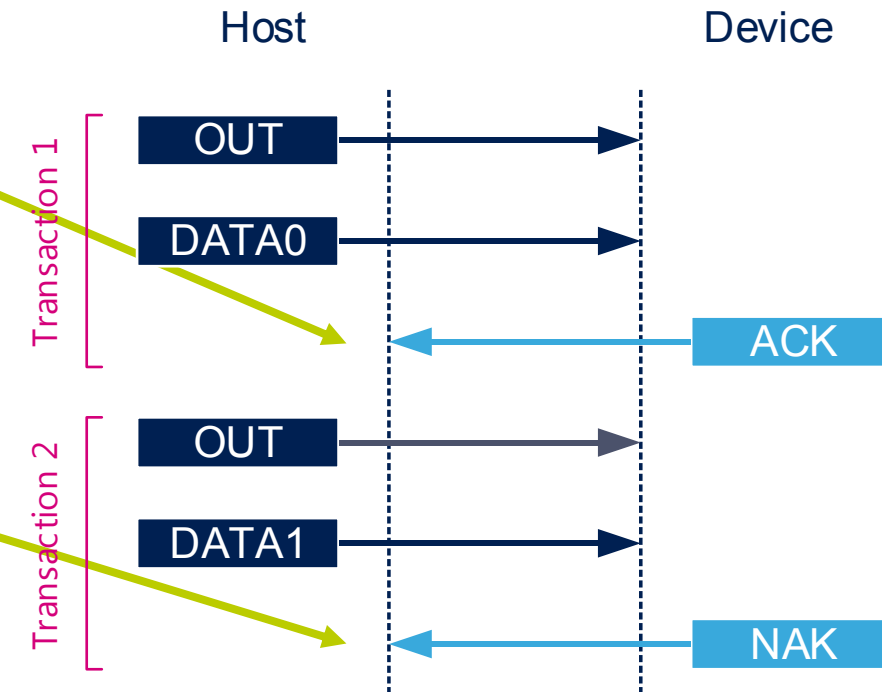


In our case it is called `USBD_CDC_DataIn`

Here the `TxState` is set to 0 which allow to use `CDC_Transmit_FS` function again.

In this moment we can use the buffer again

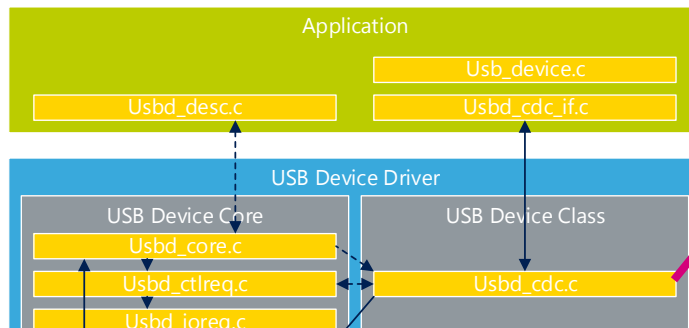
- EP can receive only preconfigured amount of data or less
- Then is EP deactivated.
- If not activated it will not receive more data.
- They are NAKed
- Our libraries enable EP after enumeration
- But after successful receive it must be enabled by user code



Prepare buffer where we will store our data

First the CDC structure is used to store buffer source for TX

```
uint8_t USBD_CDC_SetRxBuffer (USB_HandleTypeDef *pdev, uint8_t *pbuff)
```



Structure is pointed from main USB handle it is dynamically allocated (from HEAP) during enumeration

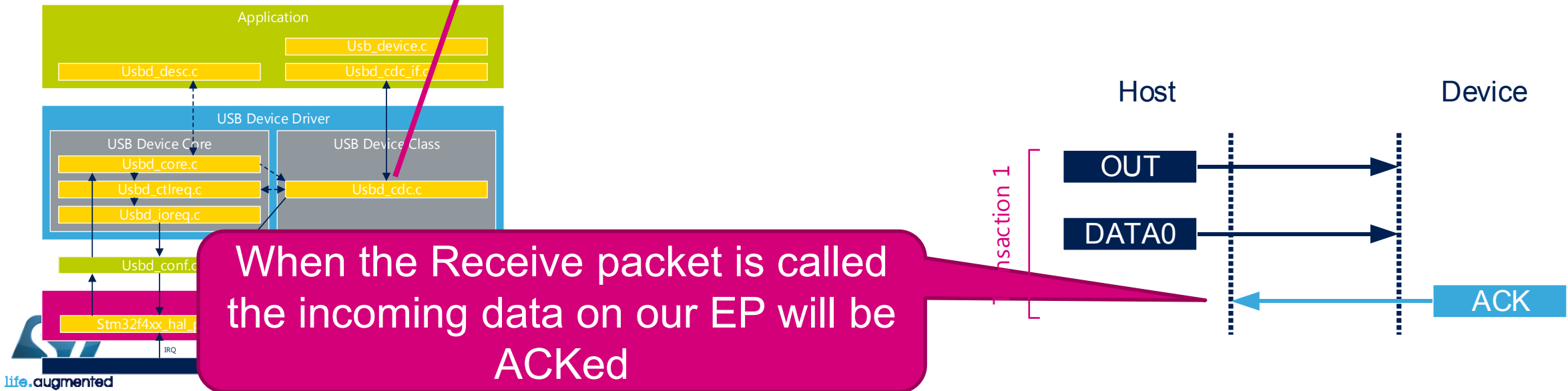
```
typedef struct
{
    uint32_t data[CDC_DATA_HS_MAX_PACKET_SIZE/4];    /* Force 32bits alignment */
    uint8_t  CmdOpCode;
    uint8_t  CmdLength;
    uint8_t  *RxBuffer;
    uint8_t  *TxBuffer;
    uint32_t RxLength;
    uint32_t TxLength;

    __IO uint32_t TxState;
    __IO uint32_t RxState;
}
USB_CDC_HandleTypeDef;
```

Here Rx size is defined by CDC max packet size(64B in FS)

Allow to start receiving packets.
Before all data are NAKed

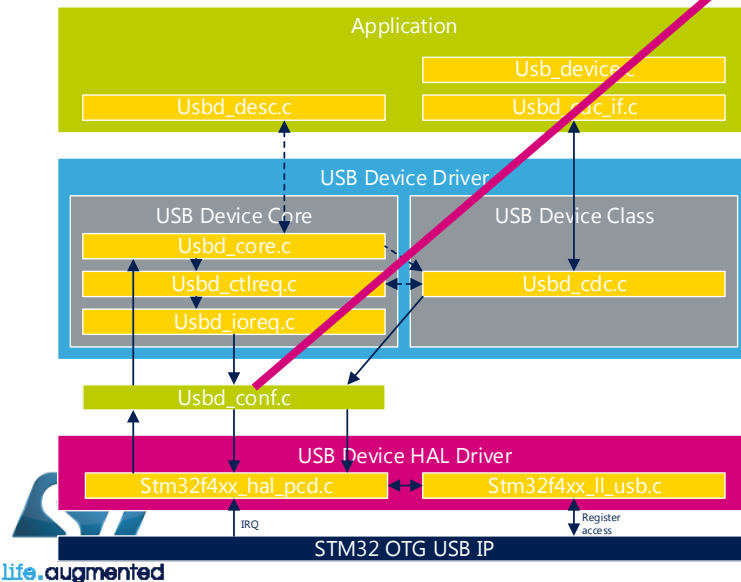
```
uint8_t USBDCDReceivePacket(USB_HandleTypeDef *pdev)
```



```
uint8_t USBDCDC_ReceivePacket(USB_HandleTypeDef *pdev)
```

Function will prepare the EP for receive

USB_LL_PrepareReceive

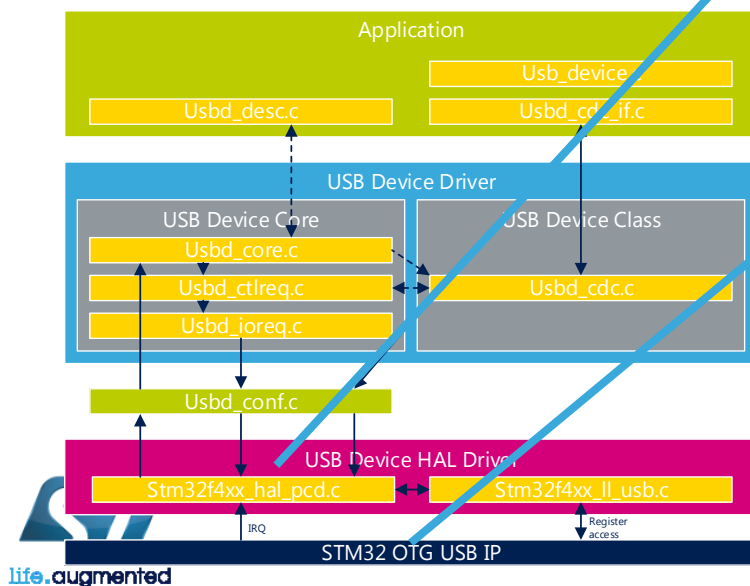


USBD_LL_PrepareReceive

HAL_PCD_EP_Receive

LL driver to configure registers on USB IP

USB_EPStartXfer

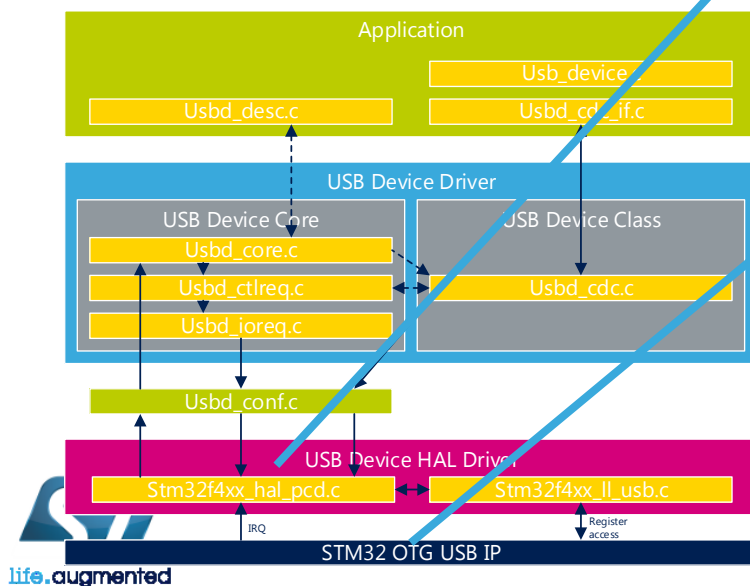


USBD_LL_PrepareReceive

HAL_PCD_EP_Receive

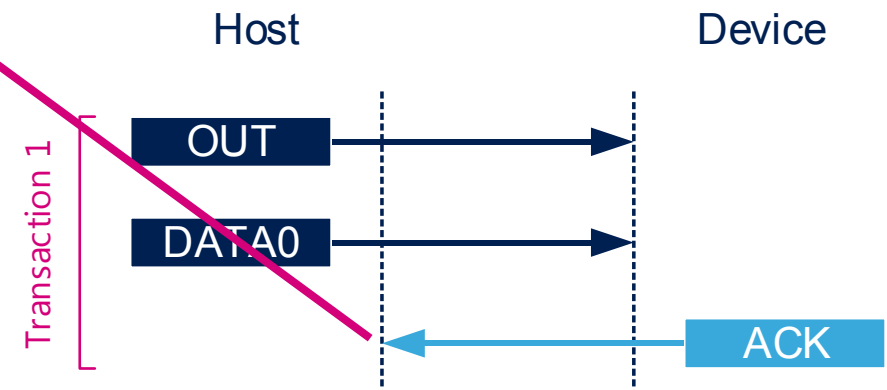
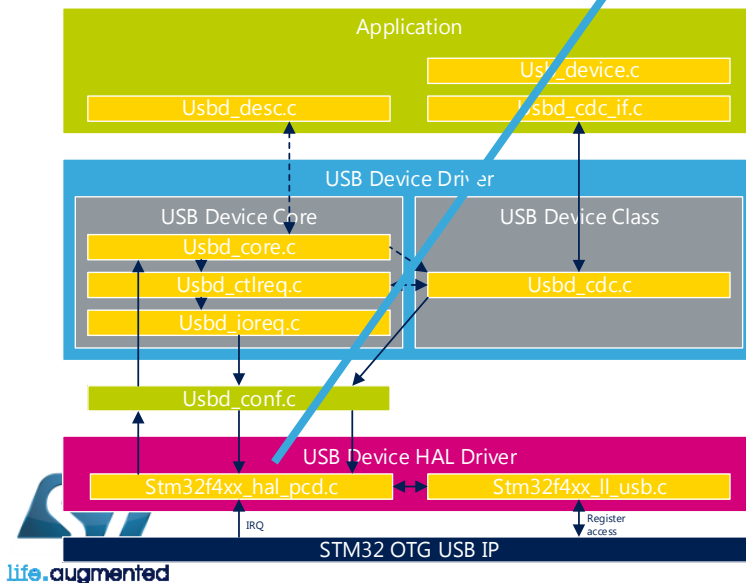
LL driver to configure registers on USB IP

USB_EPStartXfer



When data are ACKed the USB IRQ is called and inside is called PCD handler to handle interrupt routine

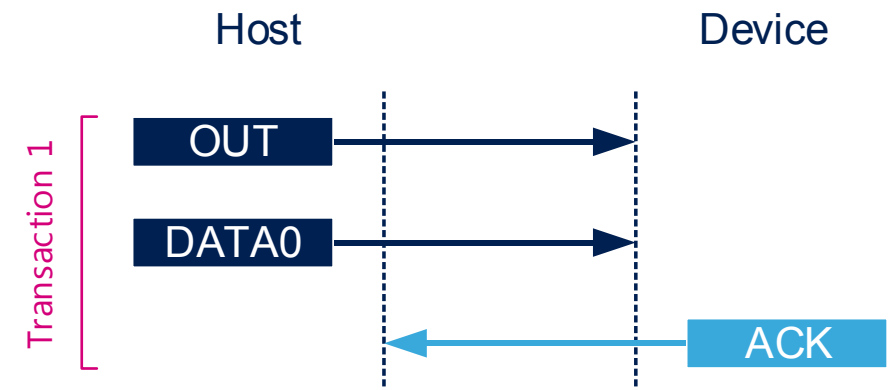
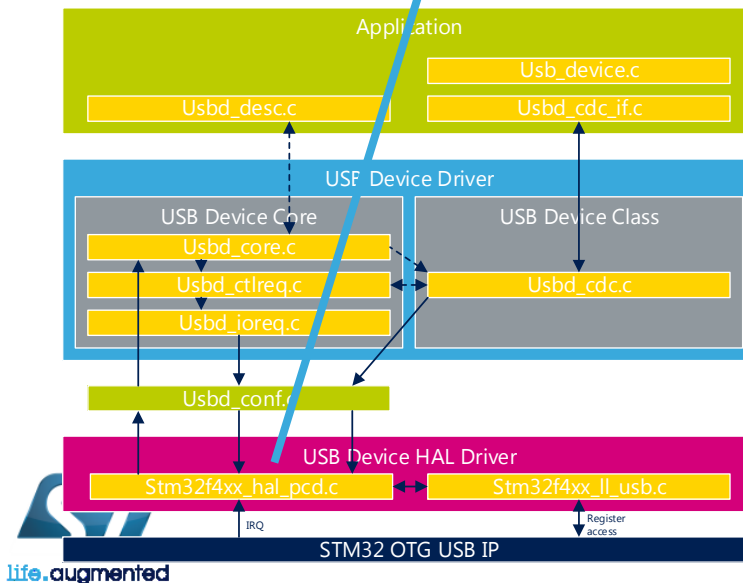
```
HAL_PCD_IRQHandler(&hpcd_USB_OTG_FS);
```



```
HAL_PCD_IRQHandler(&hpcd_USB_OTG_FS);
```

USB_ReadPacket

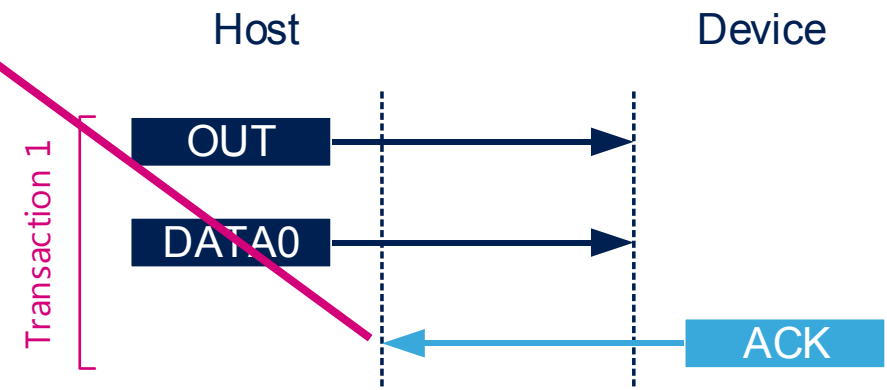
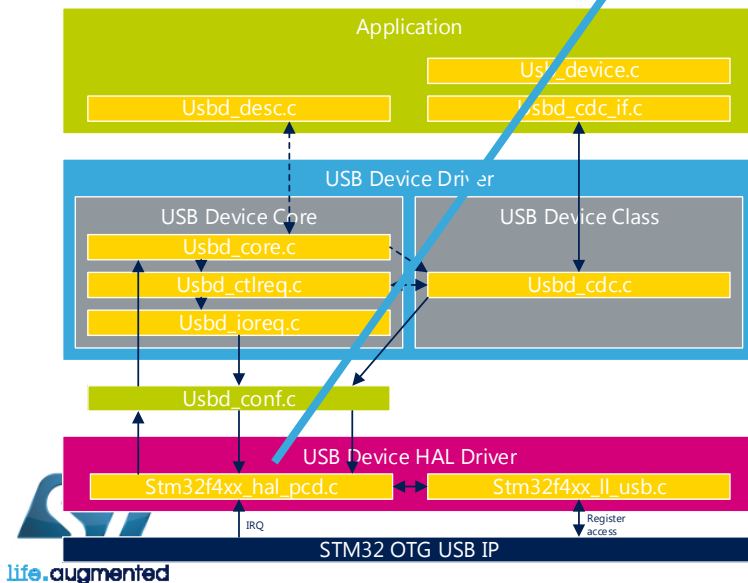
First the packed is read from RX fifo




```
HAL_PCD_IRQHandler(&hpcd_USB_OTG_FS);
```

```
HAL_PCD_DataInStageCallback(hpcd, epnum);
```

OUT packet transfe complete interrupt routines



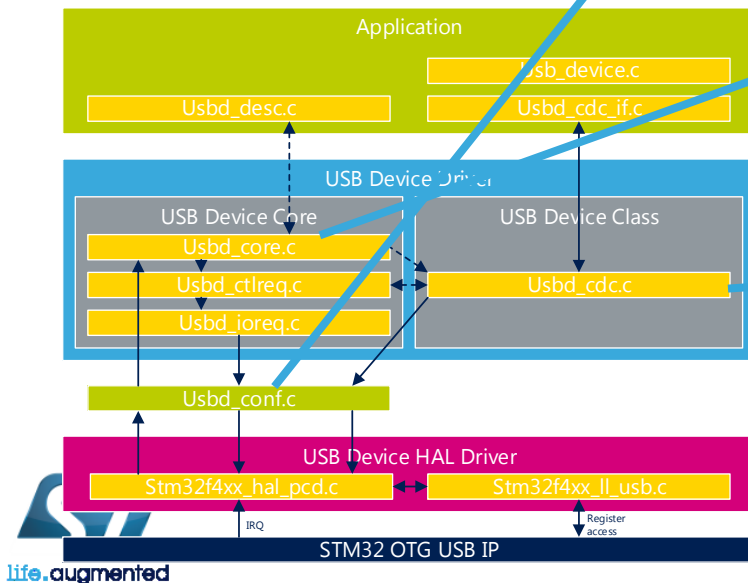
```
HAL_PCD_IRQHandler(&hpcd_USB_OTG_FS);
```

```
HAL_PCD_DataInStageCallback(hpcd, epnum);
```

```
USBD_LL_DataOutStage
```

```
pdev->pClass->DataOut(pdev, epnum);
```

Call class structure, in our case
CDC



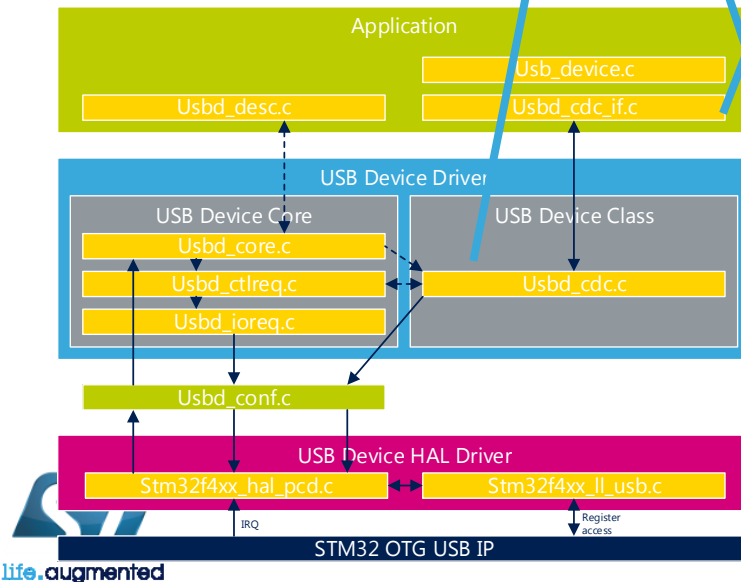
```
((USBDCDC_ItfTypeDef *)pdev->pUserData)->Receive(hdc->RxBuffer, &hcdc->RxLength);
```

```
static int8_t CDC_Receive_FS (uint8_t* Buf, uint32_t *Len)
```

Receive IF function into our application

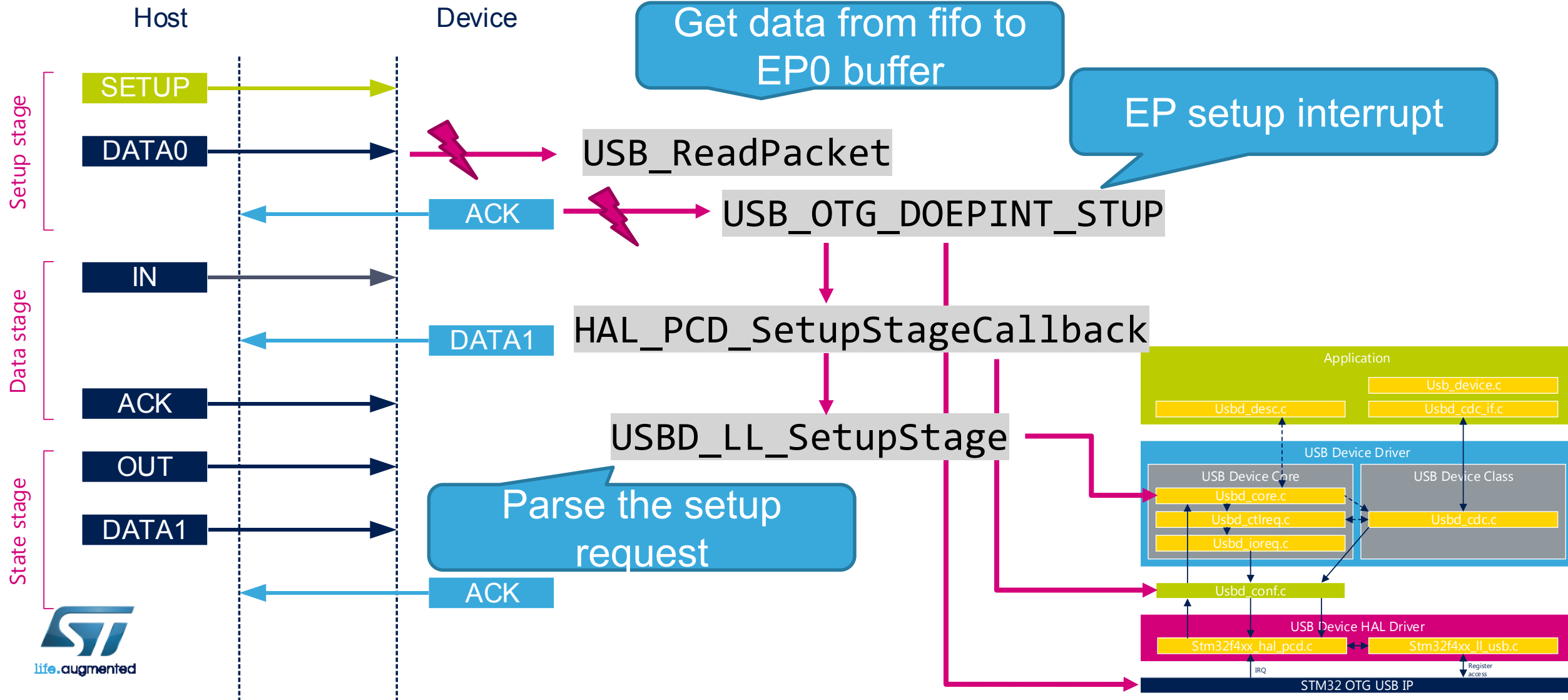
```
USBDCDC_ItfTypeDef USBDCDC_Interface_fops_FS =
{
    CDC_Init_FS,
    CDC_DeInit_FS,
    CDC_Control_FS,
    CDC_Receive_FS
};
```

USBDCDC_ReceivePacket must be called again to be able receive more packets



Control packet handling

220



Control packet handling

221

USBD_LL_SetupStage

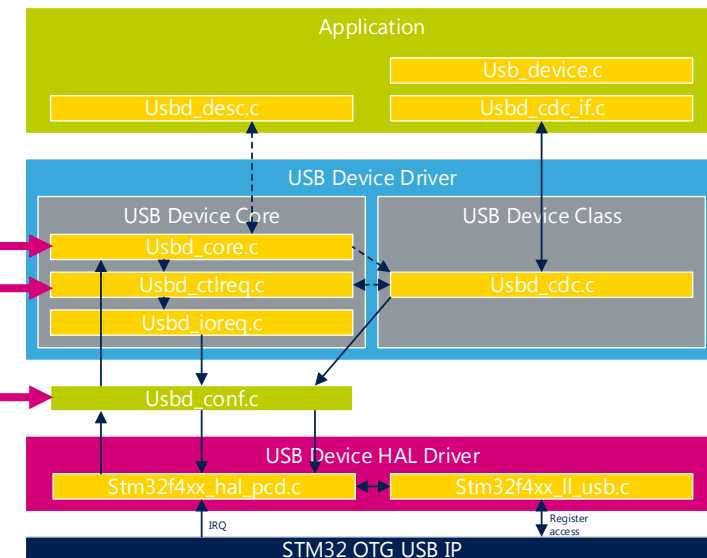
USBD_StdDevReq

USBD_StdItfReq

USBD_StdEPReq

USBD_LL_StallEP

Setup request not
recognized device
answer with STALL



Control packet handling

222

USBD_LL_SetupStage

USBD_StdDevReq

USBD_StdItfReq

USBD_StdEPReq

Usb_core.c

Usb_cdc.c

Usb_desc.c

USBD_CtlSendData

USBD_CtlSendStatus

USBD_LL_Transmit

Host

Device

SETUP

DATA0

ACK

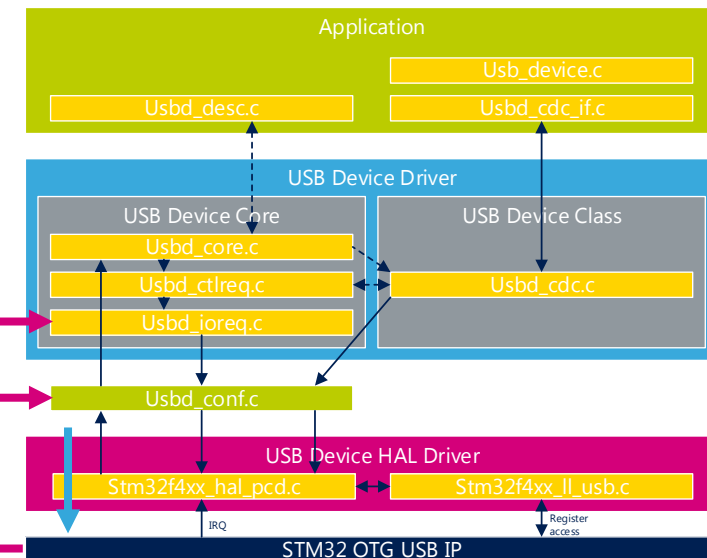
IN

DATA1

ACK

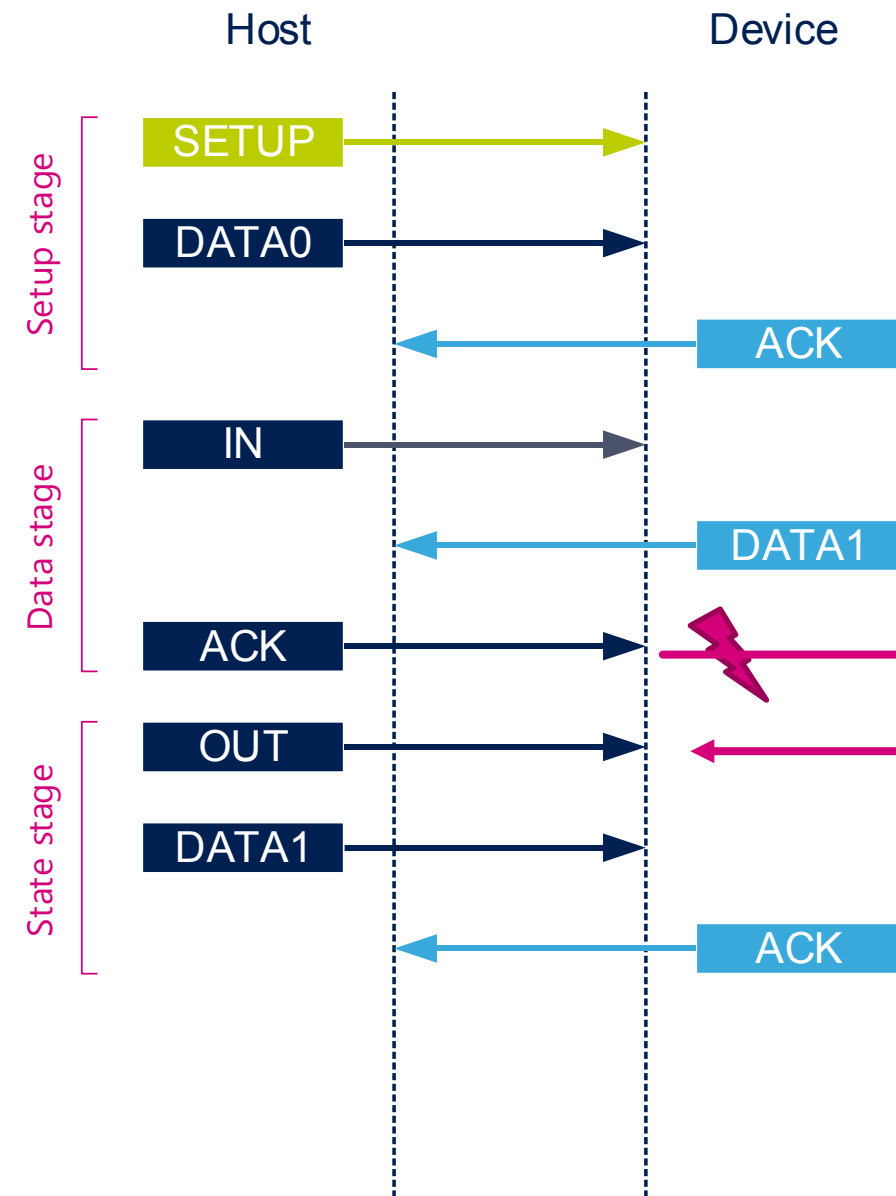
Setup stage

Data stage



Control packet handling

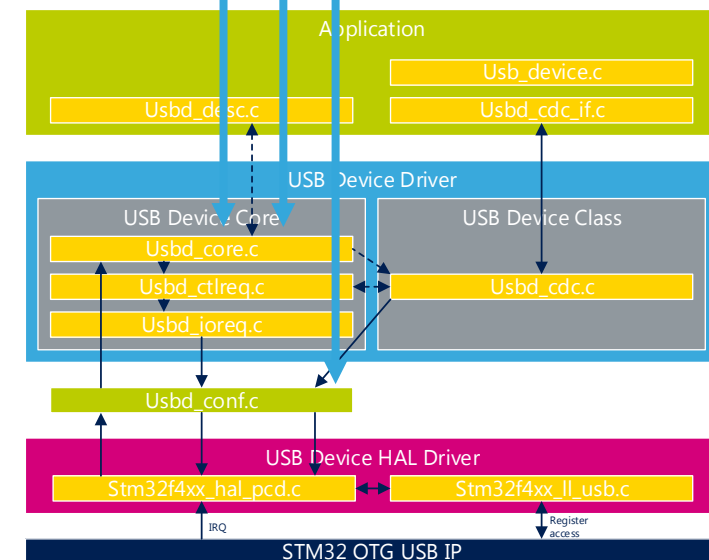
223



HAL_PCD_DataInStageCallback

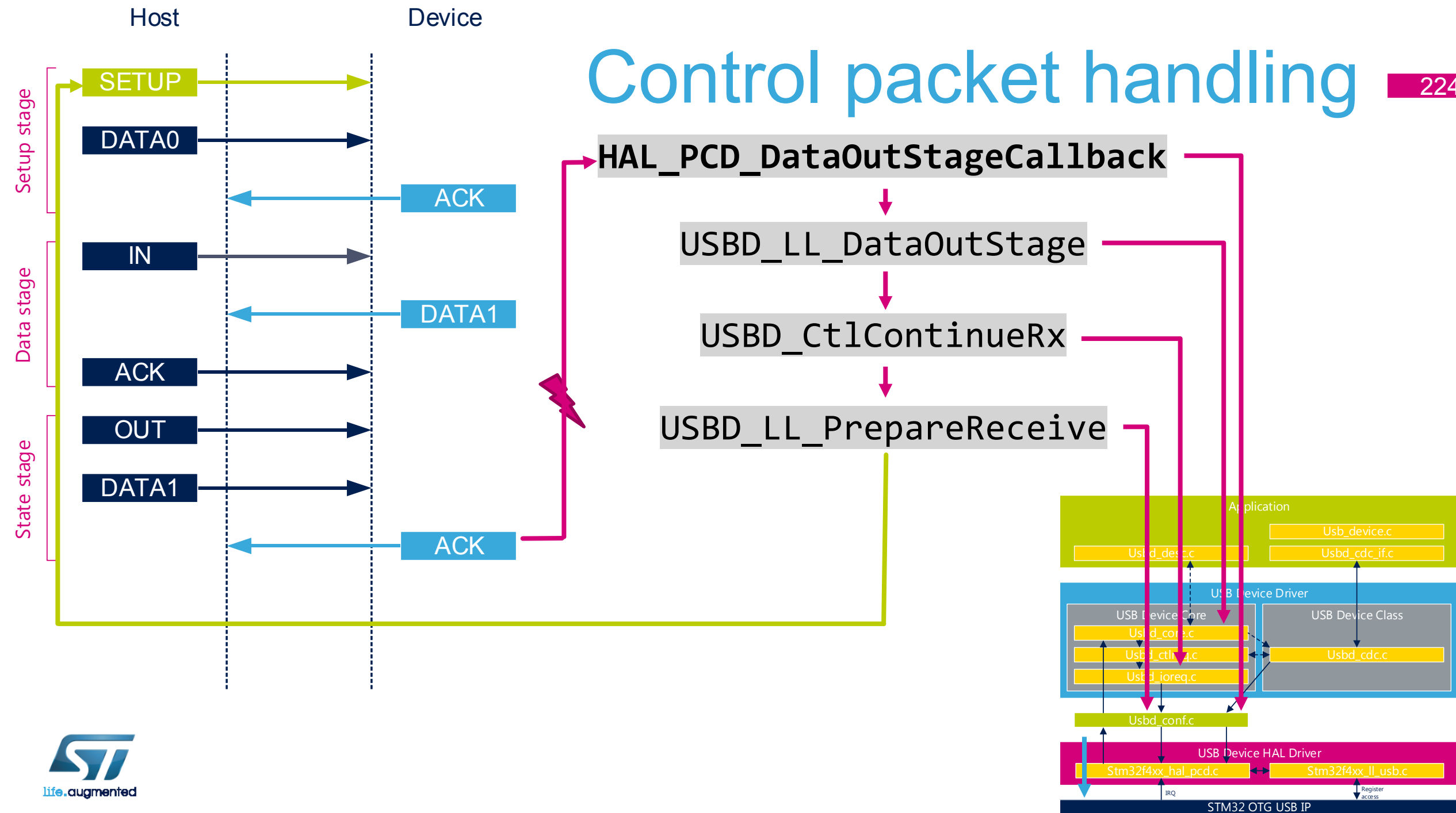
USBD_LL_DataInStage

USBD_LL_PrepareReceive



Control packet handling

224



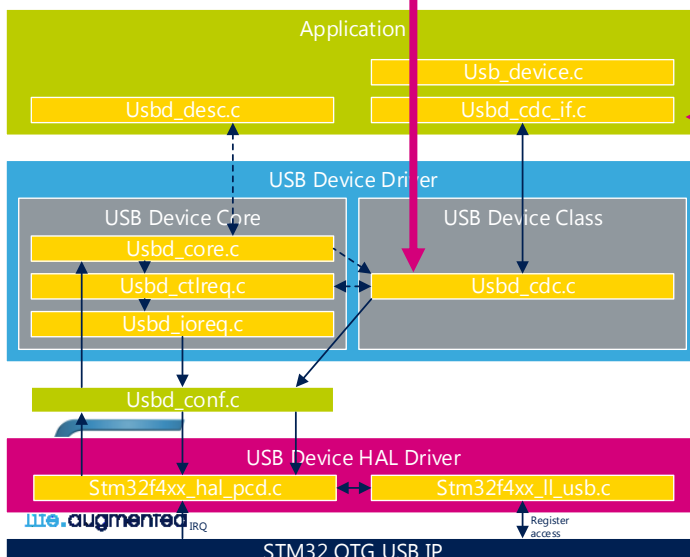
Device IF structures

225

This is name is used to call the pointer function

```
((USBD_CDC_ItfTypeDef *)  
pdev->pUserData)->Init();
```

```
USBD_CDC_ItfTypeDef  
typedef struct _USBD_C USBD_Interface_fops_FS =  
{  
    int8_t (* Init) ↔ CDC_Init_FS,  
    int8_t (* DeInit) ↔ CDC_DeInit_FS,  
    int8_t (* Control) ↔ CDC_Control_FS,  
    int8_t (* Receive) ↔ CDC_Receive_FS  
};  
}USBD_CDC_ItfTypeDef;
```



Definition of function pointers which will be used in `usbd_cdc.c`

Real function implementation In `usbd_cdc_if.c`

This is name is used to call the pointer function
pdev->pClass->Init(pdev, cfgidx)

Device Class structures

226

```
typedef struct _Device_cb
{
    uint8_t (*Init)
    uint8_t (*DeInit)
    /* Control Endpoints*/
    uint8_t (*Setup)
    uint8_t (*EP0_TxSent)
    uint8_t (*EP0_RxReady)
    /* Class Specific Endpoints*/
    uint8_t (*DataIn)
    uint8_t (*DataOut)
    uint8_t (*SOF)
    uint8_t (*IsoINIncomplete)
    uint8_t (*IsoOUTIncomplete)

    uint8_t *(*GetHSCfgDescriptor)(uint16_
    uint8_t *(*GetFSCfgDescriptor)(uint16_
    uint8_t *(*GetOtherSpeedCfgDescriptor)
    uint8_t *(*GetDeviceQualifierDescriptor)(

#if (USB_SUPPORT_USER_STRING == 1)
    uint8_t *(*GetUsrStrDescriptor)(struct _USB_HandleTypeDev *pdev, uint8_t index, uint
#endif
} USB_ClassTypeDef;

USB_ClassTypeDef USB_CDC =
{
    USB_CDC_Init,
    USB_CDC_DeInit,
    USB_CDC_Setup,
    NULL, /* EP0_TxSent, */
    USB_CDC_EP0_RxReady,
    USB_CDC_DataIn,
    USB_CDC_DataOut,
    NULL,
    NULL,
    NULL,
    USB_CDC_GetHSCfgDesc,
    USB_CDC_GetFSCfgDesc,
    USB_CDC_GetOtherSpeedCfgDesc,
    USB_CDC_GetDeviceQualifierDescriptor,
};
```

Defined in
usb_cdc.c

Used in
usb_core.c

Usbd structure

227

```
/* USB Device handle structure */
typedef struct _USB_HANDLETypeDef
{
    uint8_t
    uint32_t
    uint32_t
    uint32_t
    USB_SpeedTypeDef
    USB_EndpointTypeDef
    USB_EndpointTypeDef
    uint32_t
    uint32_t
    uint8_t
    uint8_t
    uint8_t
    uint8_t
    uint8_t
    uint8_t
    uint32_t

    USB_SetupReqTypeDef
    USB_DescriptorsTypeDef
    USB_ClassTypeDef
    void
    void
    void
} USB_HANDLETypeDef;
```

```
id;
dev_config;
dev_default_config;
dev_config_status;
dev_speed;
ep_in[15];
ep_out[15];
ep0_state;
ep0_data_len;
dev_state;
dev_old_state;
dev_address;
dev_connection_status;
dev_test_mode;
dev_remote_wakeup;

request;
*pDesc;
*pClass;
*pClassData;
*pUserData;
*pData;
```

USB_ClassTypeDef

Defined in
usb_cdc.c

USB_CDC_HANDLETypeDef

Dynamically allocated in usb_cdc.c

USB_CDC_ItfTypeDef

Defined in usb_cdc_if.c

Defined in usb_conf.c.c

Handle for usb LL driver

PCD_HANDLETypeDef