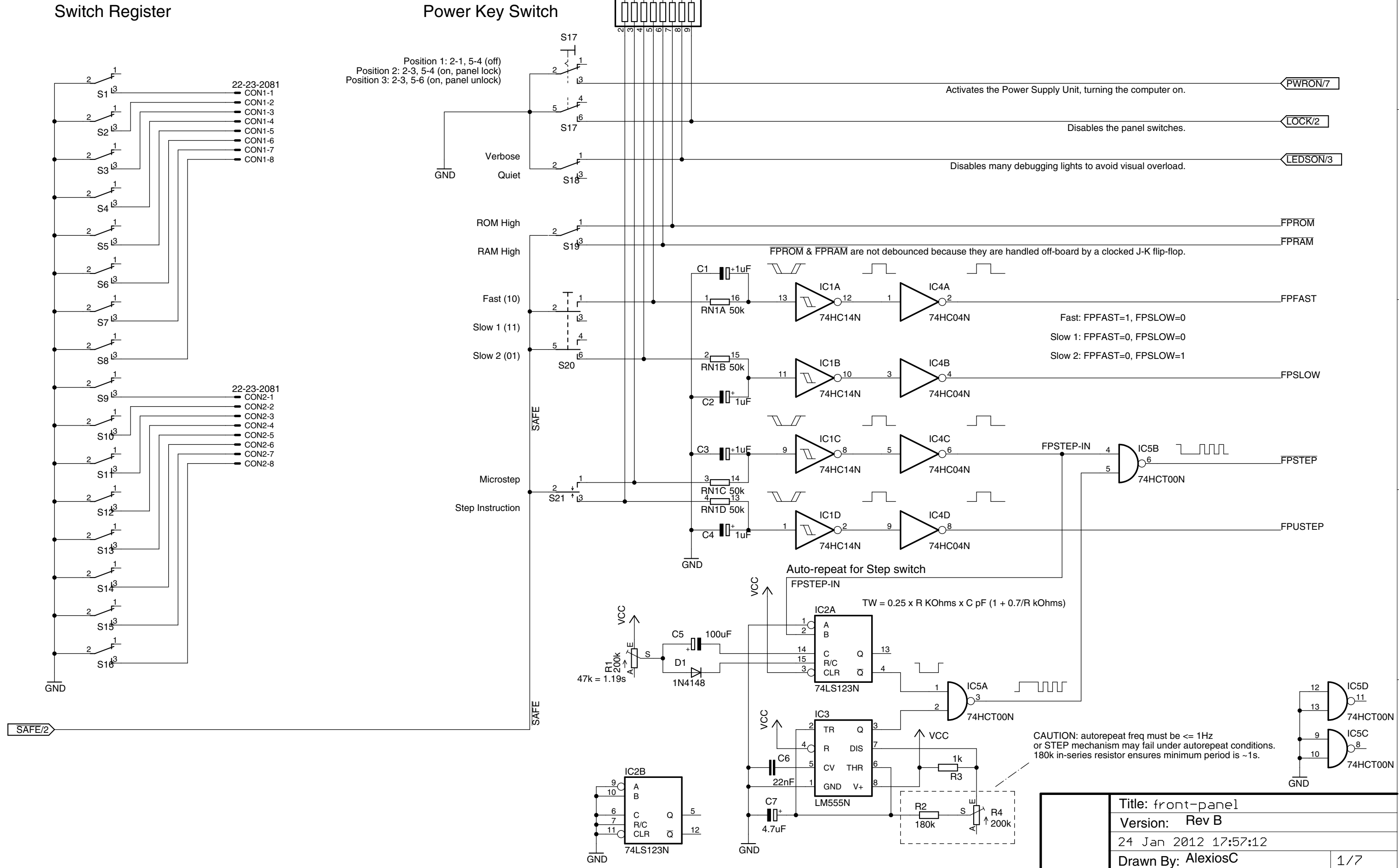
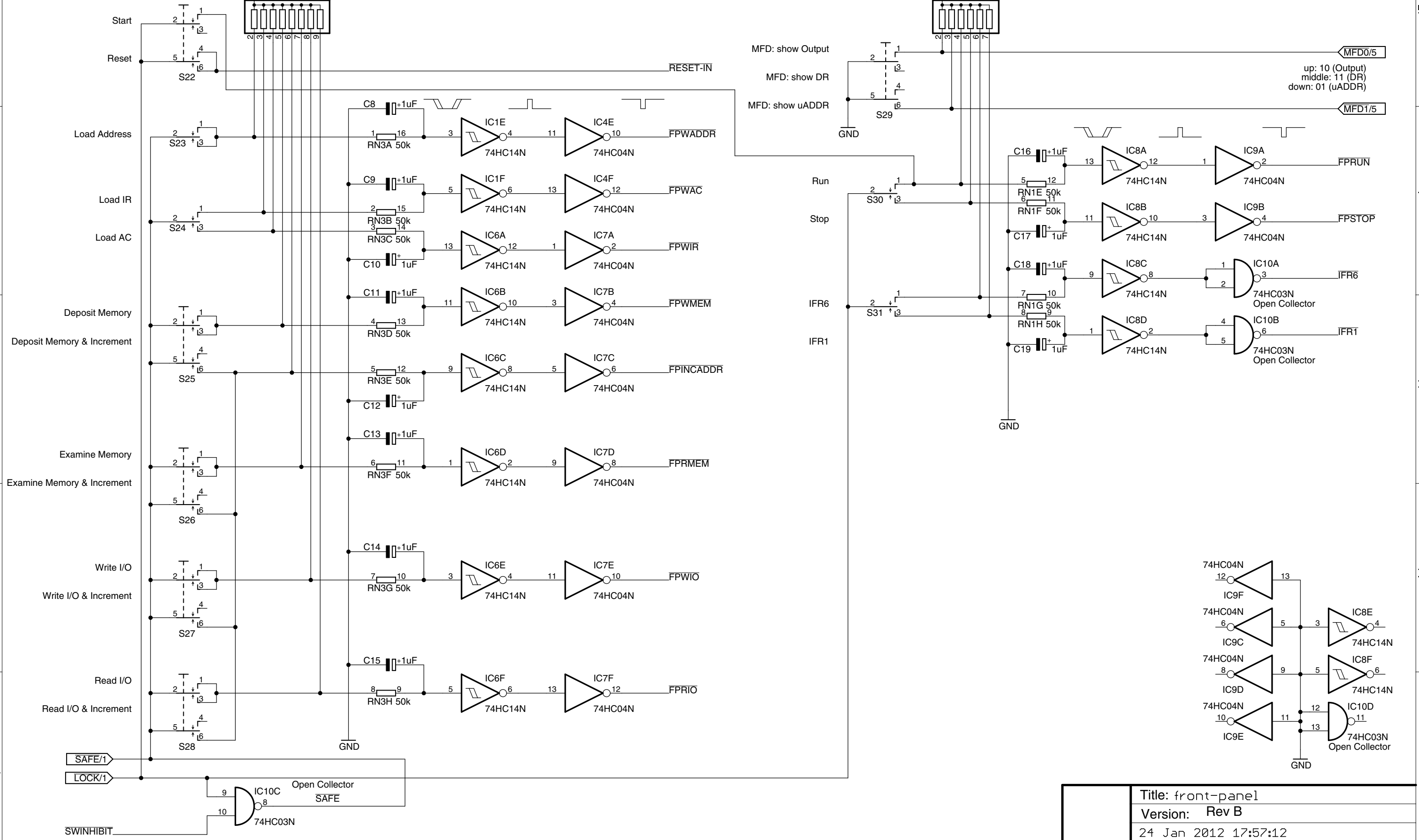


Front Panel Switches 1



CFT CPU

Front Panel Switches 2

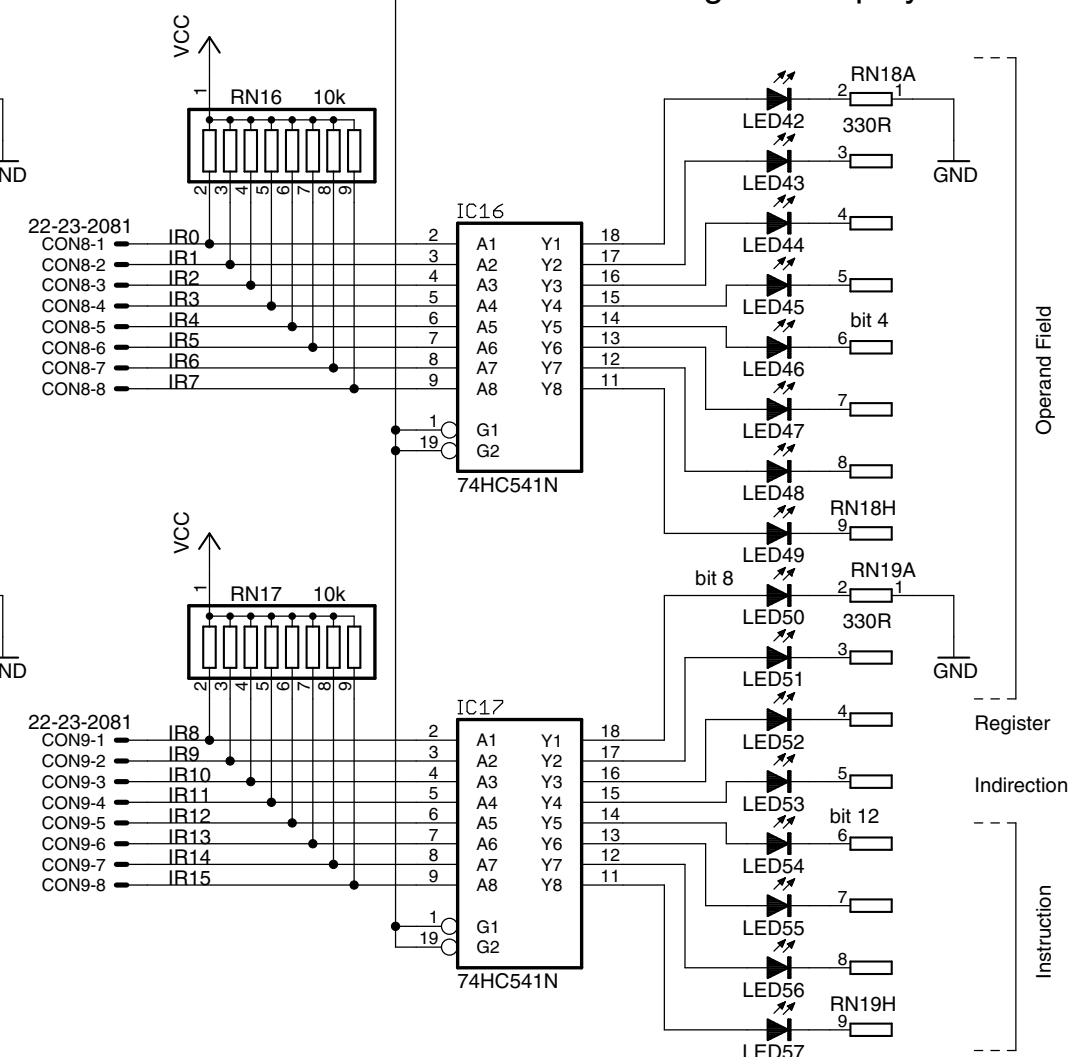
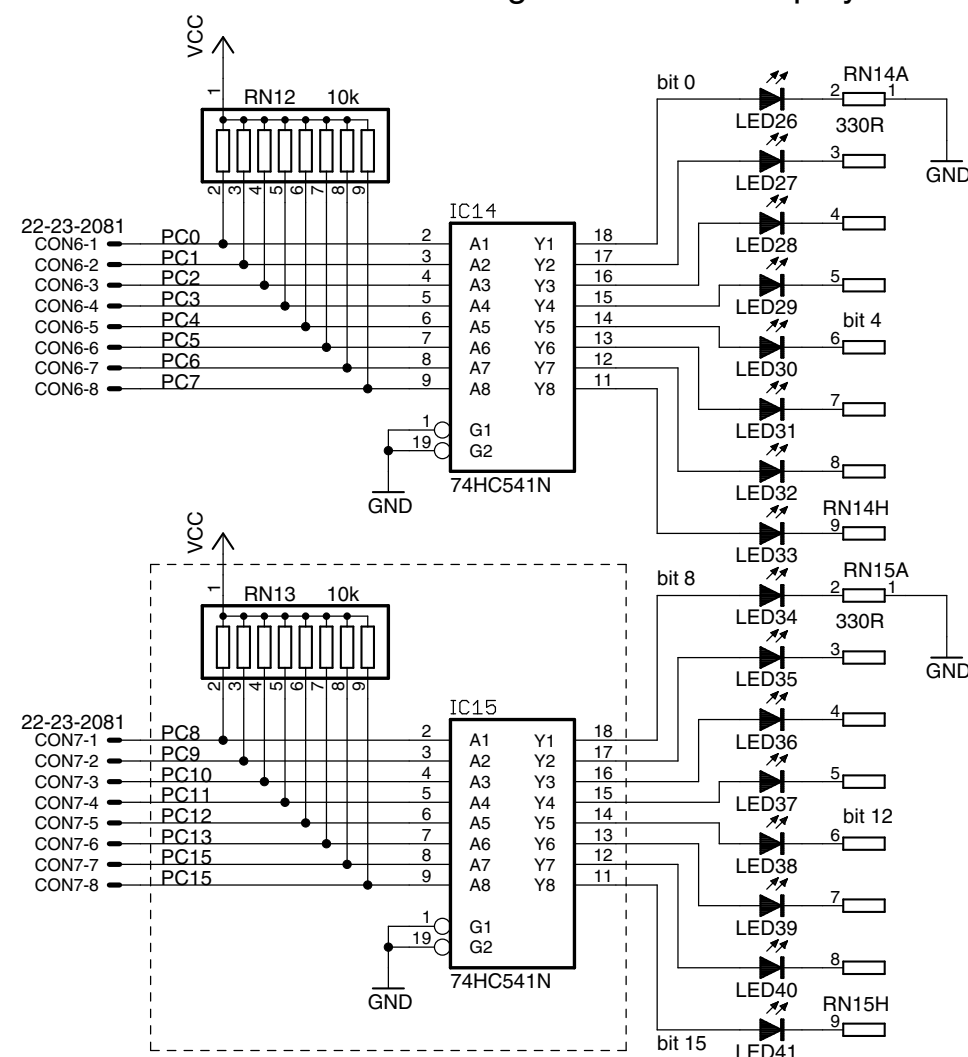
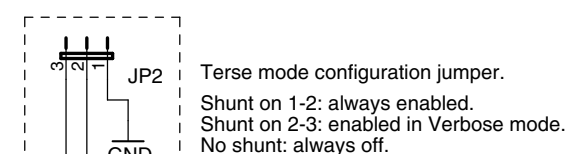


SWINHIBIT disables some panel switches while the processor is running.

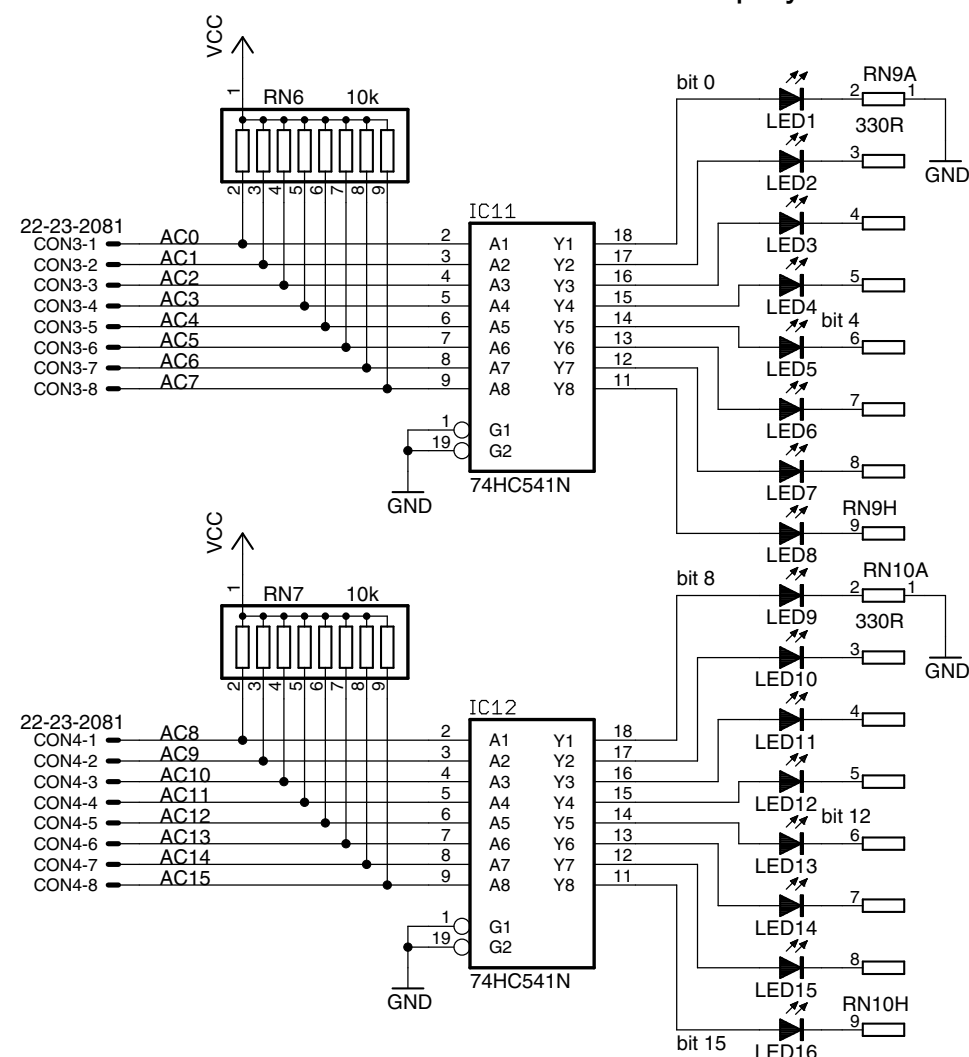
Title: front-panel	
Version: Rev B	
24 Jan 2012 17:57:12	
Drawn By: AlexiosC	2/7

CFT CPU

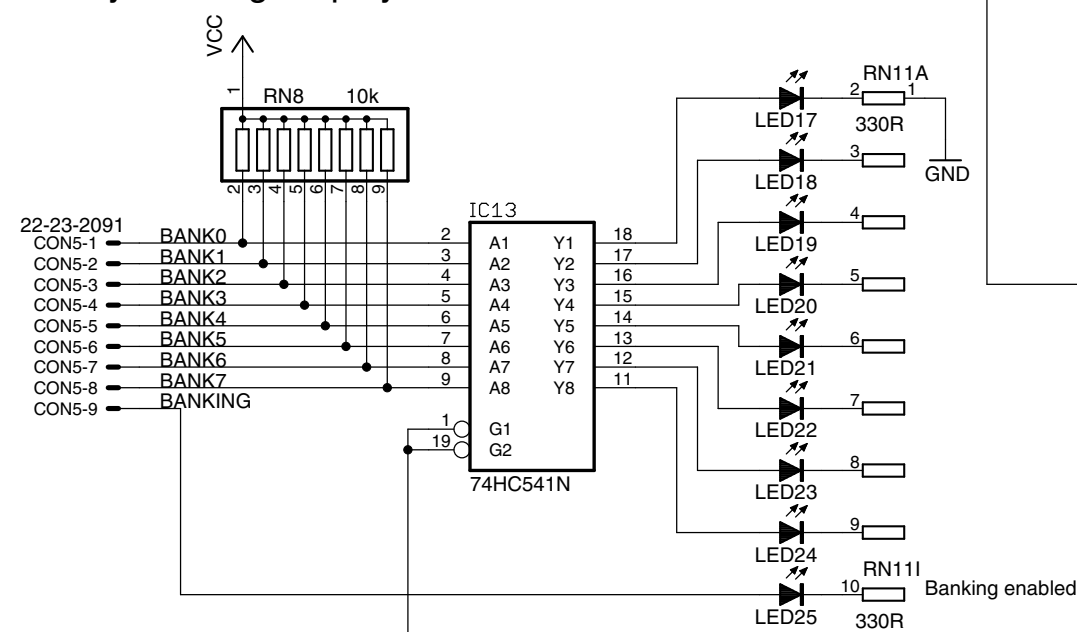
Front Panel LEDs 1



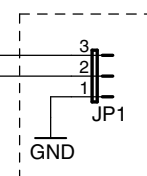
Accumulator Display



Memory Banking Display



Please note: in all cases, buffer ICs may be moved to the respective processor board. In this case, the pull-up resistors on the buffers' inputs may be omitted.



Terse mode configuration jumper.
Shunt on 1-2: always enabled.
Shunt on 2-3: enabled in Verbose mode.
No shunt: always off.

Title: front-panel	
Version: Rev B	
24 Jan 2012 17:57:12	
Drawn By: AlexiosC	3/7

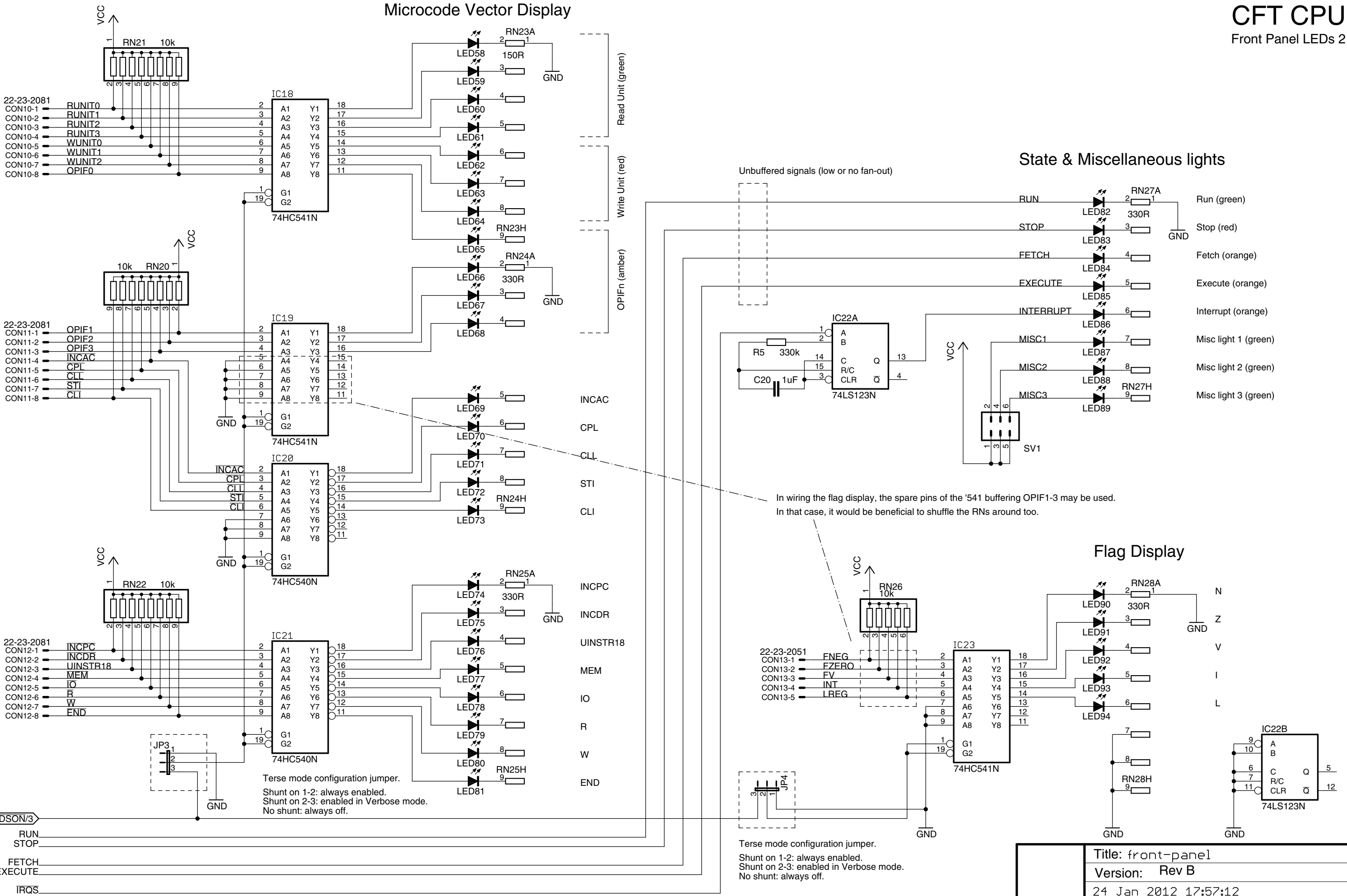
CFT CPU

Front Panel LEDs 2

Microcode Vector Display

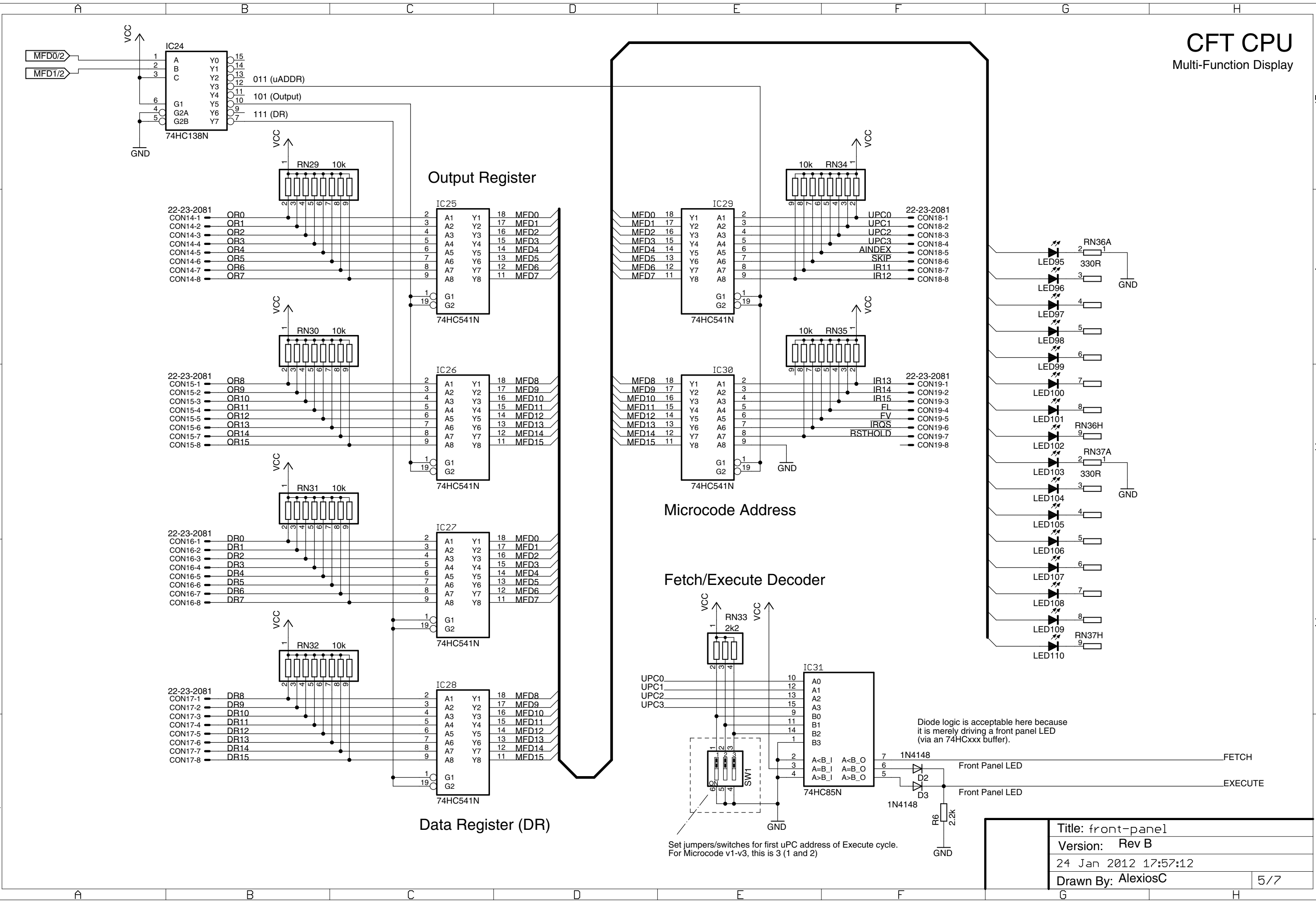
State & Miscellaneous lights

Flag Display



CFT CPU

Multi-Function Display

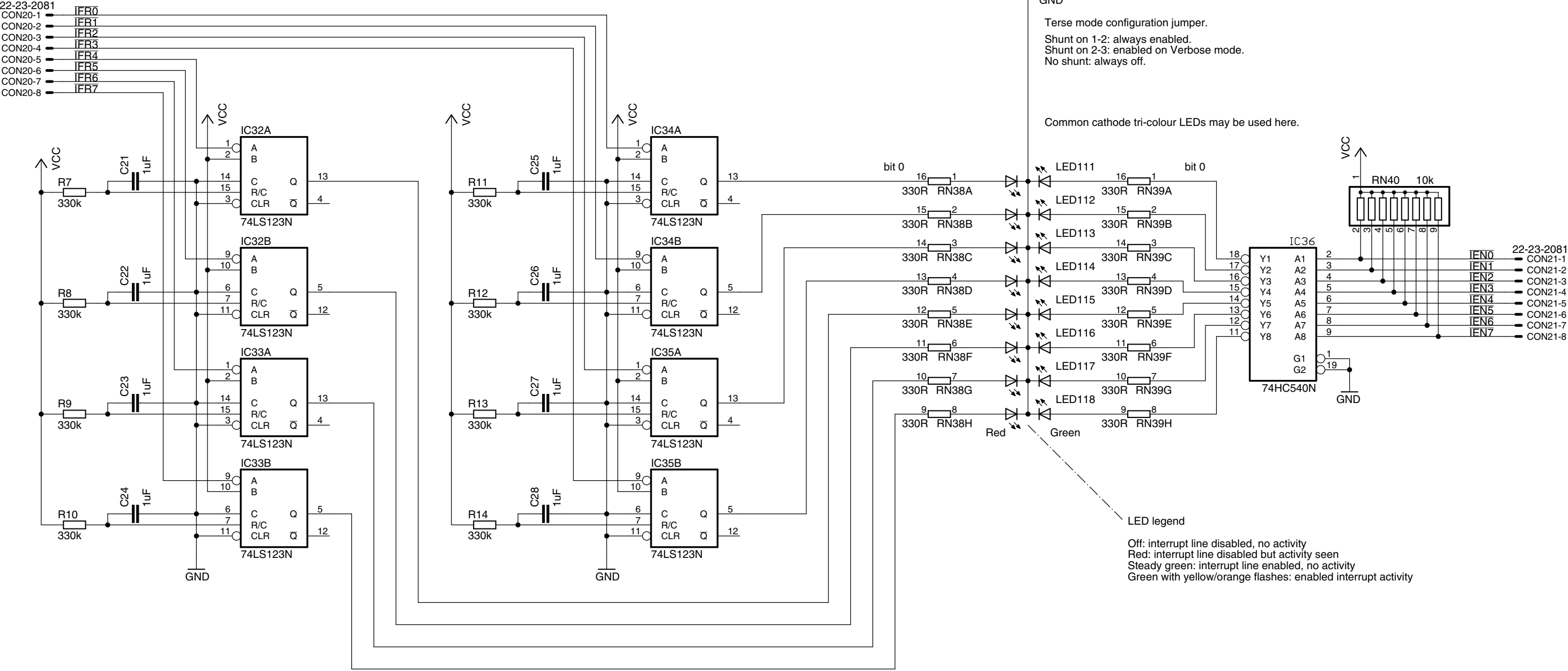


CFT CPU

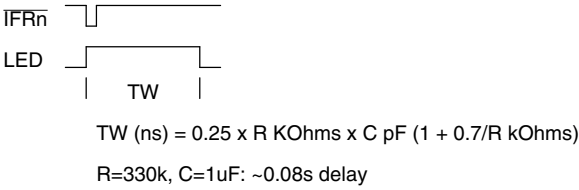
Front Panel LEDs, Interrupt display

LEDSON/4

Interrupt Status Display



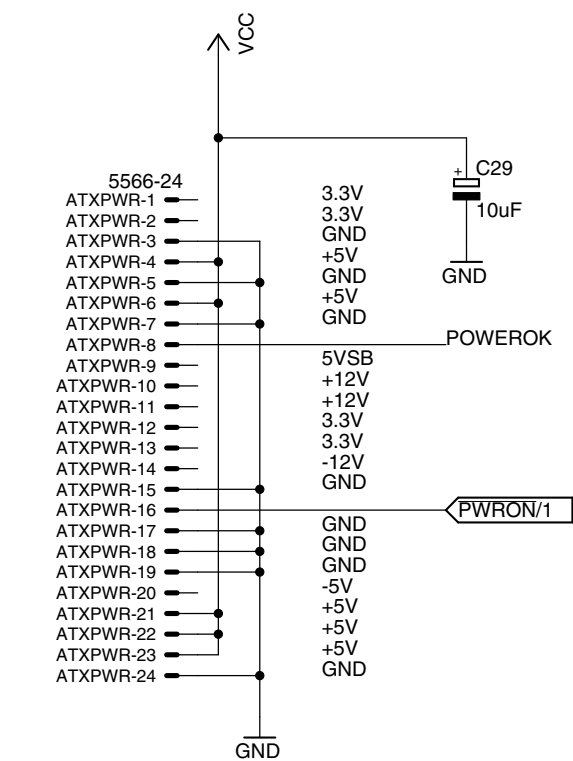
IFRn pulses are not normally visible to the naked eye.
'123 Dual Monostable Vibrators produce pulses for IRQ signals.



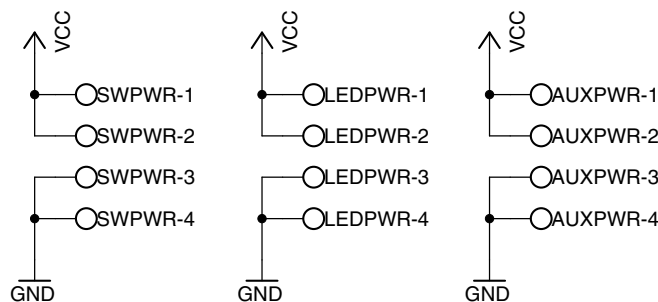
Title: front-panel	
Version: Rev B	
24 Jan 2012 17:57:12	
Drawn By: AlexiosC	6/7

CFT CPU

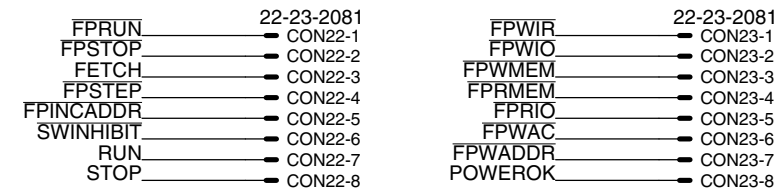
Front Panel Connectors



ATX 24-pin power socket (Molex Mini Fit Jr)
May use screw terminals.



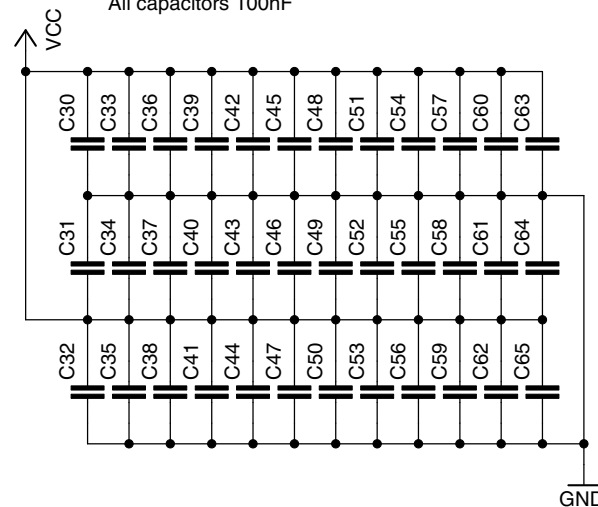
Front Panel Controller Connections



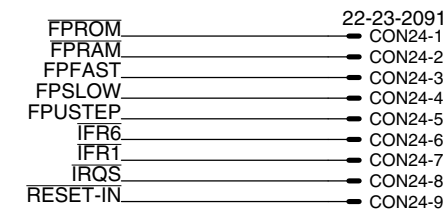
Connector to Front Panel Controller Card.

Decoupling Capacitors

All capacitors 100nF



Connections to Other Cards



Title: front-panel

Version: Rev B

24 Jan 2012 17:57:12

Drawn By: AlexiosC

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