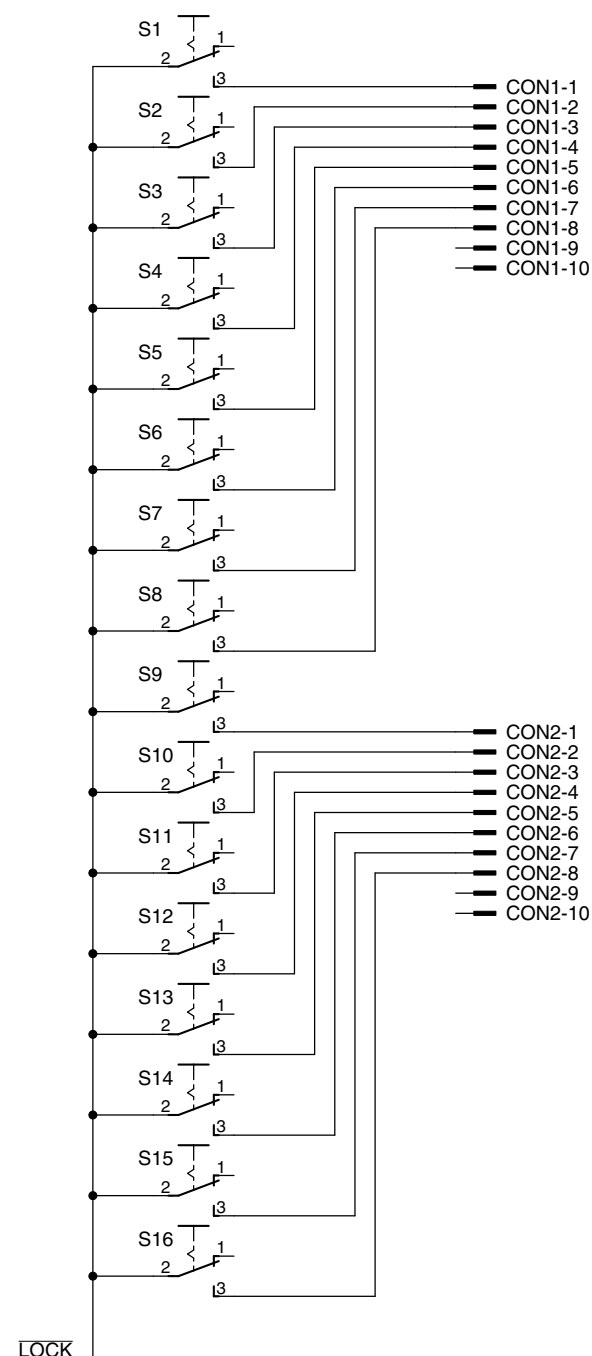


CFT CPU

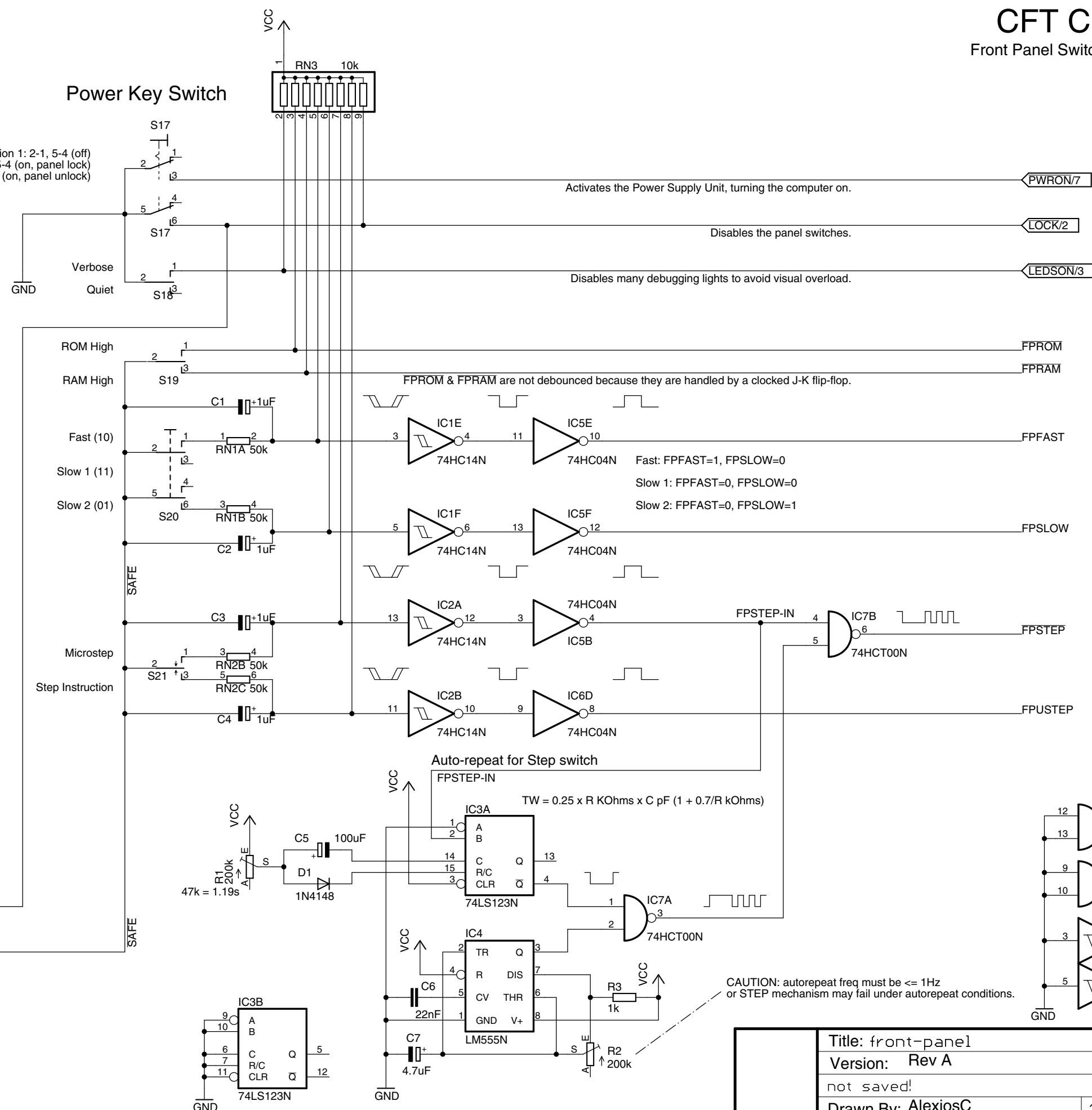
Front Panel Switches 1

Switch Register



Power Key Switch

Position 1: 2-1, 5-4 (off)
Position 2: 2-3, 5-4 (on, panel lock)
Position 3: 2-3, 5-6 (on, panel unlock)



Title: front-panel
Version: Rev A
not saved!
Drawn By: AlexiosC
1/7

CFT CPU

Front Panel Switches 2

5

4

3

2

1

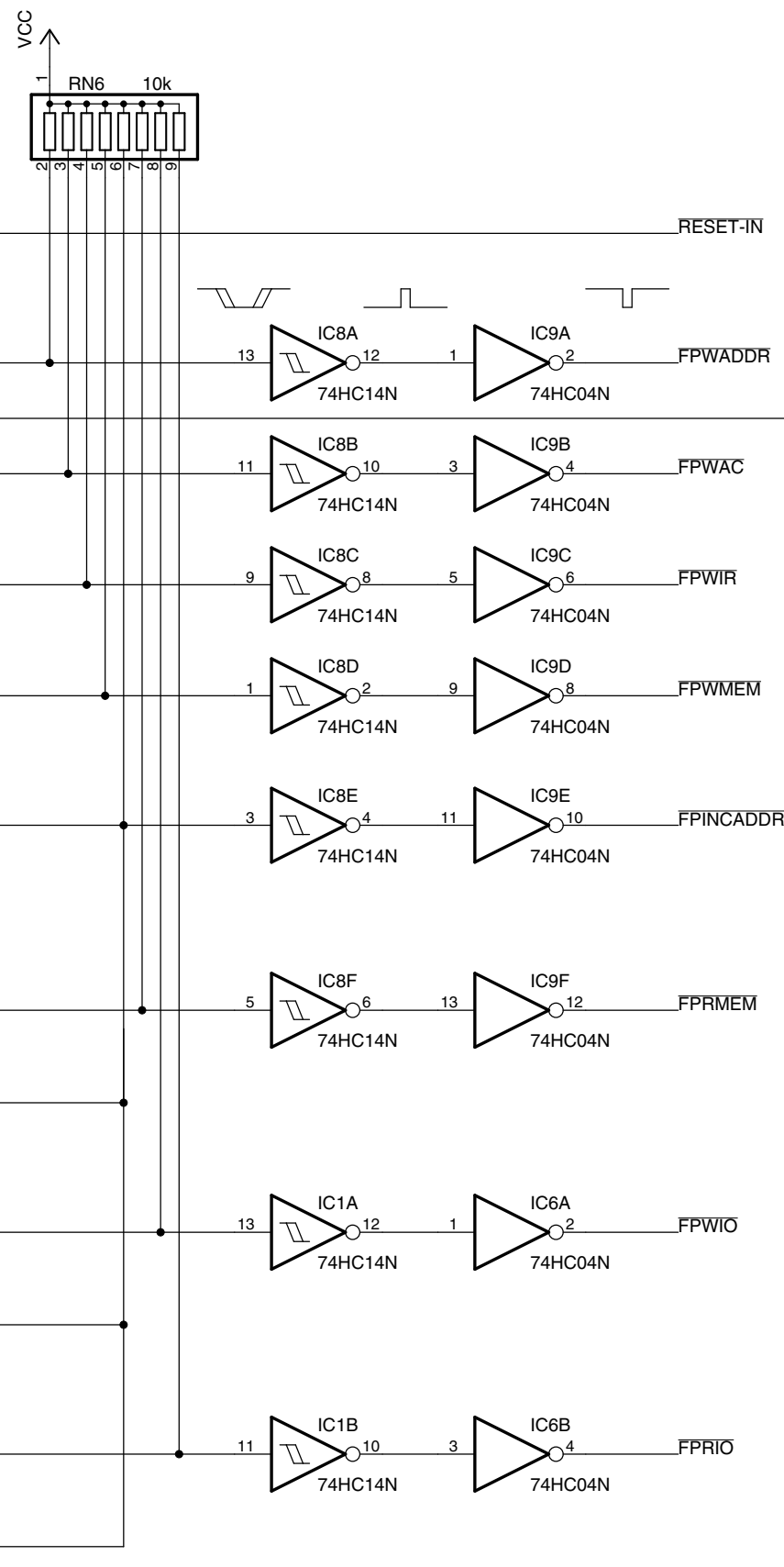
5

4

3

2

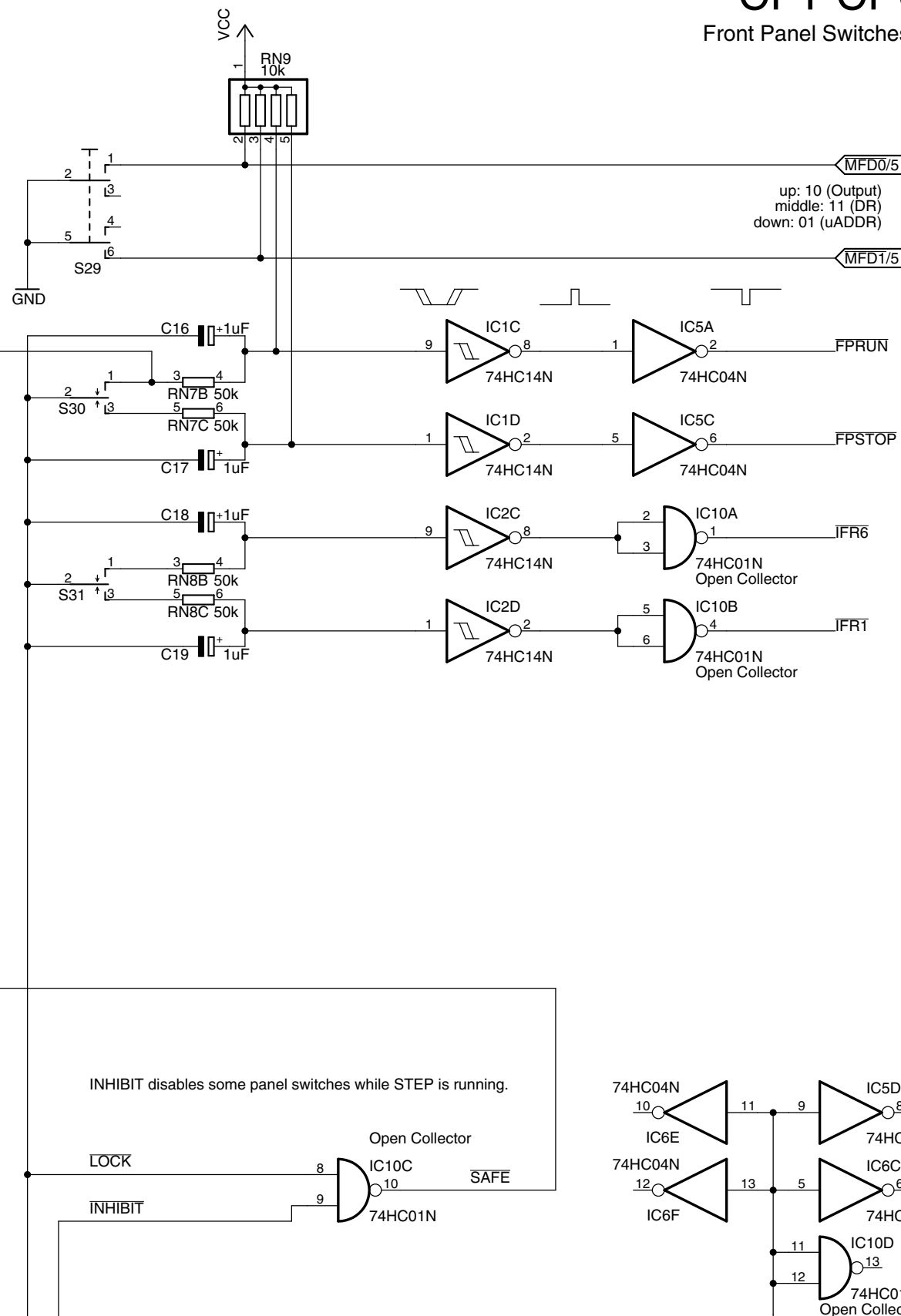
1



MFD: show Output

MFD: show DR

MFD: show uADDR



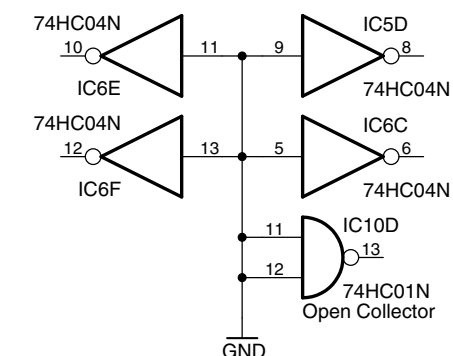
INHIBIT disables some panel switches while STEP is running.

LOCK

INHIBIT

Open Collector

SAFE



Title: front-panel

Version: Rev A

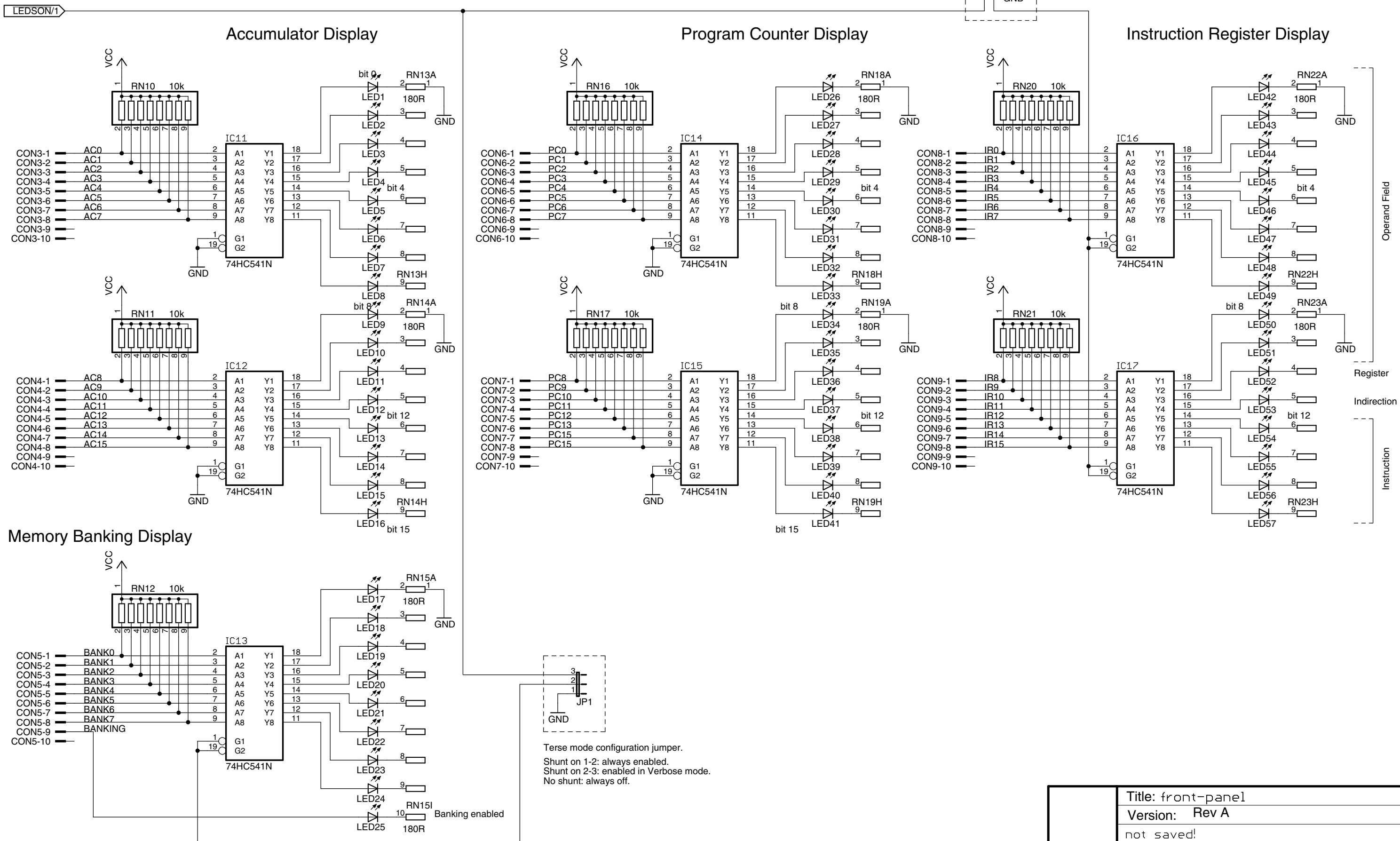
not saved!

Drawn By: AlexiosC

2/7

CFT CPU

Front Panel LEDs 1



Title: front-panel

Version: Rev A

not saved!

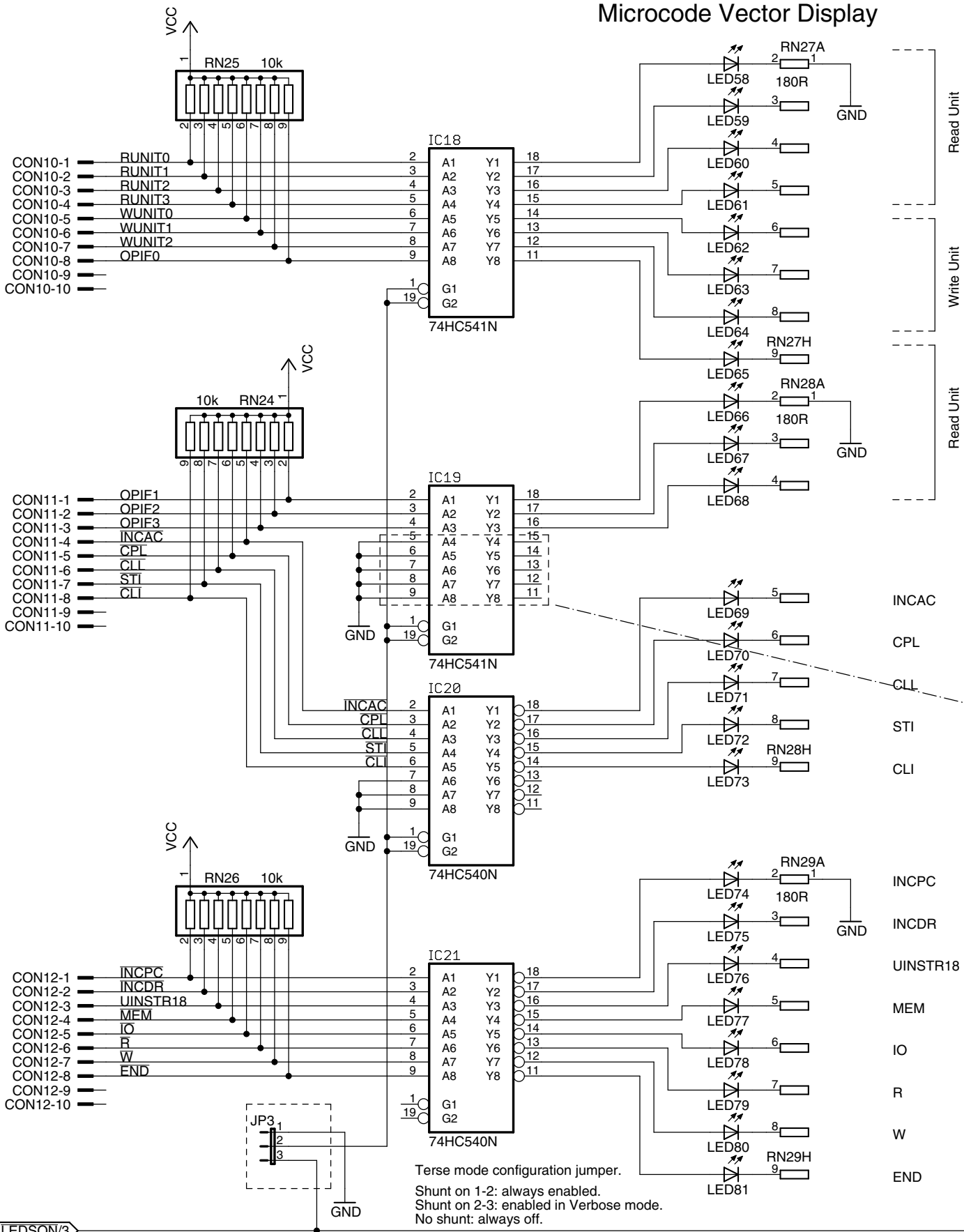
Drawn By: AlexiosC

3/7

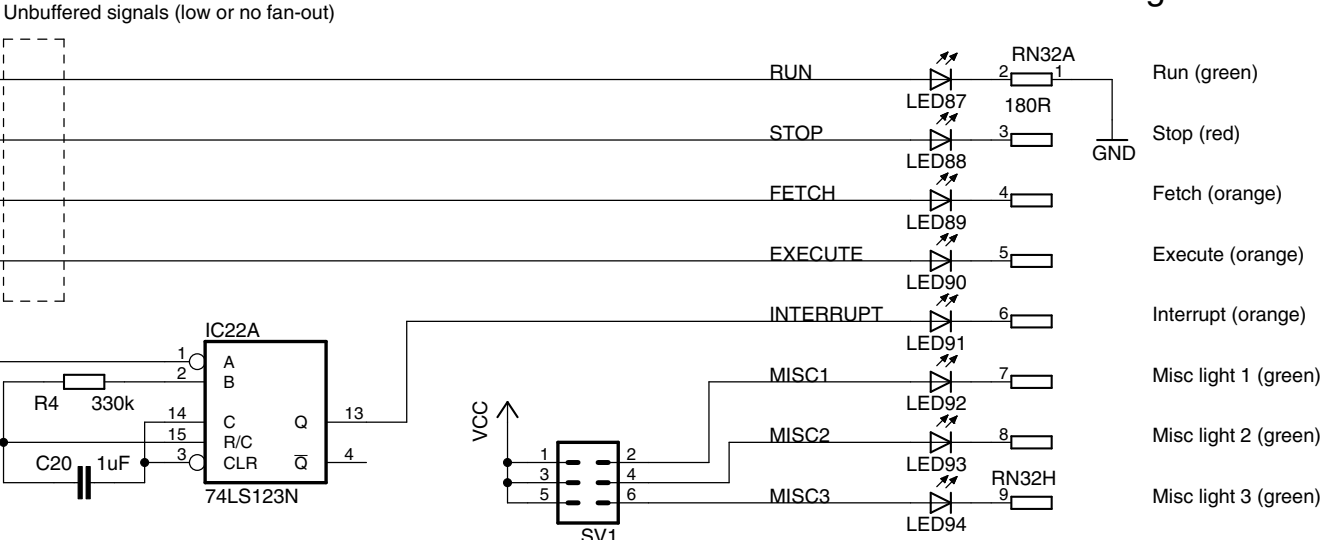
CFT CPU

Front Panel LEDs 2

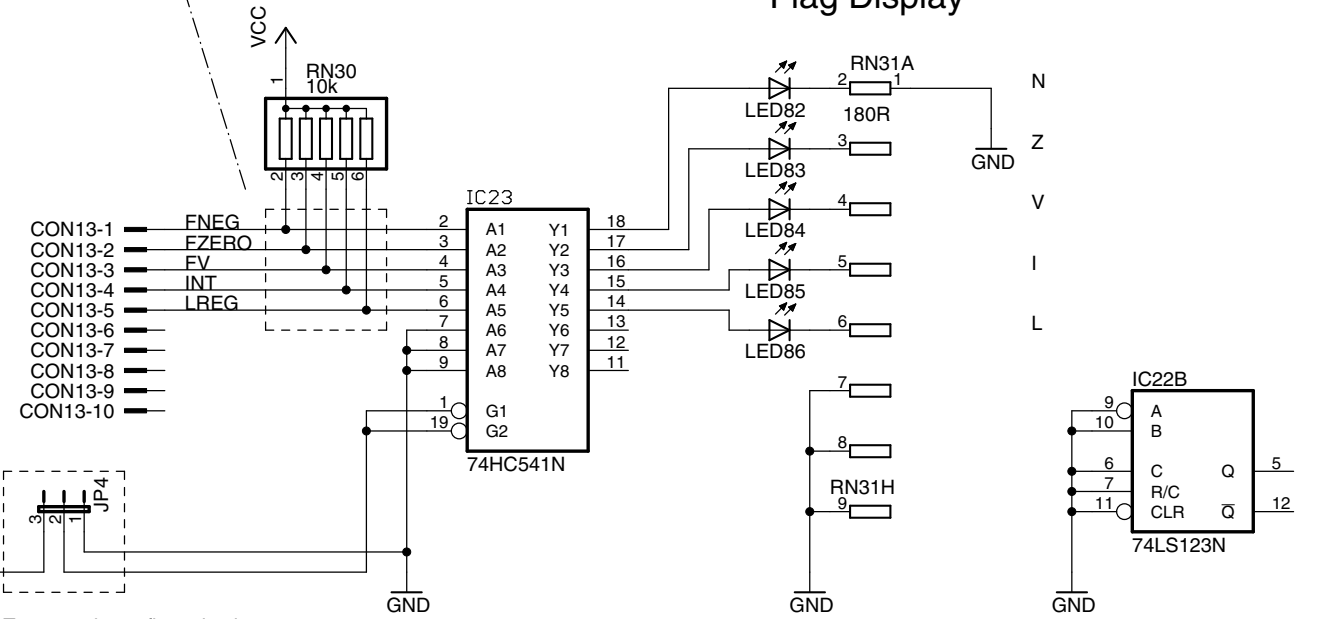
Microcode Vector Display



State & Miscellaneous lights



Flag Display



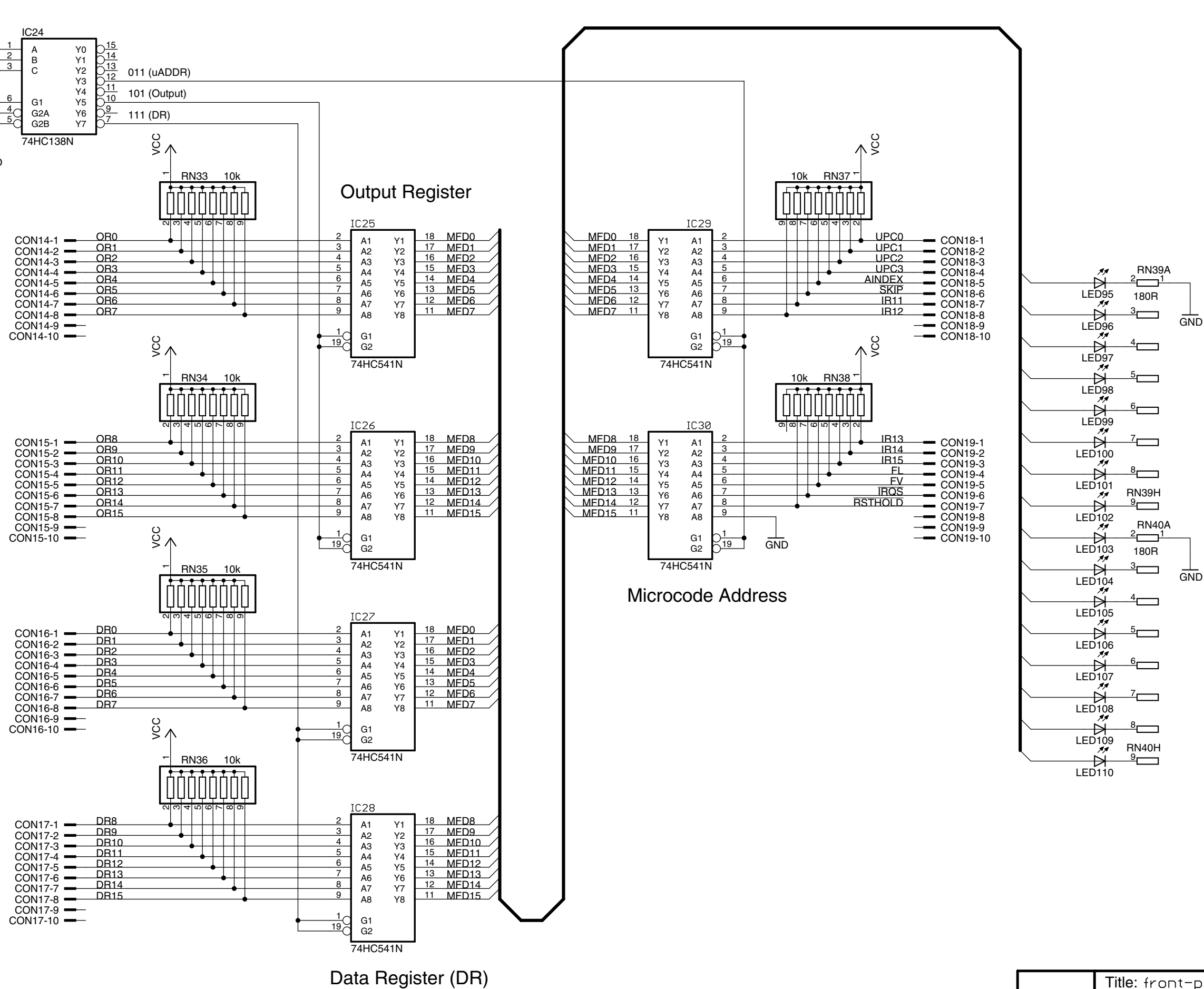
In wiring the flag display, the spare pins of the '541 buffering OPIF1-3 may be used. In that case, it would be beneficial to shuffle the RNs around too.

Terse mode configuration jumper.
Shunt on 1-2: always enabled.
Shunt on 2-3: enabled in Verbose mode.
No shunt: always off.

Title: front-panel	
Version: Rev A	
not saved!	
Drawn By: AlexiosC	4/7

CFT CPU

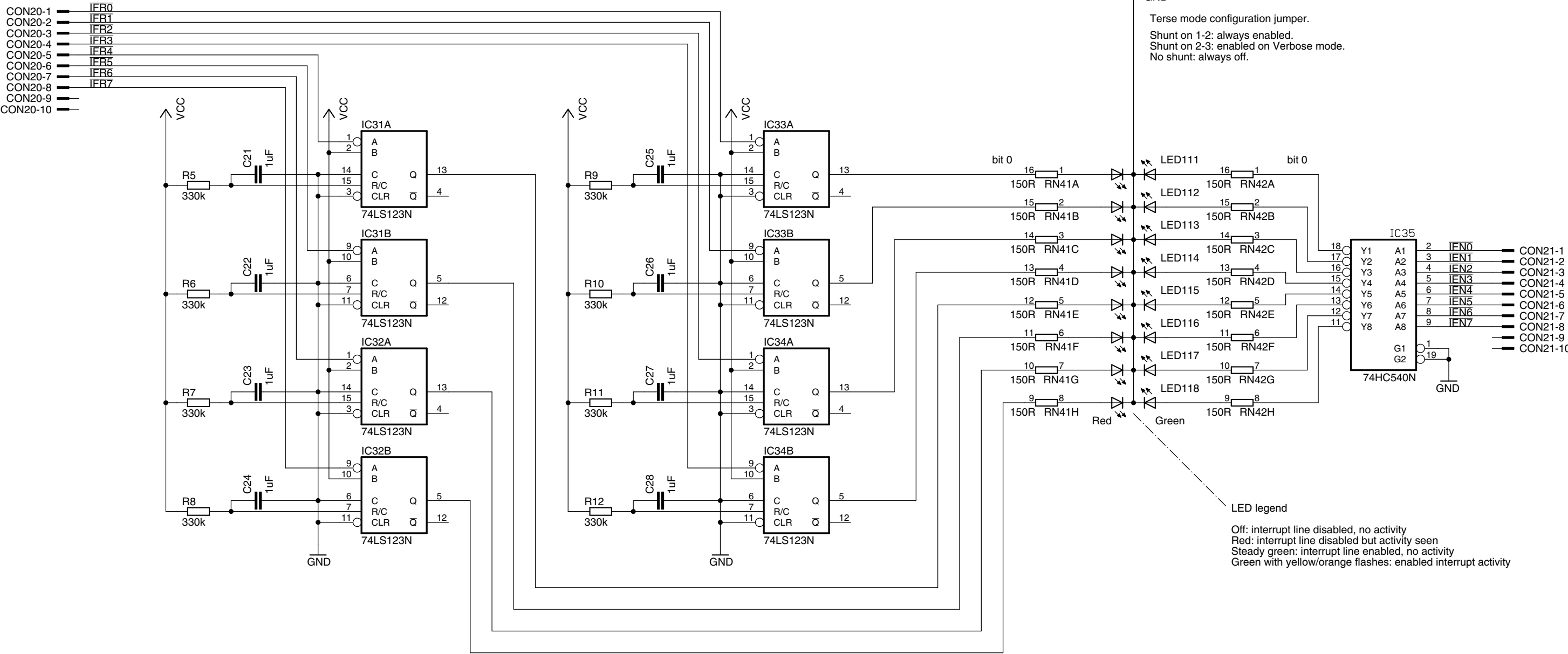
Multi-Function Display



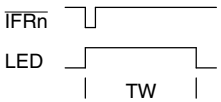
CFT CPU

Front Panel LEDs, Interrupt display

Interrupt Status Display



IFR \bar{n} pulses are not normally visible to the naked eye.
'123 Dual Monostable Vibrators produce pulses for IRQ signals.



$$TW \text{ (ns)} = 0.25 \times R \text{ kOhms} \times C \text{ pF} (1 + 0.7/R \text{ kOhms})$$

R=330k, C=1uF: ~0.08s delay

Title: front-panel

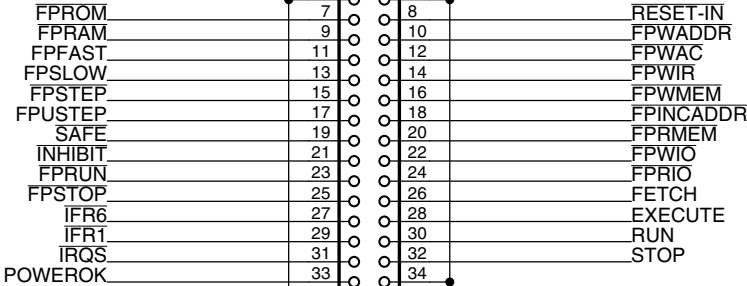
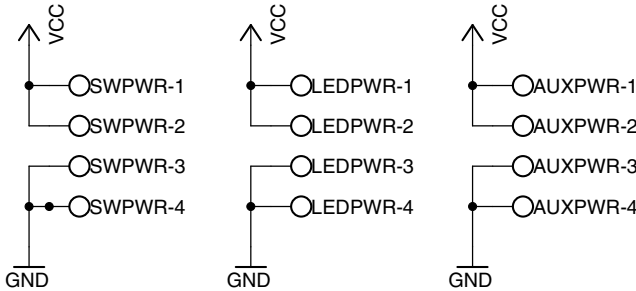
Version: Rev A

not saved!

Drawn By: AlexiosC

6/7

Front Panel Connectors



Connector to Front Panel Controller Card