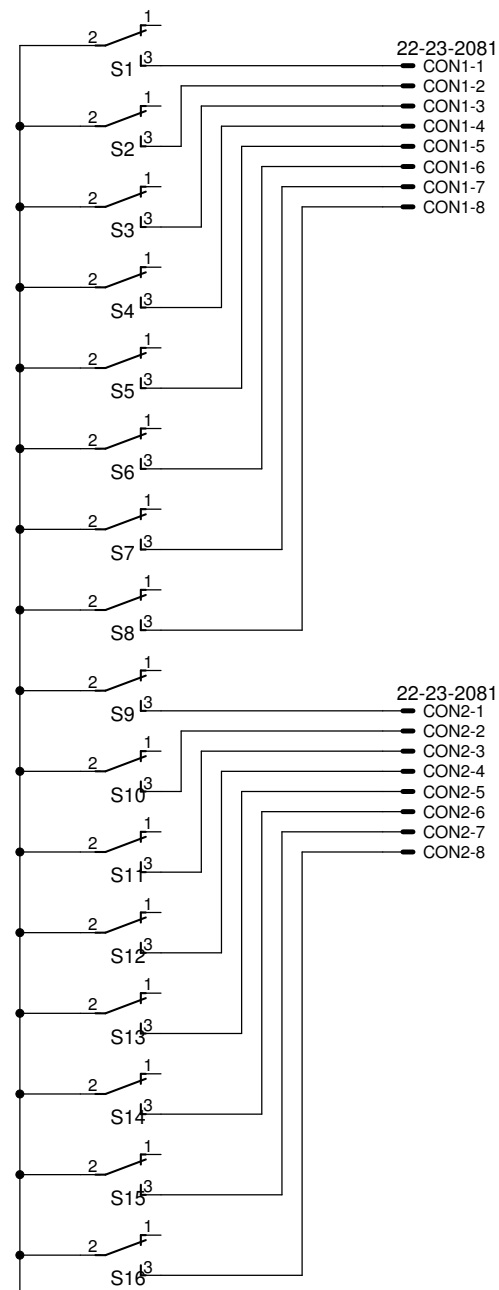


# CFT CPU

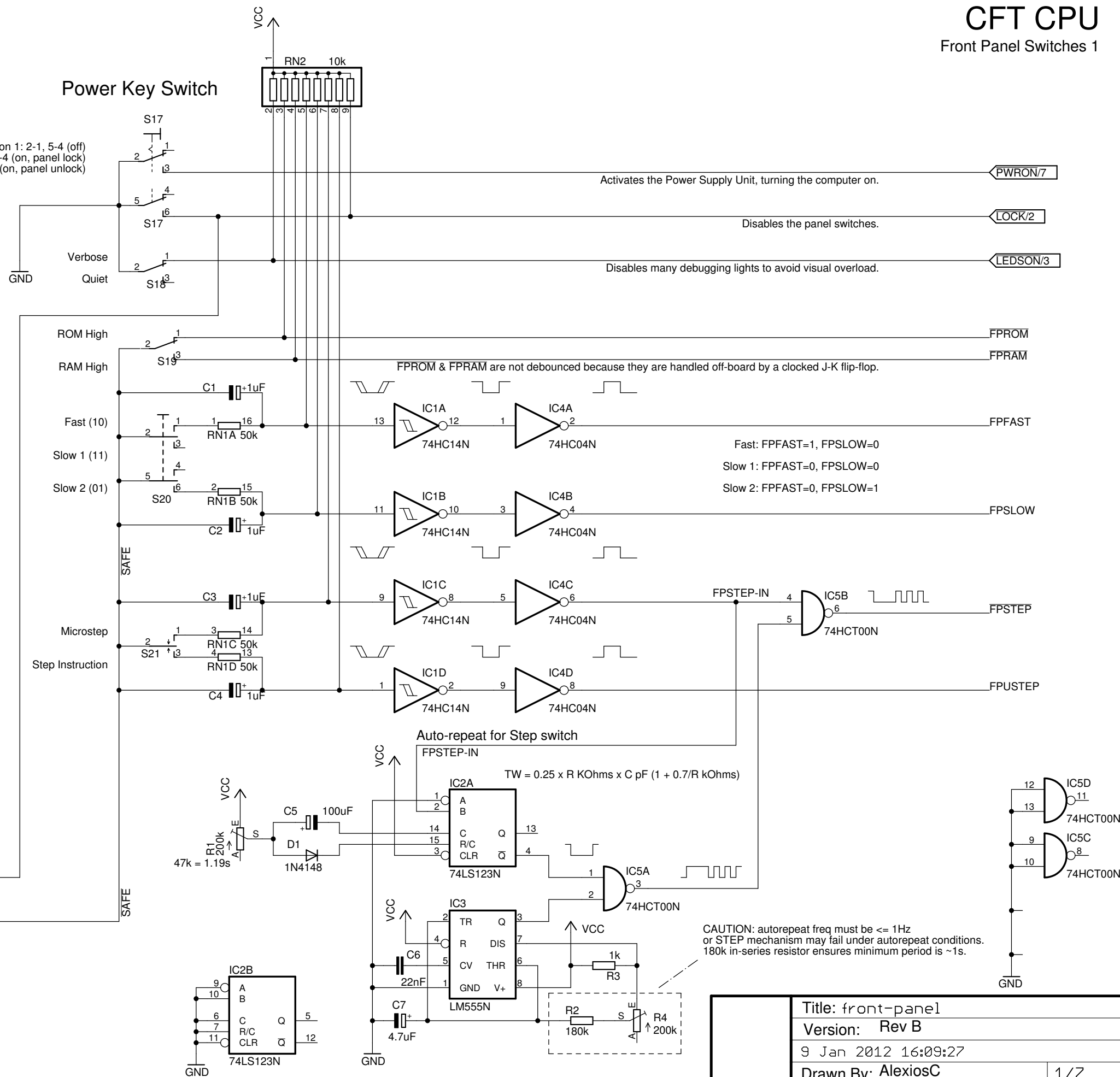
## Front Panel Switches 1

### Switch Register



### Power Key Switch

Position 1: 2-1, 5-4 (off)  
Position 2: 2-3, 5-4 (on, panel lock)  
Position 3: 2-3, 5-6 (on, panel unlock)



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# CFT CPU

## Front Panel Switches 2

5

4

3

2

1

5

4

3

2

1

VCC

-

RN4

10k

-

RN3A

50k

C8

+1uF

C9

+1uF

RN3B

50k

RN3C

50k

C10

+1uF

C11

+1uF

RN3D

50k

C12

+1uF

C13

+1uF

RN3F

50k

C14

+1uF

RN3G

50k

C15

+1uF

RN3H

50k

S22

S23

S24

S25

S26

S27

S28

S29

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S323

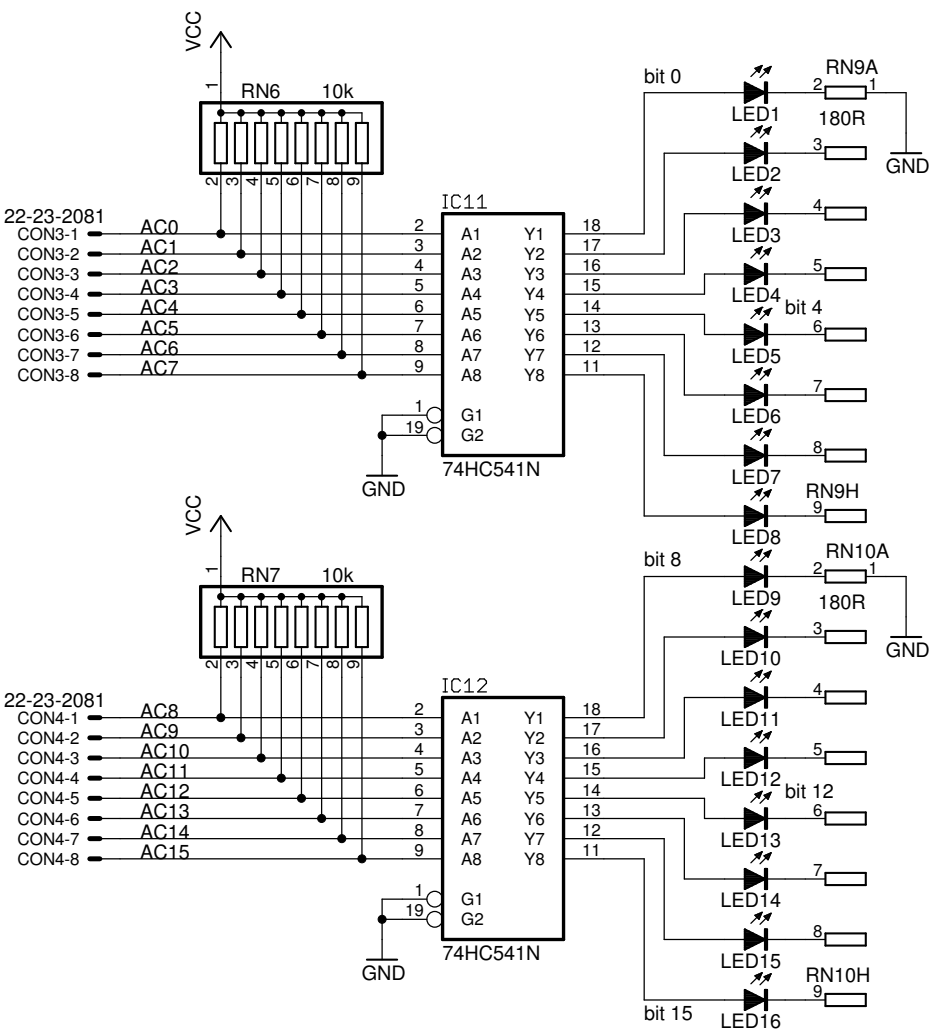
S324

# CFT CPU

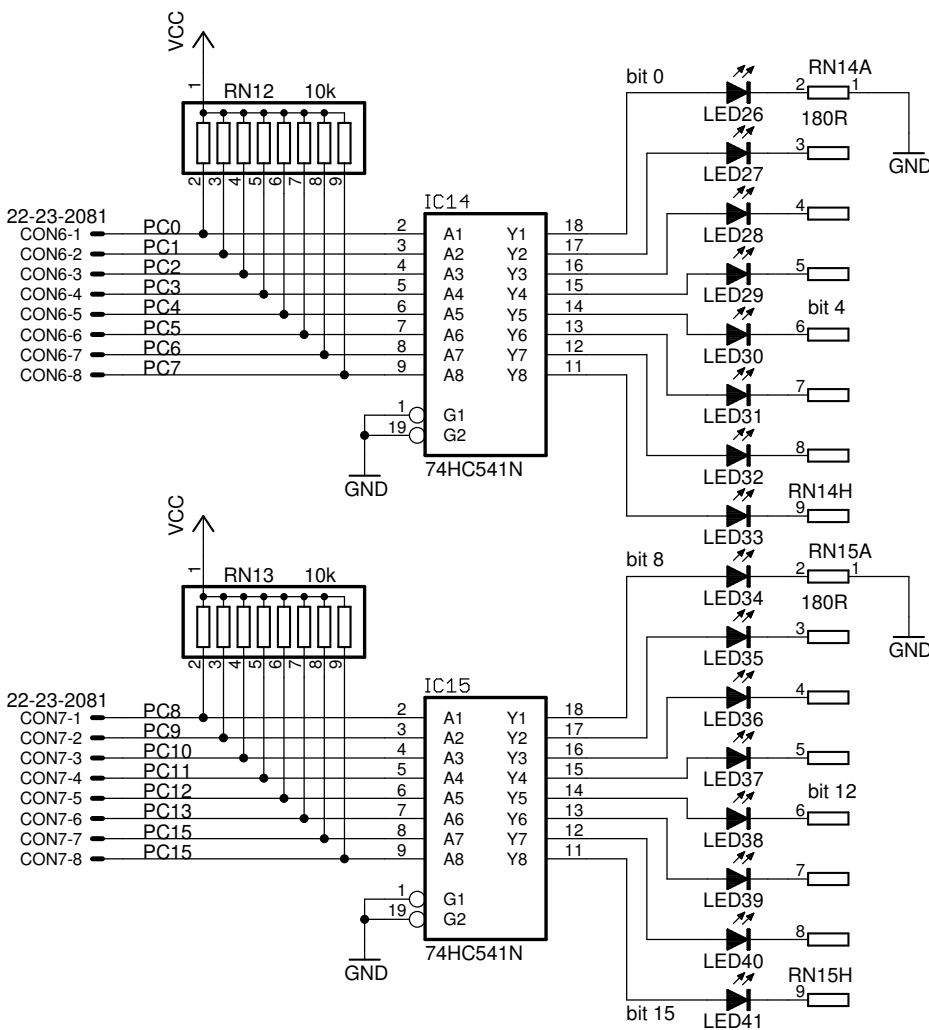
Front Panel LEDs 1

Terse mode configuration jumper.  
Shunt on 1-2: always enabled.  
Shunt on 2-3: enabled in Verbose mode.  
No shunt: always off.

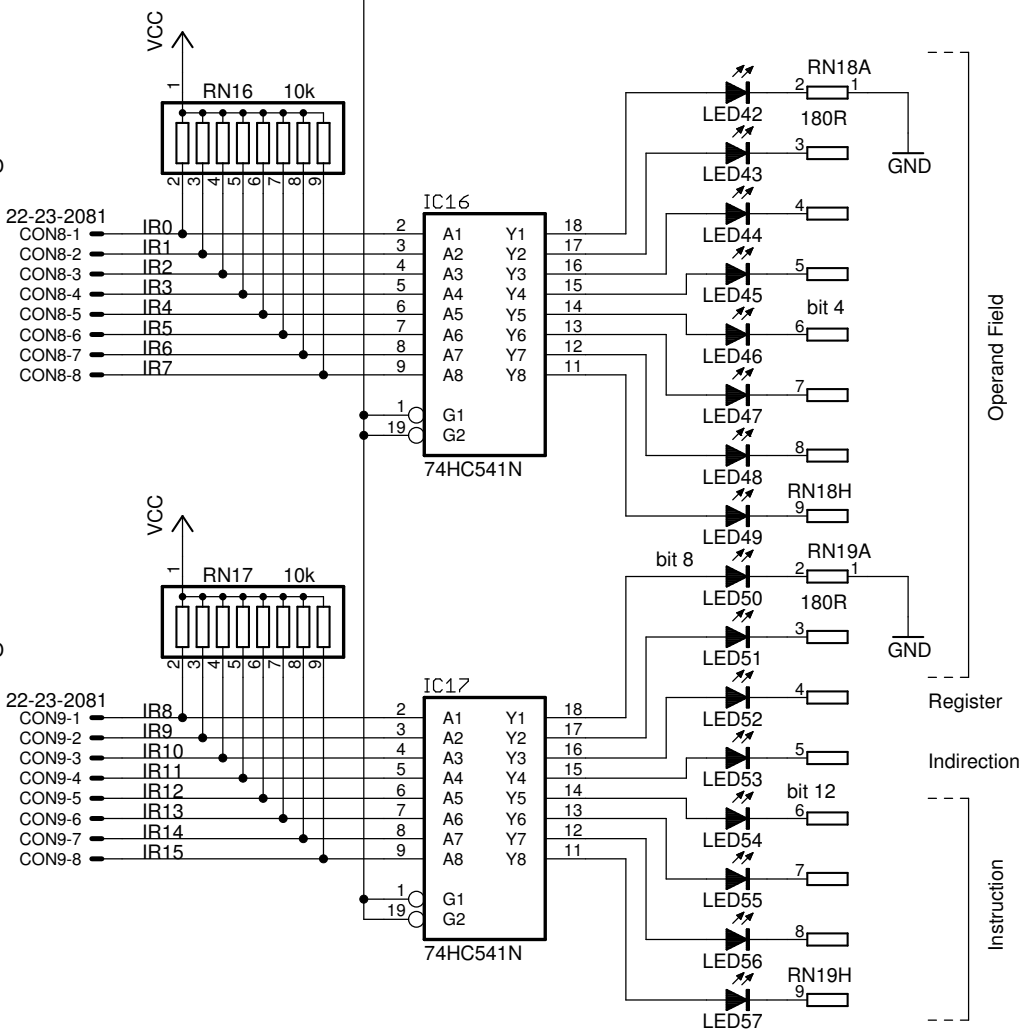
## Accumulator Display



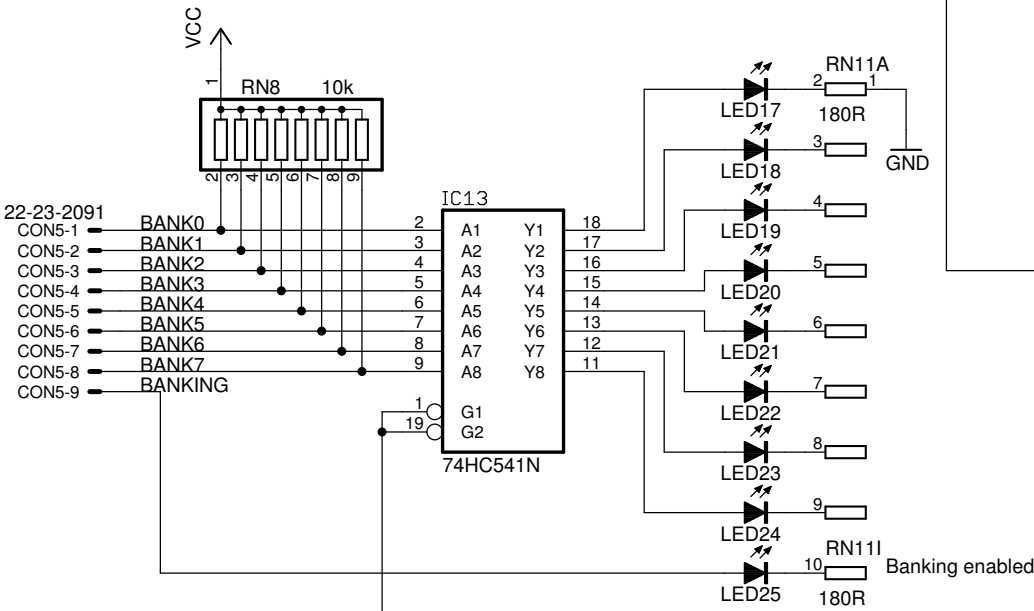
## Program Counter Display



## Instruction Register Display



## Memory Banking Display



Terse mode configuration jumper.  
Shunt on 1-2: always enabled.  
Shunt on 2-3: enabled in Verbose mode.  
No shunt: always off.

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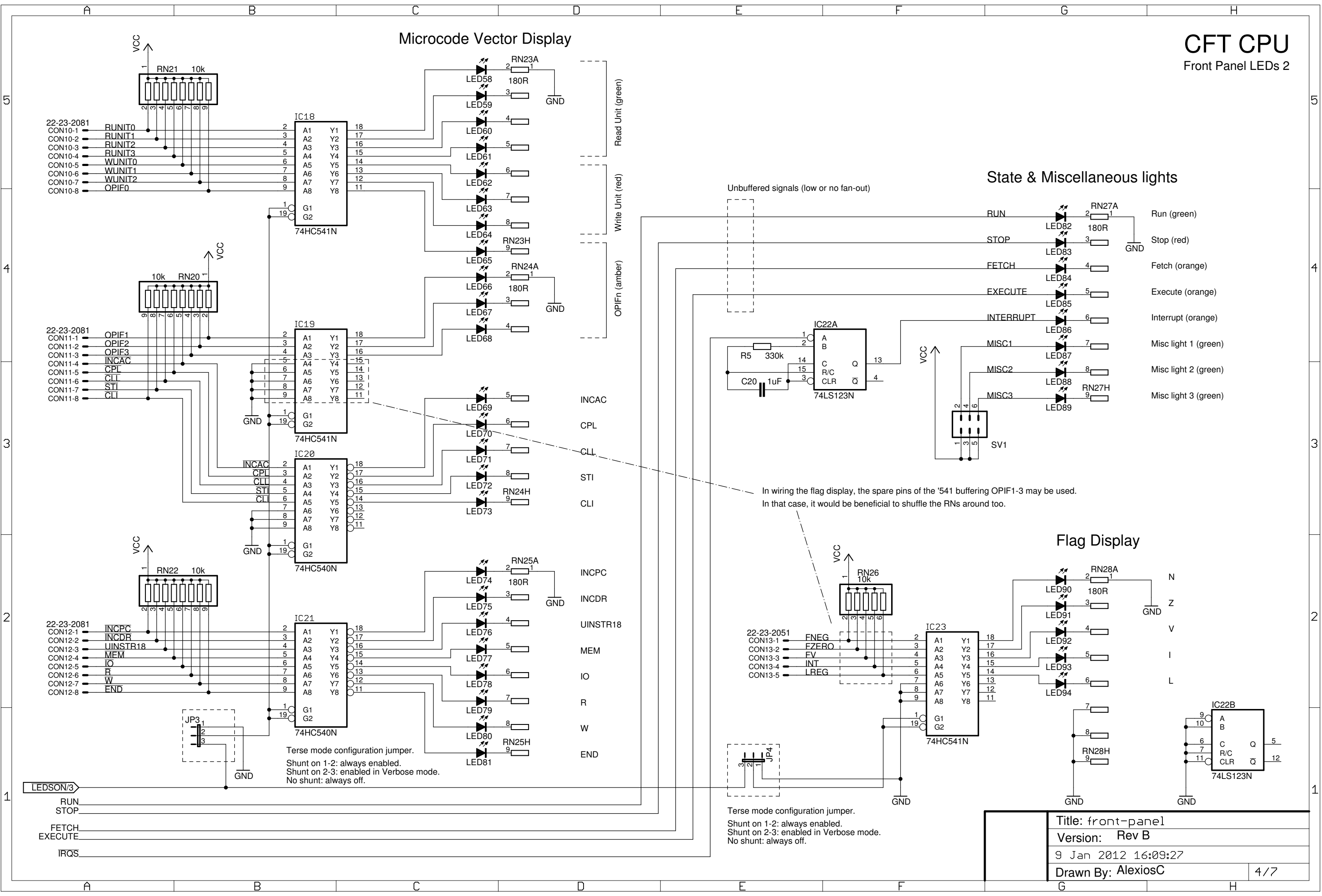
# CFT CPU

Front Panel LEDs 2

## Microcode Vector Display

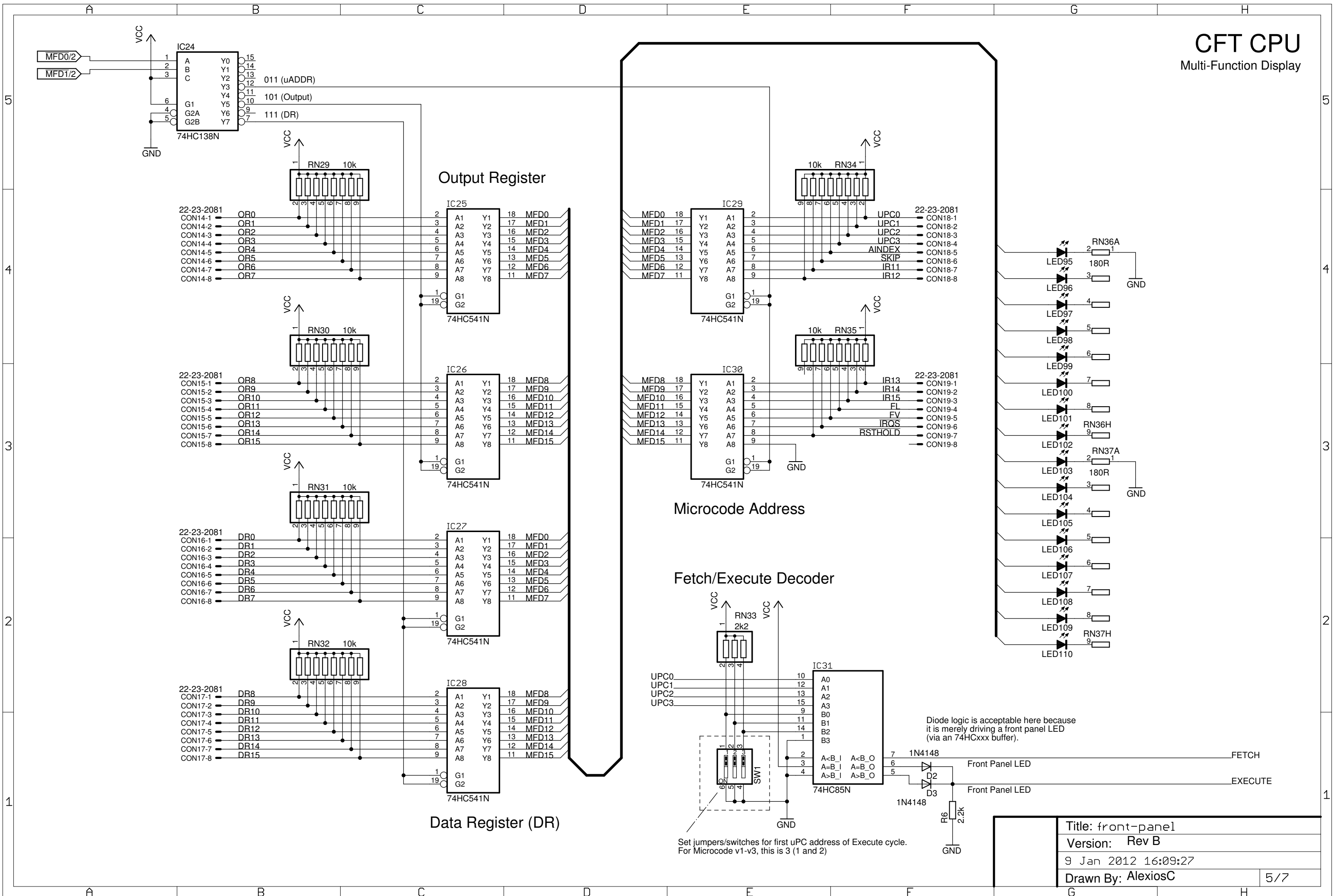
## State & Miscellaneous lights

## Flag Display



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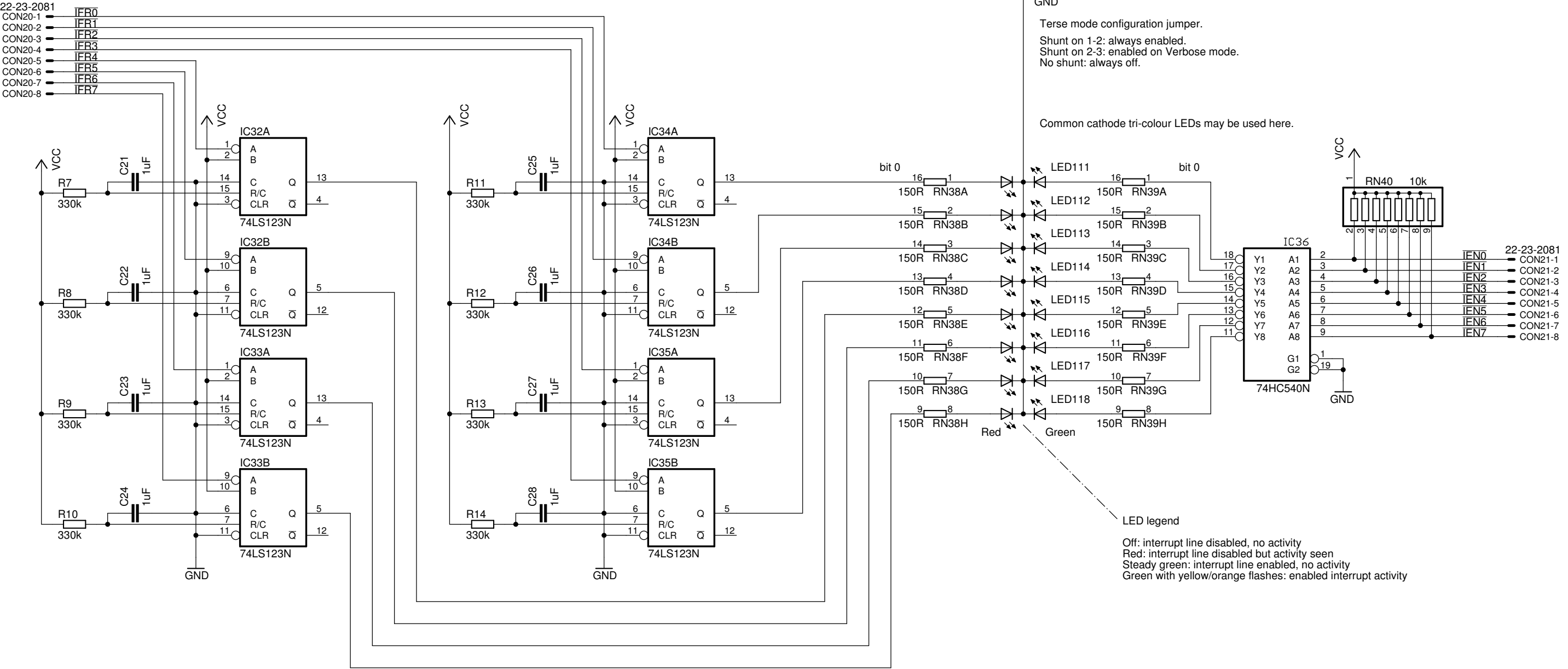
### Multi-Function Display



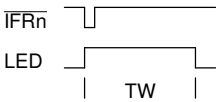
# CFT CPU

Front Panel LEDs, Interrupt display

## Interrupt Status Display



IFR<sub>n</sub> pulses are not normally visible to the naked eye.  
'123 Dual Monostable Vibrators produce pulses for IRQ signals.



$$TW (ns) = 0.25 \times R \text{ KOhms} \times C \text{ pF} (1 + 0.7/R \text{ KOhms})$$

R=330k, C=1uF: ~0.08s delay

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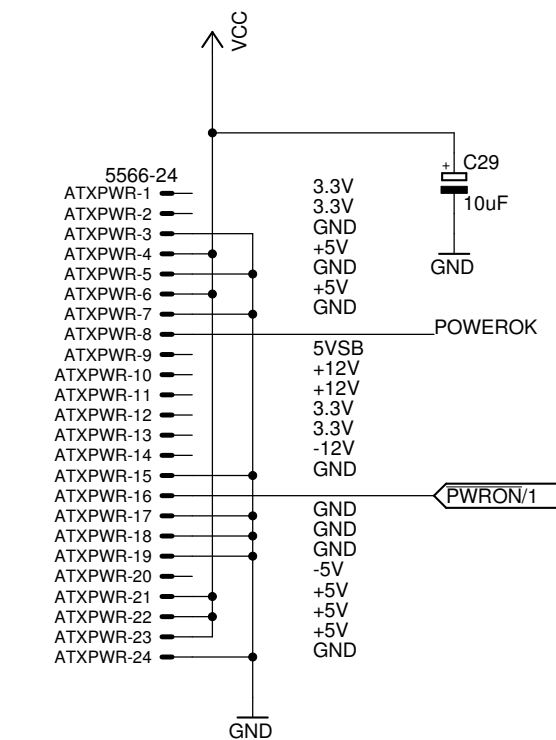
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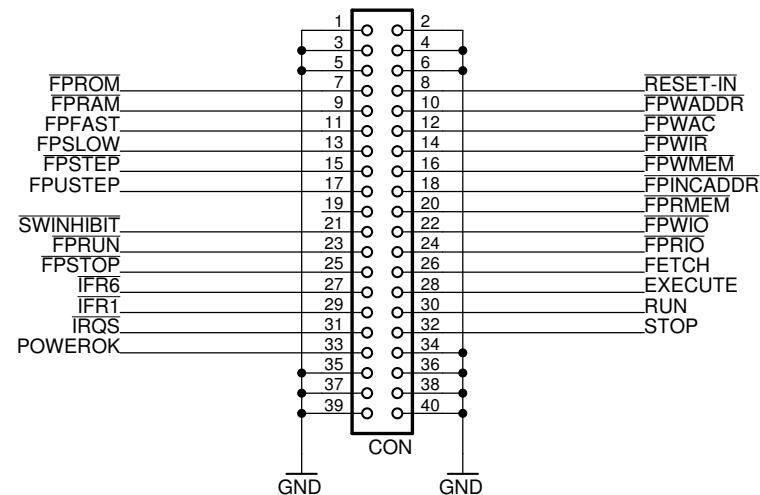
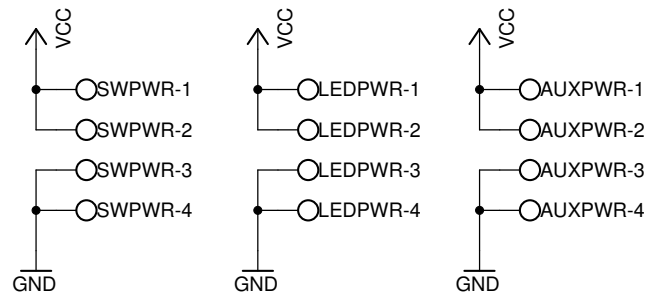
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# CFT CPU

Front Panel Connectors



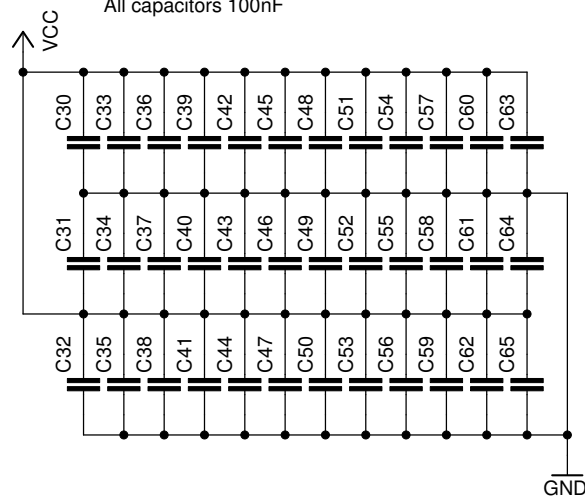
ATX 24-pin power socket (Molex Mini Fit Jr)



Connector to Front Panel Controller Card.

## Decoupling Capacitors

All capacitors 100nF



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