

Memory Controller

ENABLED

AEXT7 ROM

AEXT6

AEXT5

AEXT4

AEXT3

AXT2

AEXT1

AEXT0

State

RUN

STOP

FETCH

EXEC

INT

Flags

N

Z

V

I

L

Interrupt Controller

INT7

INT6

INT5

INT4

INT3

INT2

INT1

INT0

Program Counter

15

14

13

12

11

10

9

8

7

6

5

4

3

2

1

0

Accumulator

15

14

13

12

11

10

9

8

7

6

5

4

3

2

1

0

Output Register / Data Register / μ -Address Vector

RST

INT

V

L

OP3

OP2

OP1

OP0

I

SKIP

AIDX

μ ADDR3

μ ADDR2

μ ADDR1

μ ADDR0

Memory

MEM

I/O

R

W

OP3

OP2

OP1

OP0

I

R

Instruction Register

9

8

7

6

5

4

3

2

1

0

Operand Field

1

0

μ -Instruction

END

INCDR

INCA

CLI

STI

CLL

CPL

INCPC

OPIF3

OPIF2

OPIF1

OPIF0

WUNIT2

WUNIT1

WUNIT0

RUNIT3

RUNIT2

RUNIT1

RUNIT0

15

14

13

12

11

10

9

8

7

6

5

4

3

2

1

0

RESET

RUN

μ STEP

LOAD IR

DEPOSIT

EXAMINE

DEPOSIT

EXAMINE

RAM BNK

IFR1

FAST

VERBOSE

OUTPUT REG

START

STOP

STEP

LOAD ADDR

LOAD AC

DEP NEXT

EXAM NEXT

DEP NEXT

EXAM NEXT

ROM BNK

IFR6

SLOW

TEST

TERSE

μ ADDR VEC

OFF

ON

PANEL

C.F.T minicomputer