

Micro-Instruction Control Vector

ENDW R MEM I/O+DR+ACCLISTICPL+PCIF3IF2IF1IF0WU2WU1WU0RU3RU2RU1RU0

Interrupt Requests

76543210

Memory Bank Unit

AEXT7 ROMAEXT6AEXT5AEXT4AEXT3AEXT2AEXT1AEXT0

Program Counter

1514131211109876543210

Enabled Interrupts

76543210

Flags

MBENNZVIL

Accumulator

1514131211109876543210

76543210

State

RUNSTOPWSFETCHEXECINT

Output Register/Data Register/Micro-address vector

RSTINTVLIOP3OP2OP1OP0ISKIP AIDXμA3μA2μA1μA0

76543210



POWER



PANEL LOCK

Instruction Register

OP3OP2OP1OP0ILR9876543210

Microcode Bank Ext

μCB3μCB2μCB1μCB0

C.F.T

RESET

RUN

μSTEP

FAST

LTS ON

OR

SLOW

DR

START

STOP

STEP

CREEP

OFF

μADDR VEC

Switch Register

1514131211109876543210

800040002000100080040020010040020010084020108402010STORELOADIOTTRAP

LIAISZOP2OP1XORORANDADDJSRJMPOUTINSTORELOADIOTTRAP

SR→IR

MEM W

MEM R

I/O W

I/O R

RAM BNK

IFR1

→PC

→AC

W NEXT

R NEXT

W NEXT

R NEXT

ROM BNK

IFR6