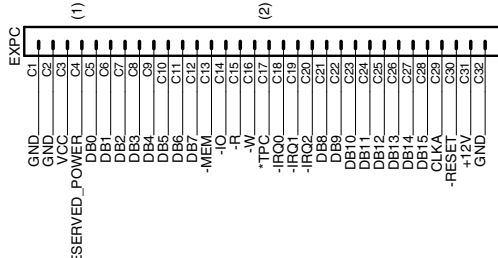
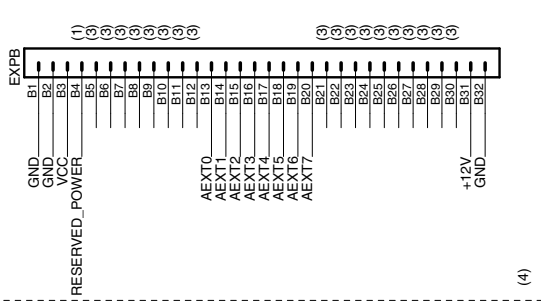
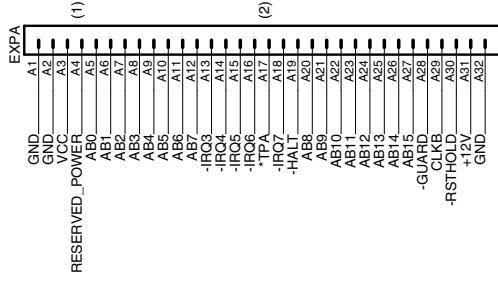
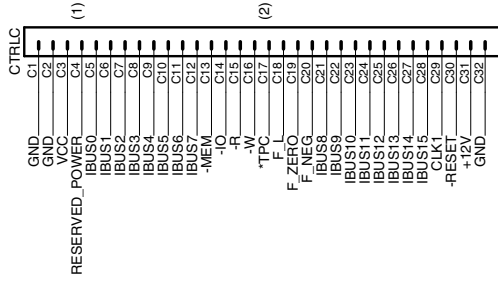
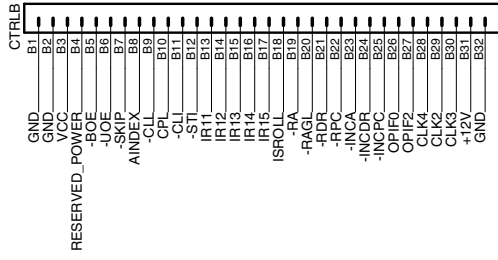
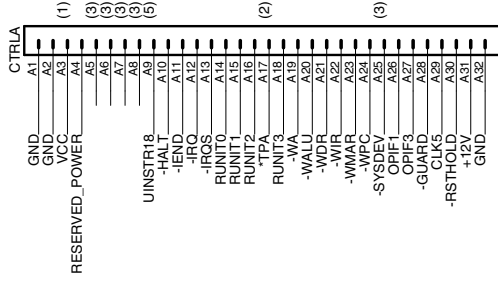


CFT CPU

Signal allocation to the Control and Expansion buses.

Internal CPU bus (Control bus)



Notes

- (1) This pin is connected to a bus bar for power distribution, but the CFT does not (yet) require it. It's likely to be connected to another voltage level like +3.3V for easier interfacing. Reserved for now.
- (2) Pins *TPA and *TPC are not bussed. They are locally connected to each card's corresponding test pins (A17 & C17) to serve as test points.
- (3) Reserved for future expansion
- (4) Cheaper, 64-pin A+C row DIN41662 Type C plugs may be used for most expansion cards.
- (5) Btt18 of the microinstruction field. Currently unused, reserved for future expansion.

This sheet shows the wiring and default parts of most CPU boards. Since these 10x16cm boards only have a single Eurocard connector, A board is either a CPU component or a computer component and must be fitted with ONE of the two connectors. Delete the other one.

Title: clock-eurocard	
Version: Rev E	
9 Nov 2011 02:08:56	
Drawn By: AlexiosC	2/2