

Micro-Instruction Control Vector



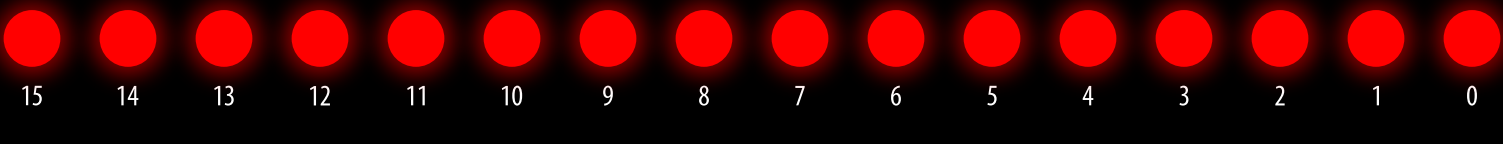
Interrupt Requests



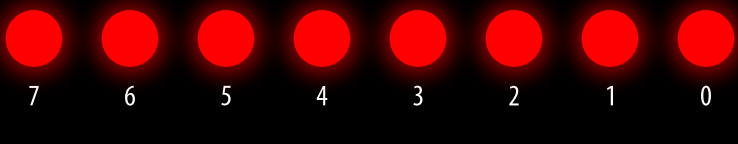
Memory Bank Unit



Program Counter



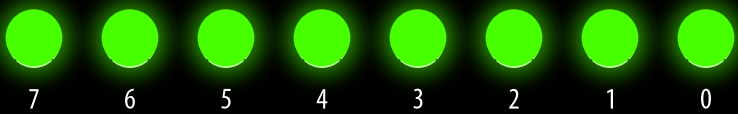
Enabled Interrupts



Flags



Accumulator



State



Output Register/Data Register/Micro-address vector



POWER



PANEL LOCK

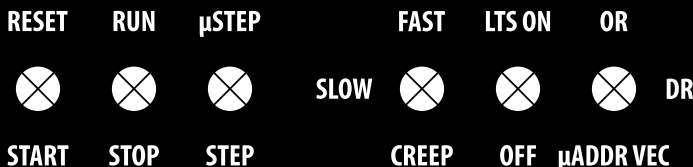
Instruction Register



Microcode Bank Ext



C.F.T



Switch Register

