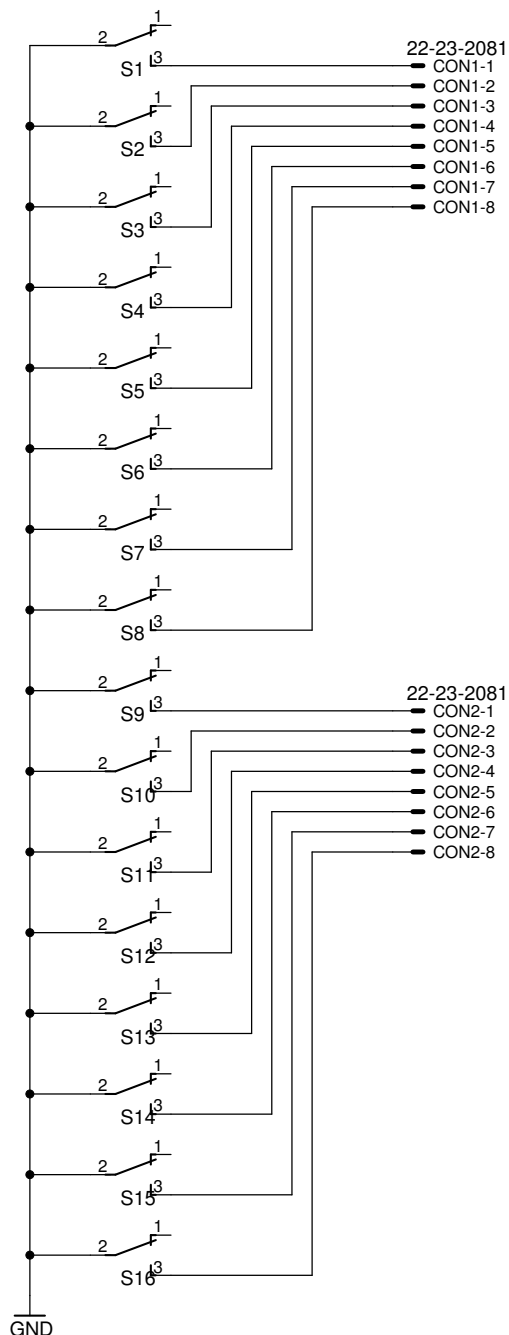


CFT CPU

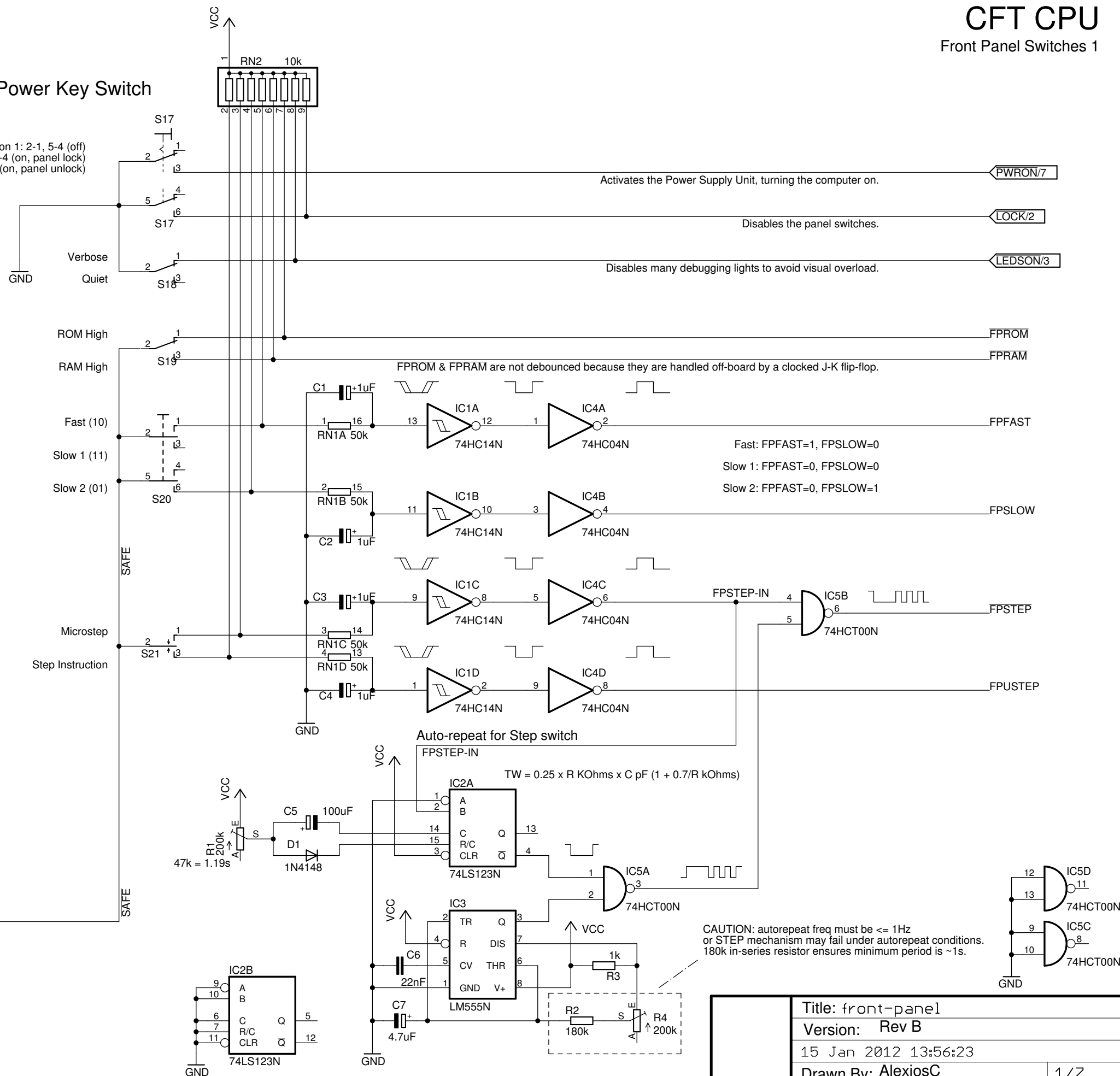
Front Panel Switches 1

Switch Register



Power Key Switch

Position 1: 2-1, 5-4 (off)
Position 2: 2-3, 5-4 (on, panel lock)
Position 3: 2-3, 5-6 (on, panel unlock)



Title: front-panel

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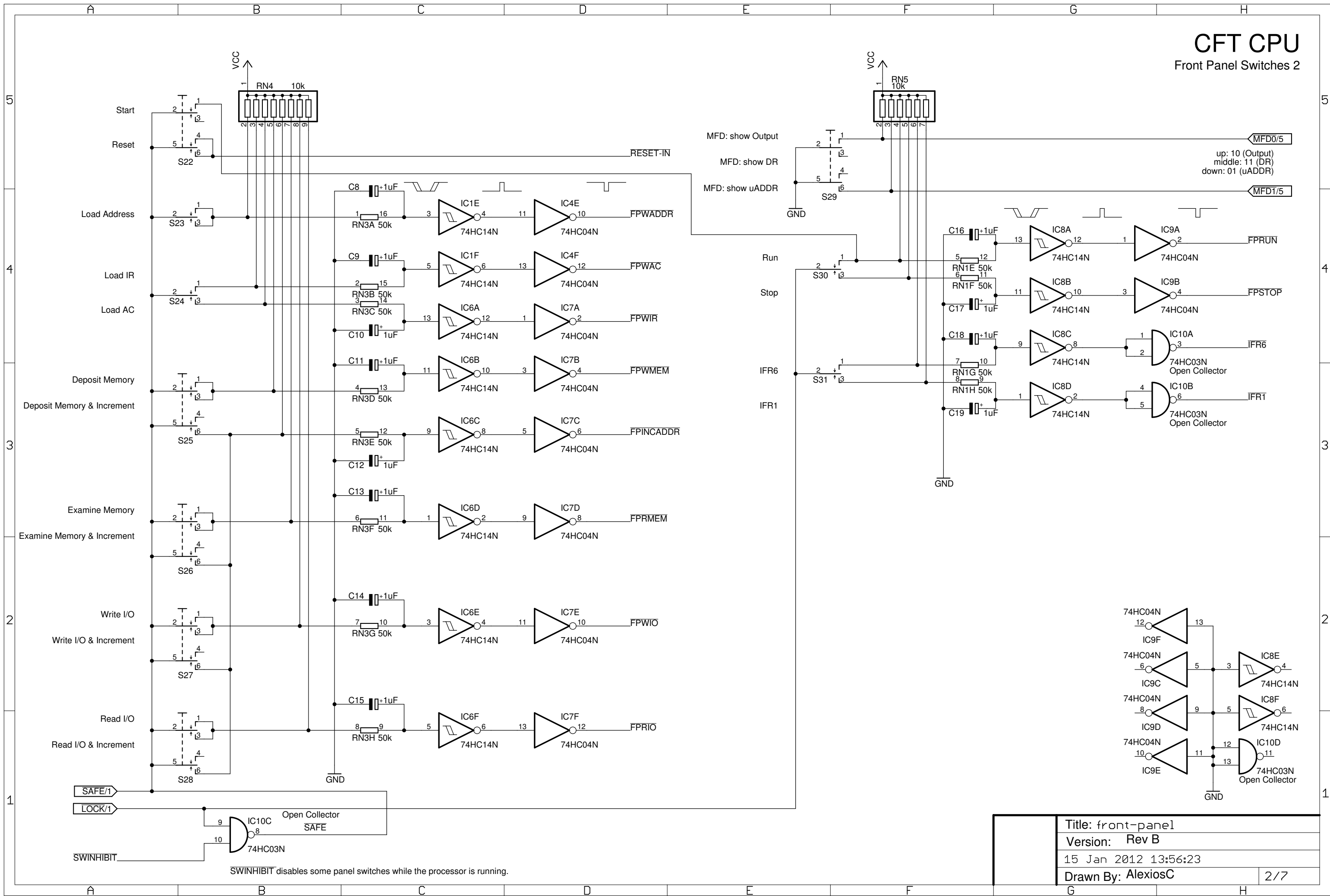
1/7

CFT CPU

Front Panel Switches 2

5
4
3
2
1

5
4
3
2
1

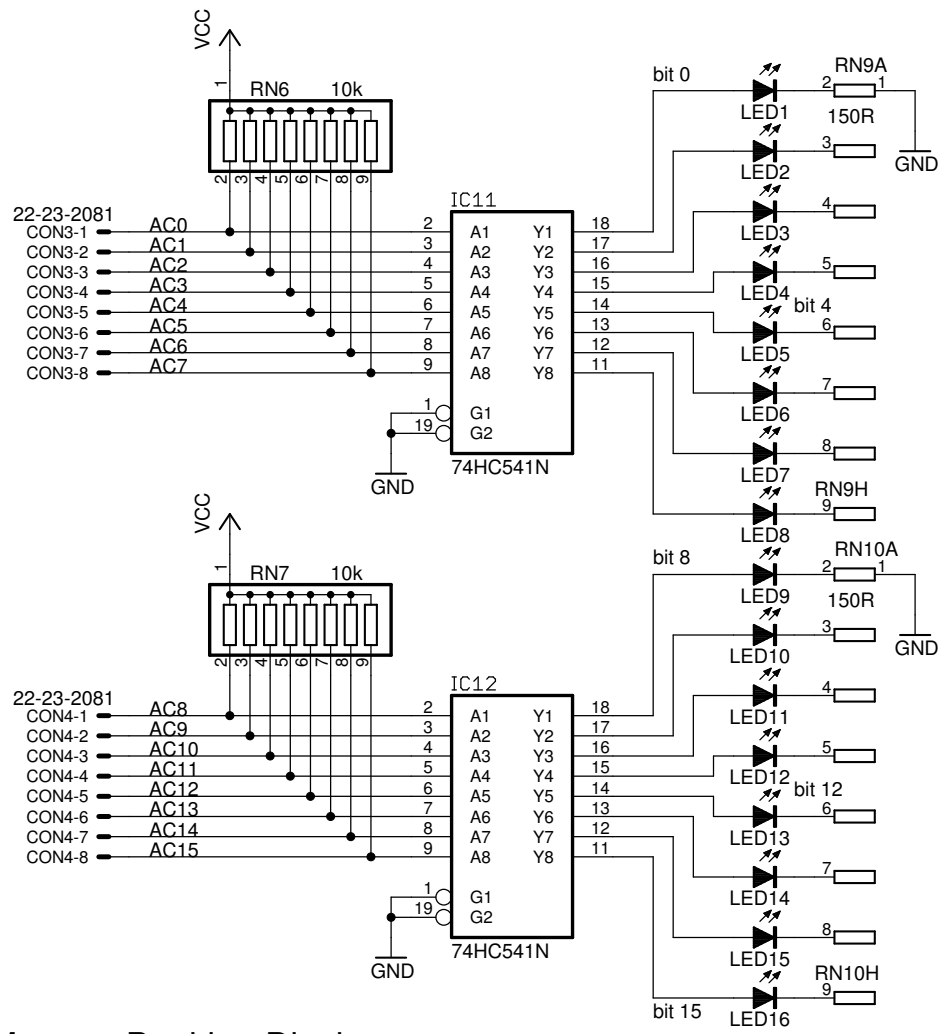


CFT CPU

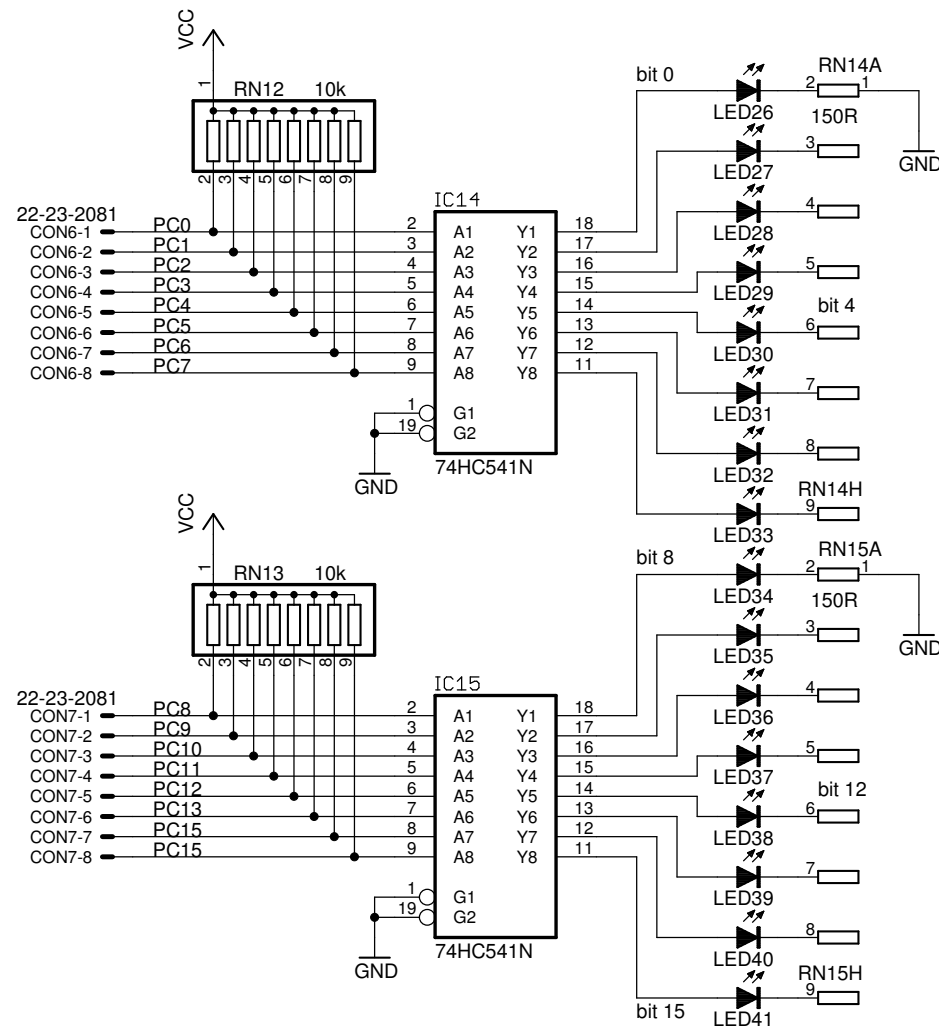
Front Panel LEDs 1

Terse mode configuration jumper.
Shunt on 1-2: always enabled.
Shunt on 2-3: enabled in Verbose mode.
No shunt: always off.

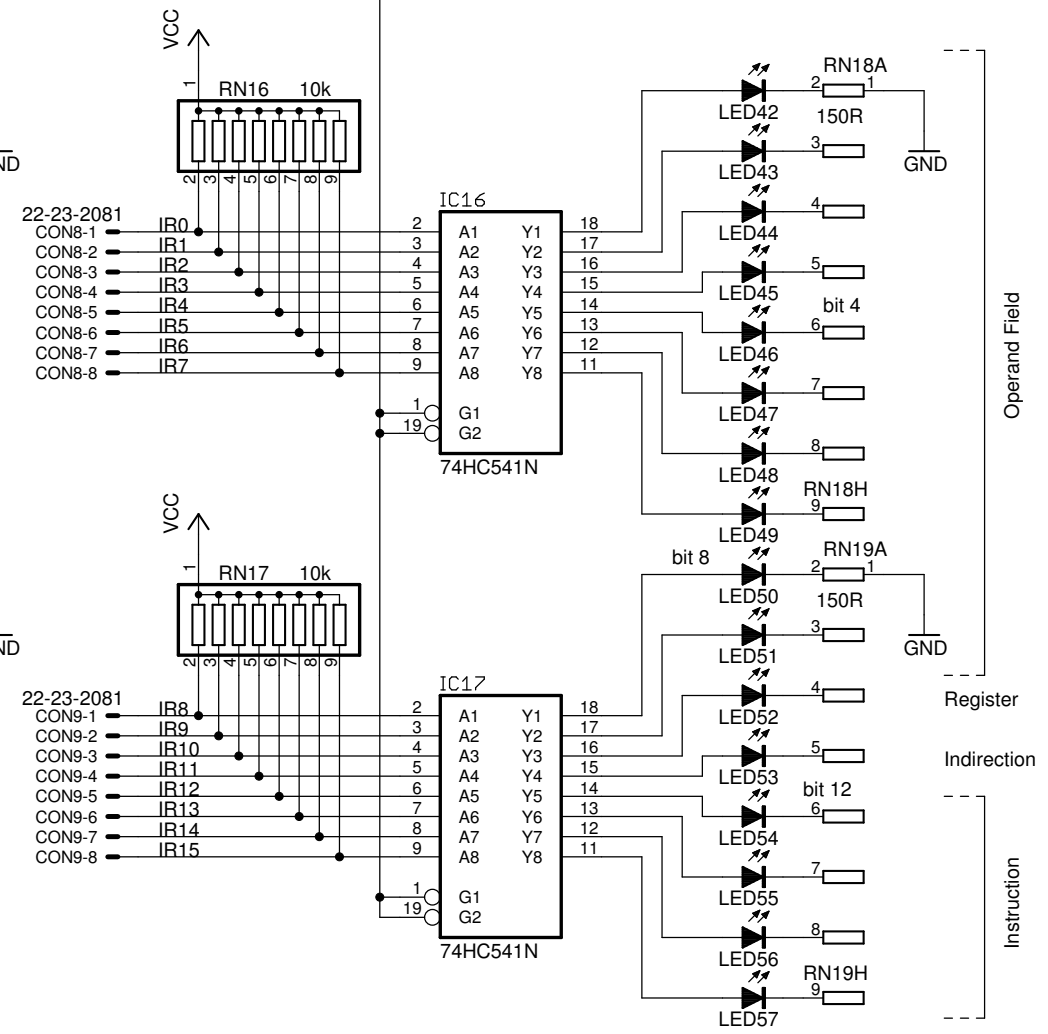
Accumulator Display



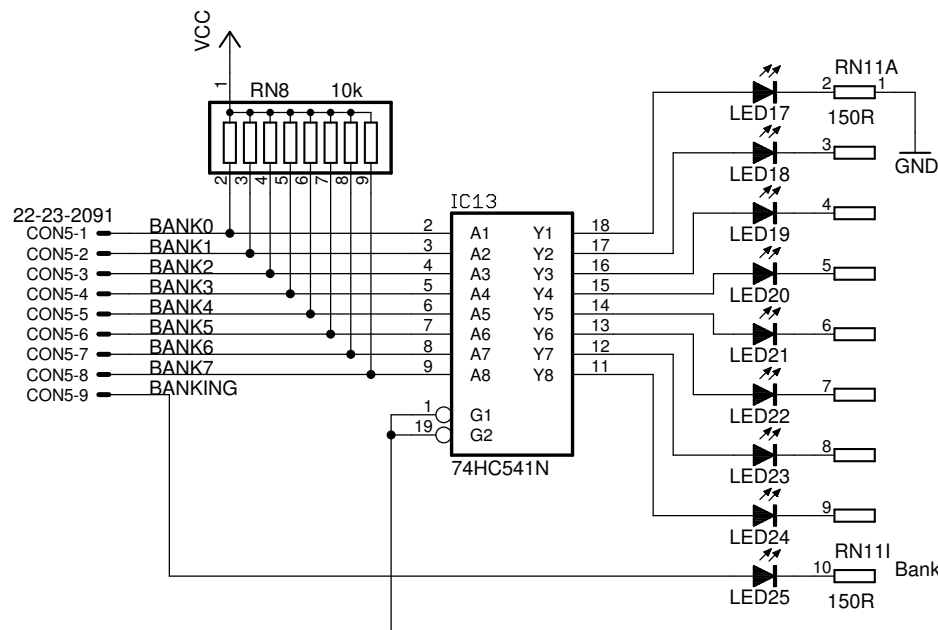
Program Counter Display



Instruction Register Display



Memory Banking Display

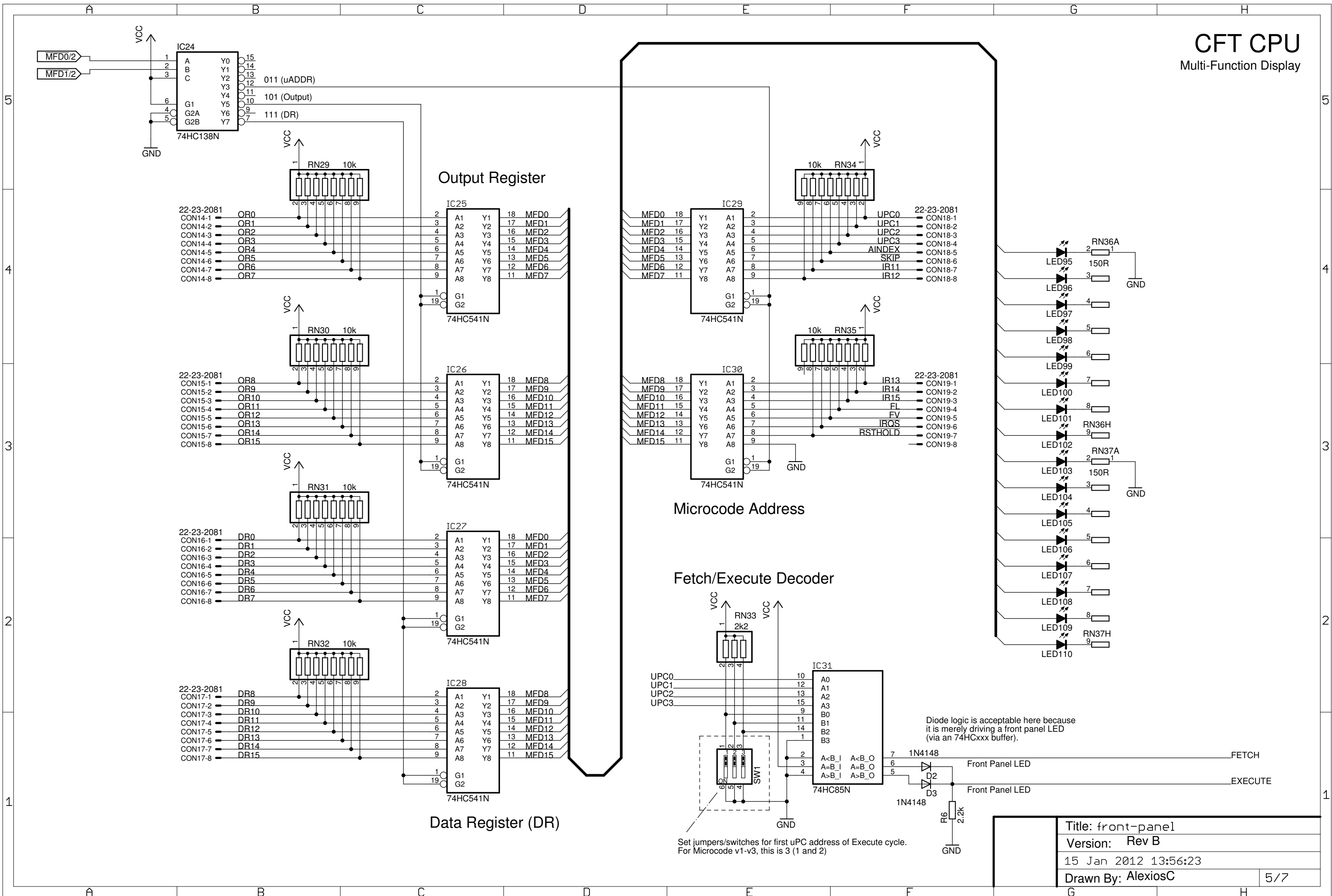


Terse mode configuration jumper.
Shunt on 1-2: always enabled.
Shunt on 2-3: enabled in Verbose mode.
No shunt: always off.

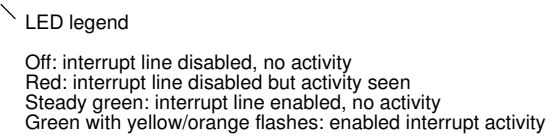
Title: front-panel	
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Drawn By: AlexiosC	3/7

CFT CPU

Multi-Function Display



Front Panel LEDs, Interrupt display

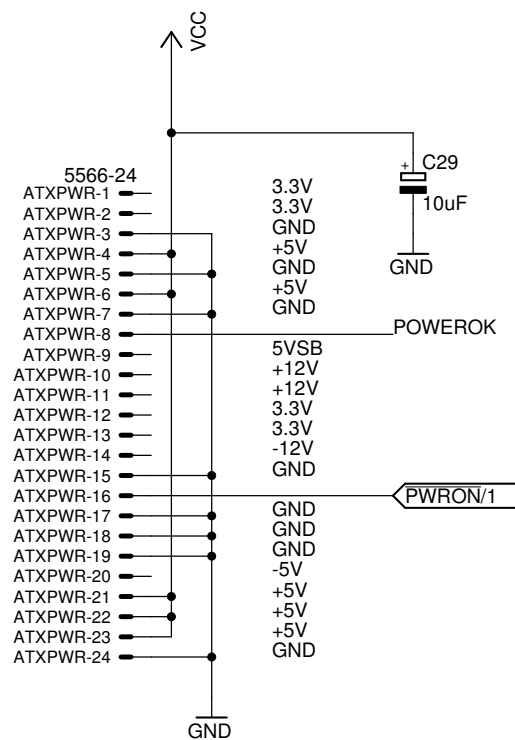


R=330k, C=1uF: ~0.08s delay

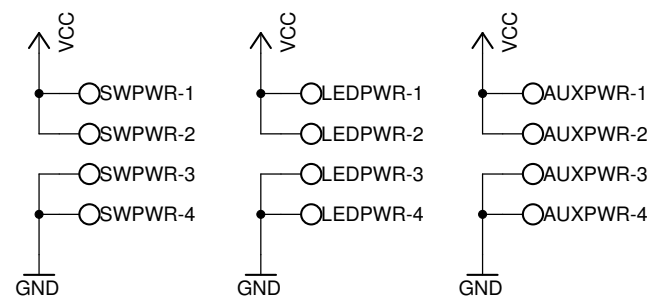
Title: front-panel	
Version: Rev B	
15 Jan 2012 13:56:23	
Drawn By: AlexiosC	6/7

CFT CPU

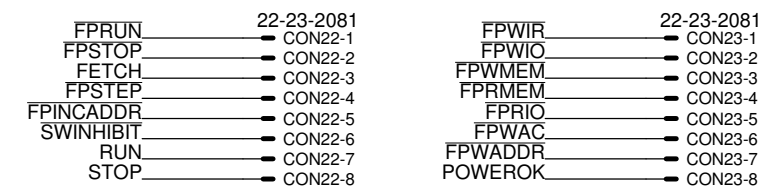
Front Panel Connectors



ATX 24-pin power socket (Molex Mini Fit Jr)
May use screw terminals.



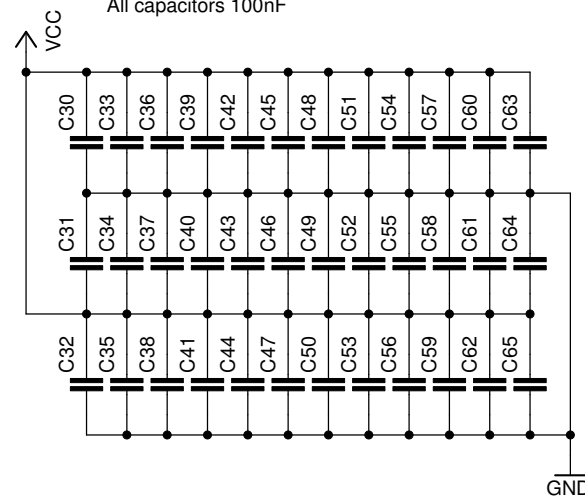
Front Panel Controller Connections



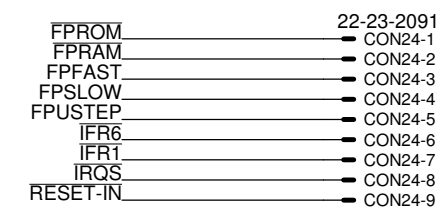
Connector to Front Panel Controller Card.

Decoupling Capacitors

All capacitors 100nF



Connections to Other Cards



Title: front-panel	
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Drawn By: AlexiosC	7/7