

Chip 2V4_TI PAL16L8

Inputs :

P13=1 A19=2 A18=3 A17=4 DSL=5 CTQ2=6 CTQ1=7 CTQ0=8 Input9=9 INPUT11=11 MUX=13
INPUT15=15

Outputs :

CASL=12 DTACK1L=14 DTACK2L=16 RAS2L=17 RAS1L=18 RAS0L=19

Equations :

$$\text{/RAS0L} = \text{A19} * \text{/P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/MUX}$$

$$+ \text{A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{A19} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{/A19} * \text{/A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/MUX}$$

$$+ \text{/A19} * \text{A18} * \text{/DSL} * \text{Input9}$$

$$\text{/RAS1L} = \text{A19} * \text{/P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/MUX}$$

$$+ \text{A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{A19} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{/A19} * \text{/A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/MUX}$$

$$+ \text{A19} * \text{/A18} * \text{/DSL} * \text{Input9}$$

$$\text{/RAS2L} = \text{A19} * \text{/P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/MUX}$$

$$+ \text{A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{A19} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{/A19} * \text{/A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/MUX}$$

$$+ \text{A19} * \text{P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{Input9}$$

$$+ \text{A19} * \text{A18} * \text{A17} * \text{/DSL} * \text{Input9}$$

$$\text{/DTACK2L} = \text{A18} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{A19} * \text{/DSL} * \text{CTQ2} * \text{CTQ1} * \text{CTQ0} * \text{/INPUT11}$$

$$+ \text{A19} * \text{P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{/CTQ2} * \text{INPUT15}$$

$$+ \text{A19} * \text{P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{INPUT15} * \text{/CTQ1}$$

$$+ \text{A19} * \text{P13} * \text{A18} * \text{/A17} * \text{/DSL} * \text{INPUT15} * \text{/CTQ0}$$

$$+ A19 * A18 * A17 * /DSL * /CTQ2 * INPUT15$$

$$+ A19 * A18 * A17 * /DSL * INPUT15 * /CTQ1$$

$$\textbf{/DTACK1L} = A19 * A18 * A17 * /DSL * INPUT15 * /CTQ0$$

$$+ A19 * /A18 * /DSL * /CTQ2 * INPUT15$$

$$+ A19 * /A18 * /DSL * INPUT15 * /CTQ1$$

$$+ A19 * /A18 * /DSL * INPUT15 * /CTQ0$$

$$+ /A19 * A18 * /DSL * /CTQ2 * INPUT15$$

$$+ /A19 * A18 * /DSL * INPUT15 * /CTQ1$$

$$+ /A19 * A18 * /DSL * INPUT15 * /CTQ0$$

$$\textbf{/CASL} = A19 * /P13 * A18 * /A17 * /DSL * CTQ2 * CTQ1 * CTQ0$$

$$+ /A19 * /A18 * /DSL * CTQ2 * CTQ1 * CTQ0$$

$$+ A18 * /DSL * INPUT15$$

$$+ A19 * /DSL * INPUT15$$