App 1 hanya butuh 80% resources App 2 hanga butuh 20% resources App 2 menustrui fitur poralel sebanyale 40% App 2 menulation form parallel subanyale 99%. praction enhanced App 1 = 0,4 fraction enhanced App 2 = 0,99 Jumlah prosesor = 2 Spold up App 1 = 1,25 KM speed up App 2 = 1,98 speed up overall App 1 = 1
resource App 2 + (resource App 1. [!- Fraction enhanced + Fraction Juntah (PU)] = 119

Speed up overall App2 = 

resource App1 + (resource App2 - [1-traction enhanced + traction whenced to truth (pu

Love : +3 F2,0(Rx) LD MULTO FZ, FU, F2 1+4 F\$, F2, F0 DIVD OH. F4, ocry) LD +3 ADDD F4, F0, F4 42 ADDD F10, F8, FZ +2 SD \$4,0(Ry) +1 ADOT kx, kx, #8 +0 Ry, Ry, \*8 ADOT +0 RW, R4, RA sug +0 BNZ PZO, Losp. +1

26 cycle, mains inphibe: ditension 1 cycle dolay, schingga total cycle yang dibutuhkan deh Loup per itesors' odalah 26 + 11. Instablic . 1 cycle delay = 37 cycle

Decotes).		
pripe line u	Pipeline 1	
LO FZ, O(Rx)	(nop)	Kasena LD memelan 3 ycle +1 cycle
< 4all> 40	2nup>	
< stell > 47	(nup)	
<shan> HD</shan>	(nop)	
Multol F2, F0, F2	(nwp)	karena MULID remotar 4 cycle +1 cycl
(Stail) multa	Lnups	
Cstall ) multid	( Loup)	
<stall> multed</stall>	(nup)	
Stall mult.	Lnop	
DIVD F8, FZ, FO	LD (FY affy))	DIOU wenderth bon 11 Latarty cycle
Is tell Garena DADD)	Stall berena LD)	4) membritchhan 4 latency cycle
2 Stell borera 2005	(Stall hourses)	
(Stall koverals)	< Stall horan LD)	
(stall knem mus)	ADD 0 \$4, PO, F4	
Estall Fasem DIVD.)	< Stall Gorner ADD D>	
Latell Over pups.	(Stall Kurera ADD).	
Cstall karen 1919)	(stall known nun)	
( Stell forms 1917)	Ustell loven bivis	
Stall kovern MVD>.	Ustall keen MVO)	
,	CStall Korera Dlup).	
Amo. Flo, F8, F2	50 F4,0 (Ry).	
LStell koven ADDDS	L Stall Karen SD)	
Litall kanera MODD).	AMTChx, Px, #8	
4005 Pg, Ry, #8	Listall Gener APDID	. 26 Cycle.
(Stall Kavera ADDI)	SUB RW, RY, RX	The second secon
BNZ PW, Lup.	(hop)	

John institute. N't dietecture d'properite 1 d'smat your sama dengan Institute. N' di priperite o dan Kasera N't memilitui Laturcy yang Lebih kecil dani & Institute N', Institute N't1 Lebih dulu selesai. Beriban dua alasan mengapa hal ini berbahaya 1.

- 1. It ka introbe. N dan N+1 menuh's ke register yang some, maka perlu dipastikan bahna instrukt.

  N hams menulis lebih dulu dibanding han historia.

  N+1.
- 2. Jiha instalik. p +1 odalah branch dan instalik. N, maka sebelum Instalik. N selesat, instalik. N +1 hodal boleh menuhir hasil nya ke register. Instalik. N+1 hanya bisa menuhir hasil be register. The bonkston dengan branching dan instaliki. N.

contoh Thetale. N+1 yarg dapat selssai lebih dulu dan metala N adalah 866.

Instrube: N+1: DIPD F8, F2, FO Instrube: N+1: LD F4, O(Ry)

Instruction N+1 often feleral lebih dulu karena memilikal execution latency yang lebih rendah dibanding kan instrukti N. Execution delay DIVD lebih besar dan execution delay LD.

2.5

the same of the sa		
LD F2,0(Rx)		
Cstall horary LO>		
Cstall basem LD)		
28tall horera LDS		
multo FZ, FO, FZ		
(Stall Kosem MULTD)		
< Stall Karena MULTO>		
Cstall Komera muctos		
(Stall Karen MULTD)		
1100 P8, F2, F0		
Cstall known blvDs		
CStall Kovera DIVD>		
Stall Karena DIVD>		
estall Kovera DIVD>		
Lstall Forena DIVO>		
Stall Konsera DIND>		
(Stall Konnera DIVD)		
CStall Karena DIVD>		
< Stall Karena DIVD>		
<stall blug="" farena=""></stall>		
APDD 710, F8, F2		
Cstall Korena BNZ>		

LD FY, dRy) < Stall Karem LD> 2 Stall Lovera LD> < stall kovere LD> APDD F4, FO, F4 < stall karena ADDD) L stall borena ADDO) 50 +4,0(kg) . (Stall Karena SD). ADDI Rx, Rx, \*8 ADDI Ry, Ry, \*8 SUB RZO, RY, RX (nop> (no p> (nop> (nop) <nop> (nop> <nop> (nup> < hup> BNZ FZO, Loup

jumlah Cycle = 22