# Report Lab 3

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## 1 Code

The following code was used to complete the report:

#### 1.1 Reduction

#### 1.1.1 kernel.cu

```
2
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                            University of Illinois
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    *cr
     #define BLOCK_SIZE 512
9
10
11
    __global__ void reduction(float *out, float *in, unsigned size)
12
       13
       Load a segment of the input vector into shared memory
14
       Traverse the reduction tree
15
16
       Write the computed sum to the output vector at the correct index
17
18
19
     // Declare an array for share memory..
     __shared__ float partialSum[2 * BLOCK_SIZE];
20
21
22
     // Initialize some variables to access data...
     unsigned int t = threadIdx.x;
23
24
     unsigned int start = 2 * blockIdx.x * blockDim.x;
25
     // Validation to avoid load data outside of the input array...
26
27
     if(start + t < size)</pre>
      partialSum[t] = in[start + t];
28
29
     else
       partialSum[t] = 0.0f;
30
31
     // Same validation for the other position...
32
     if(start + blockDim.x + t < size)</pre>
33
       partialSum[blockDim.x + t] = in[start + blockDim.x + t];
34
35
     else
       partialSum[blockDim.x + t] = 0.0f;
36
37
     // Iterate through share memory to compute the sum..
38
     for (int stride = blockDim.x; stride > 0; stride /= 2){
39
       __syncthreads(); // Synchronize the share memory load and each iteration...
40
41
       if (t < stride)
         partialSum[t] += partialSum[t + stride];
42
43
     // Do not forget to synchronize last iteration...
44
45
     __syncthreads();
```

```
47  // Copy back the result...
48  out[blockIdx.x] = partialSum[0];
49 }
```

There are not significant changes in the other files.

#### 1.2 Prefix-scan

### 1.2.1 kernel.cu

```
2
                 (C) Copyright 2010 The Board of Trustees of the
3
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                            All Rights Reserved
5
    #define BLOCK_SIZE 512
9
10
   // Define your kernels in this file you may use more than one kernel if you
11
   // need to
12
13
    __global__ void scan(float *out, float *in, unsigned size){
14
     __shared__ float section[2 * BLOCK_SIZE];
15
     int t = blockDim.x * blockIdx.x + threadIdx.x;
16
17
     if(t < size)
18
      if(t == 0)
19
20
        section[0] = 0.0f;
21
        section[threadIdx.x] = in[t - 1];
22
     __syncthreads();
23
24
     for(int stride = 1; stride <= BLOCK_SIZE; stride = stride * 2){</pre>
25
       int index = (threadIdx.x + 1) * stride * 2 - 1;
26
       if(index < 2 * BLOCK_SIZE)</pre>
27
28
         section[index] += section[index - stride];
       __syncthreads();
29
30
31
     for(int stride = BLOCK_SIZE / 2; stride > 0; stride /= 2){
32
       int index = (threadIdx.x + 1) * stride * 2 - 1;
33
       if(index + stride < 2 * BLOCK_SIZE)</pre>
34
         section[index + stride] += section[index];
35
       __syncthreads();
36
37
38
     //_syncthreads();
     if(t < size)
39
       out[t] = section[threadIdx.x];
40
41
42
    __global__ void post(float *out, float *n, unsigned size){
43
     int t = blockDim.x * blockIdx.x + threadIdx.x;
44
45
     out[t] += n[t / BLOCK_SIZE];
46
47
48
    49
   Setup and invoke your kernel(s) in this function. You may also allocate more
50
    {\it GPU} memory if you need to
51
    52
   void preScan(float *out, float *in, unsigned size){
53
     dim3 dim_block(BLOCK_SIZE, 1, 1);
54
     dim3 dim_grid(size/BLOCK_SIZE + 1, 1, 1);
     scan<<<dim_grid, dim_block>>>(out, in, size);
56
57
58
   void postScan(float *out, float *n, unsigned size){
59
     dim3 dim_block(BLOCK_SIZE, 1, 1);
```

```
dim3 dim_grid(size/BLOCK_SIZE + 1, 1, 1);
     post<<<dim_grid, dim_block>>>(out, n, size);
62
63
    1.2.2 main.cu
    2
             (C) Copyright 2010 The Board of Trustees of the
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                   All Rights Reserved
5
6
     *cr
     9
   #include <stdio.h>
   #include "support.h"
10
   #include "kernel.cu"
11
12
   int main(int argc, char* argv[])
13
14
   {
15
     Timer timer;
     // Initialize host variables
16
     printf("\nSetting up the problem..."); fflush(stdout);
17
     startTime(&timer);
18
19
     float *in_h, *out_h;
20
21
     float *in_d, *out_d;
     unsigned num_elements;
22
     cudaError_t cuda_ret;
23
24
     /* Allocate and initialize input vector */
25
     if(argc == 1) {
26
       num_elements = 1000000;
27
28
     } else if(argc == 2) {
       num_elements = atoi(argv[1]);
29
     } else {
30
31
       printf("\n Invalid input parameters!"
         32
         "\n Usage: ./prefix-scan <m> # Input of size m is used"
33
         "\n");
34
       exit(0):
35
36
37
     initVector(&in_h, num_elements);
38
      /* Allocate and initialize output vector */
      out_h = (float*)calloc(num_elements, sizeof(float));
40
      if(out_h == NULL) FATAL("Unable to allocate host");
41
42
     stopTime(&timer); printf("%f s\n", elapsedTime(timer));
43
     printf("Input size = %u\n", num_elements);
44
45
46
      // Allocate device variables
     printf("Allocating device variables..."); fflush(stdout);
47
     startTime(&timer);
48
      cuda_ret = cudaMalloc((void**)&in_d, num_elements*sizeof(float));
49
      if(cuda_ret != cudaSuccess) FATAL("Unable to allocate device memory");
50
     cuda_ret = cudaMalloc((void**)&out_d, num_elements*sizeof(float));
51
      if(cuda_ret != cudaSuccess) FATAL("Unable to allocate device memory");
52
53
      cudaDeviceSynchronize();
      stopTime(&timer); printf("%f s\n", elapsedTime(timer));
54
55
      // Copy host variables to device
56
     printf("Copying data from host to device..."); fflush(stdout);
57
     startTime(&timer);
58
      cuda_ret = cudaMemcpy(in_d, in_h, num_elements*sizeof(float), cudaMemcpyHostToDevice);
59
      if(cuda_ret != cudaSuccess) FATAL("Unable to copy memory to the device");
60
      cuda_ret = cudaMemset(out_d, 0, num_elements*sizeof(float));
     if(cuda_ret != cudaSuccess) FATAL("Unable to set device memory");
62
      cudaDeviceSynchronize();
63
     stopTime(&timer); printf("%f s\n", elapsedTime(timer));
```

```
65
        // Launch kernel
 66
 67
       printf("Launching kernel..."); fflush(stdout);
       startTime(&timer);
 68
        // Set up and invoke your kernel inside the preScan function,
 69
        // which is in kernel.cu
 70
       preScan(out_d, in_d, num_elements);
 71
       cuda_ret = cudaDeviceSynchronize();
 72
 73
        if(cuda_ret != cudaSuccess) FATAL("Unable to launch/execute kernel");
       stopTime(&timer); printf("%f s\n", elapsedTime(timer));
 74
 75
        // Copy device variables from host
 76
       printf("Copying data from device to host..."); fflush(stdout);
 77
       startTime(&timer);
 78
       cuda_ret = cudaMemcpy(out_h, out_d, num_elements*sizeof(float), cudaMemcpyDeviceToHost);
 79
       if(cuda_ret != cudaSuccess) FATAL("Unable to copy memory to host");
 80
 81
       cudaDeviceSynchronize();
       stopTime(&timer); printf("%f s\n", elapsedTime(timer));
 82
 83
        // My code...
 84
       float *partial_h, *partial_d;
 85
 86
       partial_h = (float *) malloc((num_elements/BLOCK_SIZE + 1) * sizeof(float));
       partial_h[0] = 0;
 87
 88
       int n = 1;
        for(int i = BLOCK_SIZE - 1; i < num_elements; i += BLOCK_SIZE){</pre>
 89
         partial_h[n] = partial_h[n - 1] + out_h[i];
 90
 91
       }
 92
       if((num_elements/BLOCK_SIZE + 1) <= 10){</pre>
 93
         for(int i = 0; i < n; i ++){
           printf("\nPARTIAL[%d] = %0.3f", i, partial_h[i]);
 95
 96
         printf("\n");
       }
98
99
        11
       cuda_ret = cudaMalloc((void**)&partial_d, (num_elements/BLOCK_SIZE + 1) * sizeof(float));
100
       if(cuda_ret != cudaSuccess) FATAL("Unable to allocate device memory");
101
        cuda_ret = cudaMemcpy(partial_d, partial_h, (num_elements/BLOCK_SIZE + 1) * sizeof(float),
        if(cuda_ret != cudaSuccess) FATAL("Unable to copy memory to the device");
103
104
       postScan(out_d, partial_d, num_elements);
105
106
       cuda_ret = cudaDeviceSynchronize();
        if(cuda_ret != cudaSuccess) FATAL("Unable to launch/execute kernel");
107
108
       cuda_ret = cudaMemcpy(out_h, out_d, num_elements*sizeof(float), cudaMemcpyDeviceToHost);
110
       if(cuda_ret != cudaSuccess) FATAL("Unable to copy memory to host");
111
112
        // Verifu correctness
113
       printf("Verifying results..."); fflush(stdout);
114
       verify(in_h, out_h, num_elements);
115
116
        // Printing results (just for debugging purposes)...
117
       if(num_elements <= 100){</pre>
118
         printf("\nPrinting IN (%d elements)...\n", num_elements);
119
         for(int i = 0; i < num_elements; i++){</pre>
120
          printf("%0.3f ", in_h[i]);
121
122
         printf("\n");
123
124
125
         printf("\nPrinting OUT (%d elements)...\n", num_elements);
         for(int i = 0; i < num_elements; i++){
  printf("%0.3f ", out_h[i]);</pre>
126
127
128
         printf("\n");
129
130
131
        // Free memory
132
       cudaFree(in_d); cudaFree(out_d); cudaFree(partial_d);
133
```

## 2 Answer to Questions

1. In your kernel implementation, how many threads can be simultaneously executing? Assume a GeForce GTX 280 GPU which has 30 streaming multiprocessors.

## References

- [1] Andres Calderon. GitHub Personal Repository, 2015. https://github.com/aocalderon/PhD/tree/master/Y1Q1/GPU/lab2.
- [2] David Kirk and Wen-Mei Hwu. Programming Massively Parallel Processors: A Hands-On Approach. Morgan Kaufmann, 2012.
- [3] Wen-Mei Hwu. A Tiled Kernel for Arbitrary Matrix Dimensions Heterogeneous Parallel Programming. Coursera Course, 2015. https://www.dropbox.com/s/4y06b1m6dozp2kt/2%20-%208%20-%202.8-%20A%20Tiled%20Kernel%20for%20Arbitrary%20Matrix%20Dimensions.mp4?dl=0.
- [4] David Luebke, John Owens, Mike Roberts and Cheng-Han Lee. *Using NVVP Part1 and Part 2 Intro to Parallel Programming*. Udacity Course, 2015. https://www.youtube.com/watch?v=hyKA5fb5ZJI.
- [5] Nvidia Corporation. GeForce® GTX 200 GPU Architectural Overview. Technical Brief #TB-04044-001\_v01, 2008. http://www.nvidia.com/docs/IO/55506/GeForce\_GTX\_200\_GPU\_Technical\_Brief.pdf.