	А	В	D	E	F	G	Н	1	J	К	L	М
		Б		L	'	J	'''		<u> </u>	I N	L	IVI
1	Board Type ->	Total of columns to the right	TF board, -2 FPGAs (Peter)	CU dev board, -2 FPGAs (Peter)	BU dev board, -2 FPGAs (Peter)	BRIL board, -2 FPGAs (BRIL will buy)	Lawrence board, -2 FPGAs (FPGAs from Lawrence) 1 of 3 initially assembled	Spare board, -2 FPGAs (FPGAs from Charlie) 1 of 3 initially assembled		Benchtop Standalone (No FPGAs) 1 of 3 initially assembled	Spare board and parts (Assemble if needed)	Extra bare boards to assembler
2	Qty of each type ->		20	2	2	1	1	1		1	2	2
4	Bare PCBs	32	20	2	2	1	1	1		1	2	2
6	-2 FPGAs from Peter	48	40	4	4							
7	-2 FPGAs from BRIL	2				2						
8	-2 FPGAs from Lawrence	2					2					
9	-2 FPGAs from Charlie	2						2				
	First pass assembly,											
	with -2 FPGA	2					1	1				
	First pass assembly,	_					_	_				
	No FPGA	1								1		
	Final assembly,											
13	with -2 FPGAs	25	20	2	2	1						
15	Non-FPGA part kits	30	20	2	2	1	1	1		1	2	
	Front Panels	29	20	2	2	1	1	1			2	
17	Heat Sinks	29	20	2	2	1	1	1			2	
	Per Board FireFly 25Gx12											
19	Receiver	8	4	4								
	Per Board FireFly 25Gx12											
	Transmitter	3	1	1	1							
	Per Board FireFly 25Gx4											
21	transceiver	10	1	1	4		4					
22	Per Board FireFly CERN-B	6			6							
24	Total FireFly 25Gx12 Receiver	88	80	8	0	0	0	0		0	0	
	Total FireFly 25Gx12											
25	Transmitter	24	20	2	2	0	0	0		0	0	
26	Total FireFly 25Gx4 transceiver	34	20	2	8	0	4	0		0	0	
	Total FireFly CERN-B	12	0	0	12	0	0	0		0	0	
	,		-	-		-	-	-		-	-	