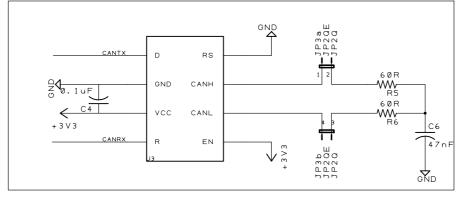
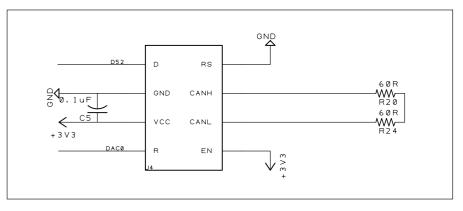


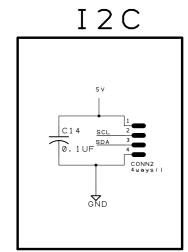
I/O TERMINAL BLOCK

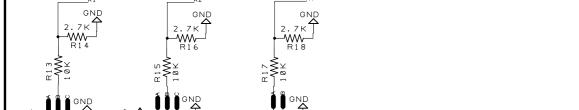




CAN0

CAN1





INPUTS ANALOG

