



中山大學  
SUN YAT-SEN UNIVERSITY



国家超级计算广州中心  
NATIONAL SUPERCOMPUTER CENTER IN GUANGZHOU

# Compilation Principle 编译原理

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## 第18讲：中间代码(2)

张献伟

[xianweiz.github.io](https://xianweiz.github.io)

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# Review Questions

- What is IR (specifically, the low-level IR)?

Intermediate Representation. A machine- and language-independent version of the original source code.

- Why do we use IR?

Clean separation of front- and back-end; easy to optimize and extend

- What is three-address code (TAC)?

A type of IR, with at most three operands. (High-level assembly)

- TAC of  $x + y * z + 5$ ?

$t_1 = y * z; t_2 = x + t_1; t_3 = t_2 + 5;$

- Possible ways to implement TAC?

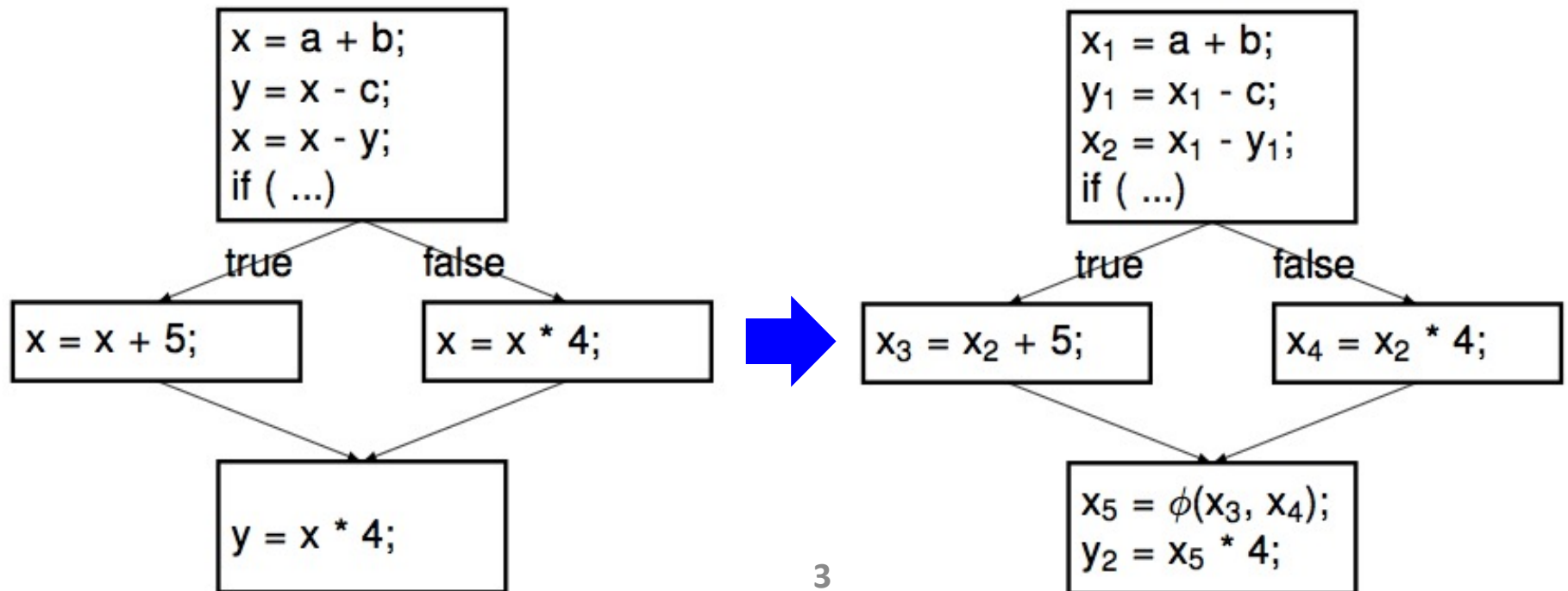
Quadruples: op arg1, arg2, result

Triples: op arg1 arg2

Indirect triples: op arg1 arg2

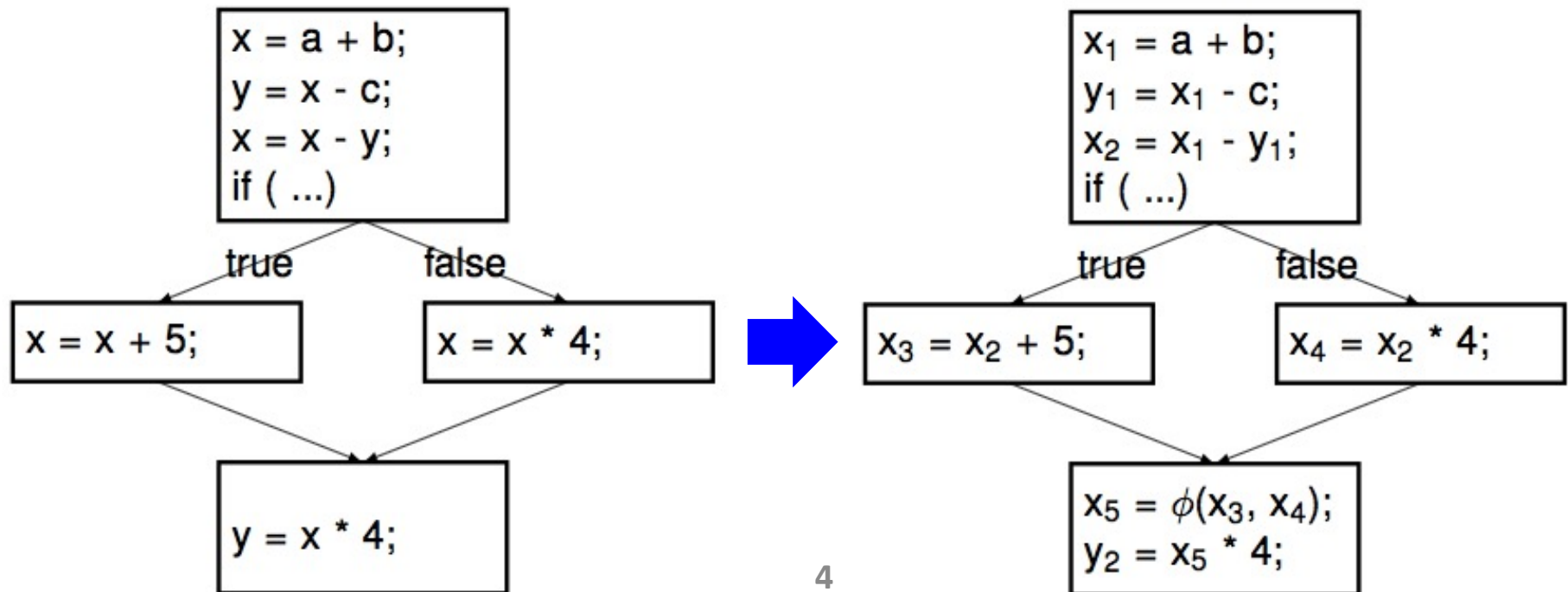
# Single Static Assignment[静态单赋值]

- Every variable is assigned to exactly once statically[仅一次]
  - Give variable a different version name on every assignment
    - e.g.  $x \rightarrow x_1, x_2, \dots, x_5$  for each static assignment of  $x$
  - Now value of each variable guaranteed not to change
  - On a control flow merge,  $\phi$ -function combines two versions
    - e.g.  $x_5 = \phi(x_3, x_4)$ : means  $x_5$  is either  $x_3$  or  $x_4$



# Benefits of SSA

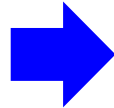
- SSA is an IR that facilitates certain code optimizations
  - SSA tells you when an optimization **shouldn't** happen
  - Suppose compiler performs CSE on previous example:
    - Without SSA, (incorrectly) tempted to eliminate second  $x * 4$ 
      - $x = x * 4; y = x * 4; \rightarrow x = x * 4; y = x;$
    - With SSA,  $x_2 * 4$  and  $x_5 * 4$  are clearly different values



# Benefits of SSA (cont.)

- SSA is an IR that facilitates certain code optimizations
  - SSA tells you when an optimization **should** happen
  - Suppose compiler performs dead code elimination (DCE): (DCE removes code that computes dead values)

```
x = a + b;  
x = c - d;  
y = x * b;
```



```
x1 = a + b;  
x2 = c - d;  
y1 = x2 * b;
```

- Without SSA, not very clear whether there are dead values
  - With SSA,  $x_1$  is never used and clearly a dead value
- Why does SSA work so well with compiler optimizations?
  - SSA makes flow of values explicit in the IR
  - Without SSA, need a separate dataflow graph
  - Will discuss more in **Compiler Optimization** section

# SSA Orthogonal to IR Impl.[正交关系]

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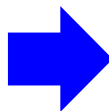
- SSA is expressed most commonly as TACs
- We learned 3 ways to implement TACs
  - quadruples
  - triples
  - indirect triples
- How you implement is orthogonal to SSA representation
  - After variable renaming, any 3-address code becomes SSA
- SSA is used widely in modern compilers:
  - GCC (GNU C Compiler)
  - LLVM Compiler
  - Oracle Java JIT Compiler
  - Google Chrome JavaScript JIT Compiler
  - PyPy Python JIT Compiler

# LLVM: SSA and Phi



- All LLVM instructions are represented in the Static Single Assignment (SSA) form
  - Affordable to the design of simpler algorithms for existing optimizations and has facilitated the development of new ones
- The 'phi' instruction is used to implement the  $\phi$  node in the SSA graph representing the function
  - `<result> = phi [fast-math-flags] <ty> [ <val0>, <label0>], ...`
  - At runtime, the 'phi' instruction logically takes on the value specified by the pair corresponding to the predecessor basic block that executed just prior to the current block

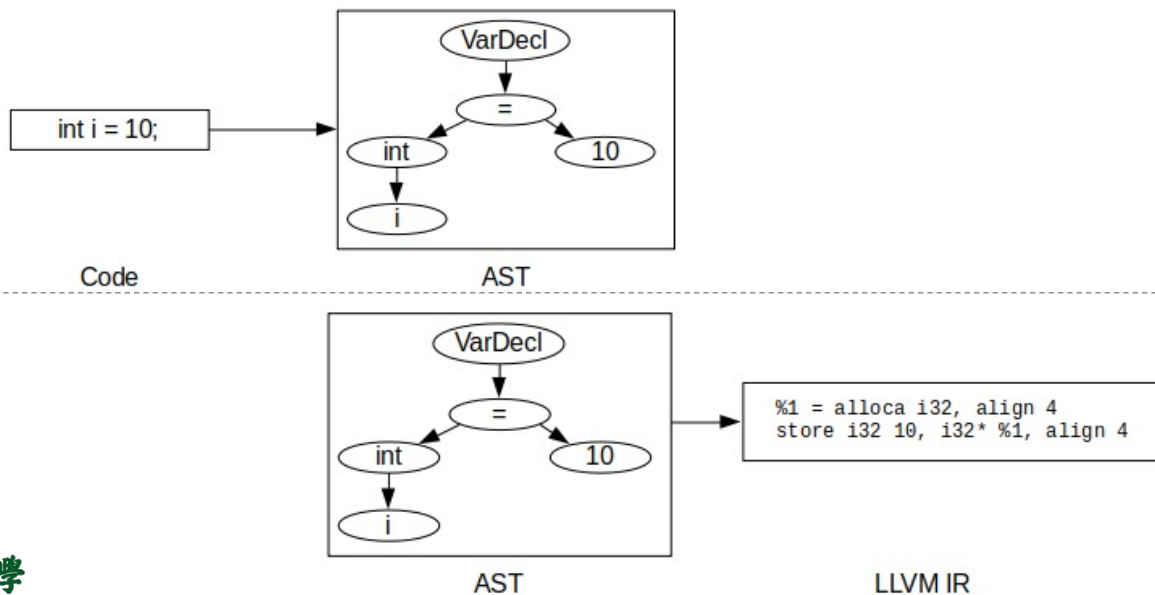
```
a = 1;  
if (v < 10)  
    a = 2;  
b = a;
```



```
a1 = 1;  
if (v < 10)  
    a2 = 2;  
b = PHI(a1, a2);
```

# Generating Code: AST to IR[IR生成]

- By now, we have
  - An AST, annotated with scope and type information
- To generate three-address codes (TACs)
  - Traversing the AST after the parse[单独遍历]
    - Writing a codeGen method for the appropriate kinds of AST nodes
  - Syntax-directed translation[语法制导]
    - Generating code while parsing





# Syntax Directed Translation[语法制导翻译]

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- Syntax directed translation can be used again for code generation[代码生成]
  - Since code generation is also dependent on syntax/AST
  - Code generation is translating syntactic structures to code
- What language structures do we need to translate?[翻译]
  - Definitions (variables, functions, ...)
  - Assignment statements
  - Control flow statements (if-then-else, for-loop, ...)
  - ...
- We are going to use the following strategy:
  - Specify SDD semantic rules (without ordering)
  - Convert SDD rules to SDT actions (with ordering)
    - In the process, we will discover SDD has non-*L-attributes*
    - We will also discuss what to do with those non-*L-attributes*

# Code Generation Overview[代码生成]

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- Program code is a collection of functions
  - By now, all functions are listed in symbol table
- Goal is to generate code for each function in that list
- Generating code for a function involves two steps:
  - Processing variable definitions[变量定义]
    - Involves laying out variables in memory
  - Processing statements[语句]
    - Involves generating instructions for statements
      - Assignment[赋值]
      - Array references[数组引用]
      - Boolean expressions[布尔表达式]
      - Control-flow statements[控制流语句]
      - ...
- We will start with processing variable definitions

# Processing Variable Definitions[变量定义]

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- To lay out a variable, both **location** and **width** are needed
  - Location: where variable is located in memory
  - Width: how much space variable takes up in memory
- Attributes for variable definition:
  - **T V**      e.g. int x;
  - **T**: non-terminal for type name
    - **T.type**: type (int, float, ...)
    - **T.width**: width of type in bytes (e.g. 4 for int)
  - **V**: non-terminal for variable name
    - **V.type**: type (int, float, ...)
    - **V.width**: width of variable according to type
    - **V.offset**: offset of variable in memory
  - But offset from what...?

# Calculate Variable Location from Offset

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- Naive method: reserve a big memory section for all data
  - Size data section to be large enough to contain all variables
  - Location = var offset + base of data section
- Naive method wastes a lot of memory
  - Vars with limited scope need to live only briefly in memory
    - E.g. function variables need to last only for duration of call
- **Solution:** allocate memory briefly for each scope[域内]
  - Allocate when entering scope, free when exiting scope
  - Variables in the same scope are allocated / freed together
  - Location = var offset + base of scope memory section
  - Will discuss more later in **Runtime Management**

# Storage Layout of Variables in a Function

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- When there are multiple variables defined in a function,
  - Compiler lays out variables in memory sequentially
  - Current offset used to place variable x in memory
    - $\text{address}(x) \leftarrow \text{offset}$
    - $\text{offset} += \text{sizeof}(x.\text{type})$

```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

# Storage Layout of Variables in a Function

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```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

Address

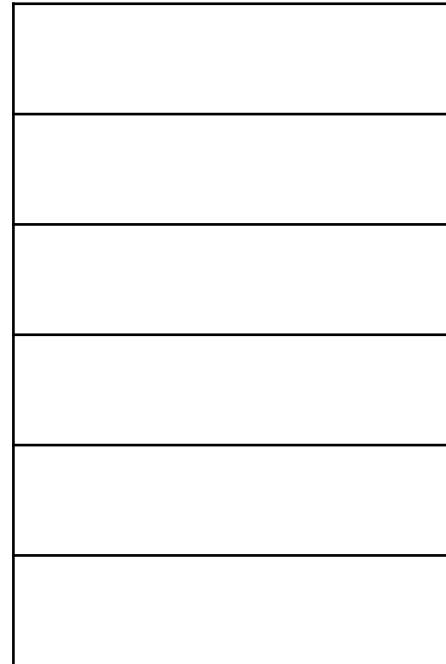
0x0000

0x0004

0x0008

0x000c

0x0010



# Storage Layout of Variables in a Function

- When there are multiple variables defined in a function,
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  - Current offset used to place variable x in memory
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```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

Address

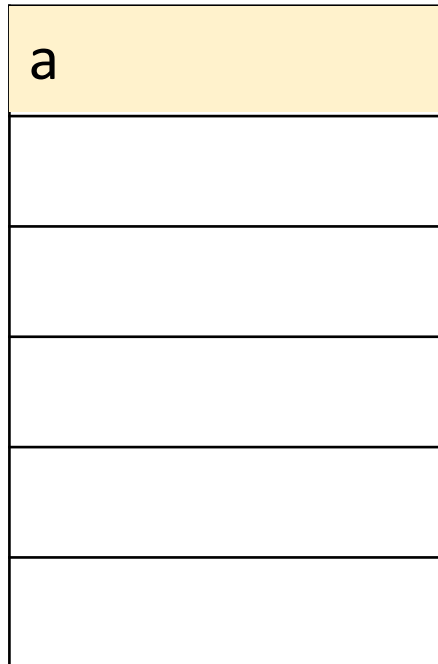
0x0000

0x0004

0x0008

0x000c

0x0010



Offset = 0

$\text{Addr}(a) \leftarrow 0$

# Storage Layout of Variables in a Function

- When there are multiple variables defined in a function,
  - Compiler lays out variables in memory sequentially
  - Current offset used to place variable x in memory
    - $\text{address}(x) \leftarrow \text{offset}$
    - $\text{offset} += \text{sizeof}(x.\text{type})$

```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

Address

0x0000

a

Offset = 0

$\text{Addr}(a) \leftarrow 0$

0x0004

b

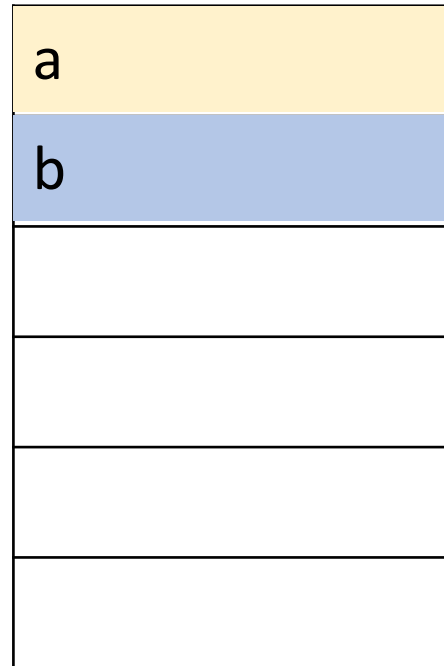
Offset = 4

$\text{Addr}(b) \leftarrow 4$

0x0008

0x000c

0x0010





# Storage Layout of Variables in a Function

- When there are multiple variables defined in a function,
  - Compiler lays out variables in memory sequentially
  - Current offset used to place variable x in memory
    - $\text{address}(x) \leftarrow \text{offset}$
    - $\text{offset} += \text{sizeof}(x.\text{type})$

```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

Address

0x0000

a

Offset = 0

$\text{Addr}(a) \leftarrow 0$

0x0004

b

Offset = 4

$\text{Addr}(b) \leftarrow 4$

0x0008

c

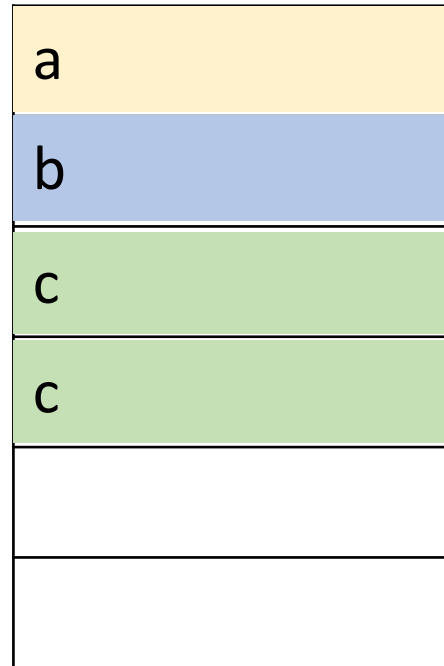
Offset = 8

$\text{Addr}(c) \leftarrow 8$

0x000c

c

0x0010



# Storage Layout of Variables in a Function

- When there are multiple variables defined in a function,
  - Compiler lays out variables in memory sequentially
  - Current offset used to place variable x in memory
    - $\text{address}(x) \leftarrow \text{offset}$
    - $\text{offset} += \text{sizeof}(x.\text{type})$

```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

Address

0x0000

a

Offset = 0

$\text{Addr}(a) \leftarrow 0$

0x0004

b

Offset = 4

$\text{Addr}(b) \leftarrow 4$

0x0008

c

Offset = 8

$\text{Addr}(c) \leftarrow 8$

0x000c

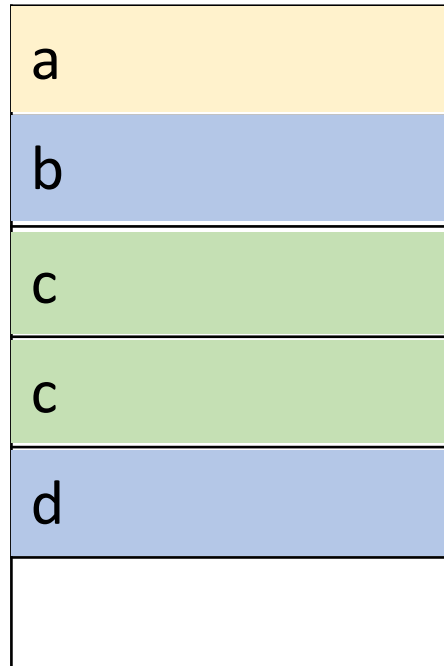
c

0x0010

d

Offset = 16

$\text{Addr}(d) \leftarrow 16$



# Storage Layout of Variables in a Function

- When there are multiple variables defined in a function,
  - Compiler lays out variables in memory sequentially
  - Current offset used to place variable x in memory
    - $\text{address}(x) \leftarrow \text{offset}$
    - $\text{offset} += \text{sizeof}(x.\text{type})$

```
void foo() {  
    int a;  
    int b;  
    long long c;  
    int d;  
}
```

Address

0x0000

a

Offset = 0

$\text{Addr}(a) \leftarrow 0$

0x0004

b

Offset = 4

$\text{Addr}(b) \leftarrow 4$

0x0008

c

Offset = 8

$\text{Addr}(c) \leftarrow 8$

0x000c

c

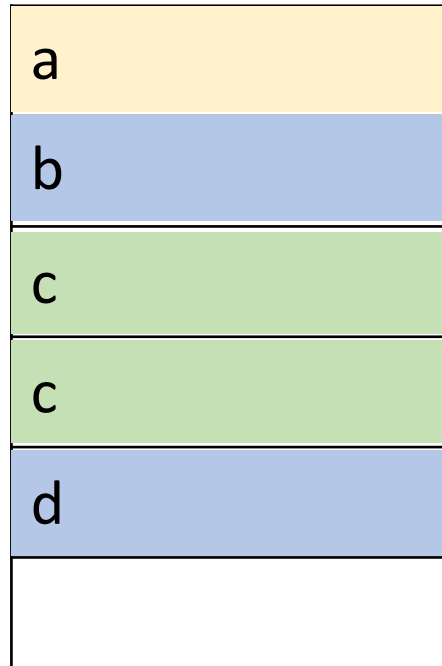
0x0010

d

Offset = 16

$\text{Addr}(d) \leftarrow 16$

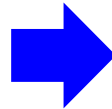
Offset = 20



# More about Storage Layout

- Allocation alignment[对齐]
  - Enforce  $\text{addr}(x) \% \text{sizeof}(x.\text{type}) == 0$
  - Most machine architectures are designed such that computation is most efficient at sizeof(x.type) boundaries
    - E.g. most machines are designed to load integer values at integer word boundaries
    - If not on word boundary, need to load two words and shift & concatenate → inefficient

```
void foo() {  
    char a;      // addr(a) = 0  
    int b;       // addr(b) = 1  
    int c;       // addr(c) = 5  
    long long d; // addr(d) = 9  
}
```

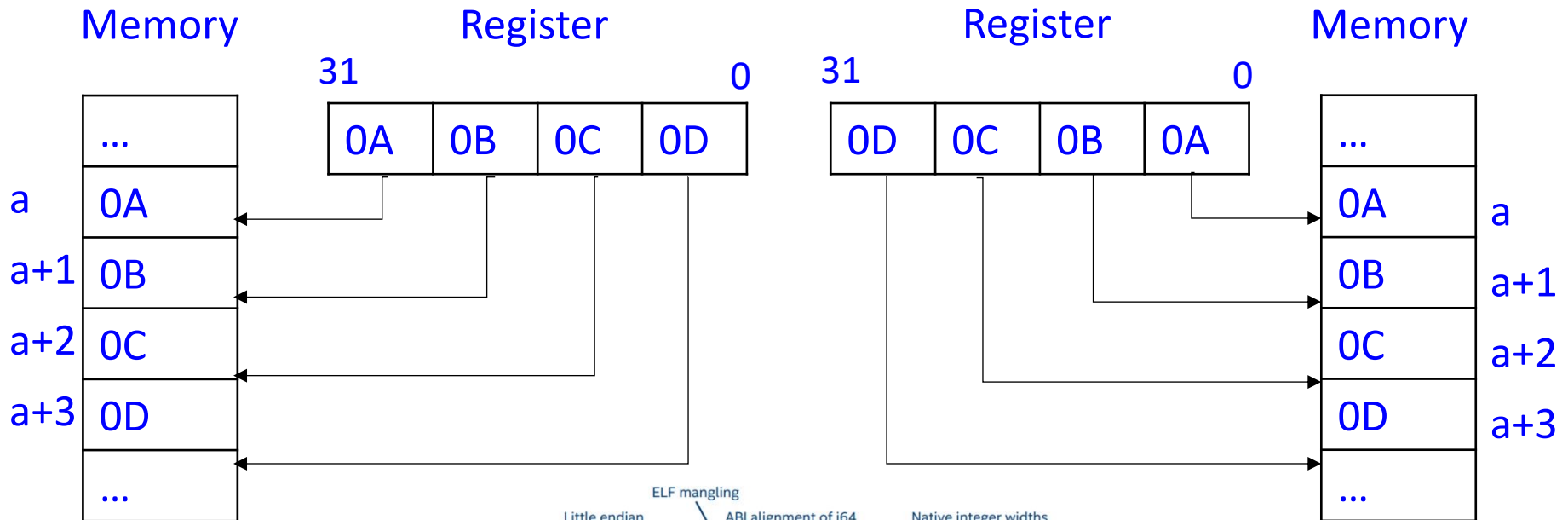


```
void foo() {  
    char a;      // addr(a) = 0  
    int b;       // addr(b) = 4  
    int c;       // addr(c) = 8  
    long long d; // addr(d) = 16  
}
```

# More about Storage Layout (cont.)

- Endianness[字节序]

- Big endian: MSB (most significant byte) in lowest address
- Little endian: LSB (least significant byte) in lowest address



target datalayout = "e-m:e-i64:64-f80:128-n8:16:32:64-S128"

Annotations for the string:

- Little endian (points to 'e')
- ELF mangling (points to 'm')
- ABI alignment of i64 (points to 'i64')
- Native integer widths (points to 'n8:16:32:64')

Big-endian[大字节序]

Little-endian[小字节序]

# Type Expressions[类型表达式]

- A **type expression** is either a basic type or is formed by applying an operator called a *type constructor*[类型构造符] to a type expression
  - Basic type: *integer, float, char, Boolean, void*
  - Array: *array(l, T)* is a type expression, if *T* is
    - $\text{int}[3] \leftrightarrow \text{array}(3, \text{int})$
    - $\text{int}[2][3] \leftrightarrow \text{array}(2, \text{array}(3, \text{int}))$
  - Pointer: *pointer(T)* is a type expression, if *T* is
    - $\text{int}^* \text{val} \leftrightarrow \text{pointer}(\text{int})$

$$\begin{aligned} P &\rightarrow D \\ D &\rightarrow T \text{ id}; D_1 \mid \varepsilon \\ T &\rightarrow B C \mid *T_1 \\ B &\rightarrow \text{int} \mid \text{real} \\ C &\rightarrow [\text{num}]C_1 \mid \varepsilon \end{aligned}$$

# CodeGen: Variable Definitions

- Translating variable definitions

- *enter(name, type, offset)*

- Save the type and relative address in the symbol-table entry for the name

①  $P \rightarrow \{ \text{offset} = 0 \} D$

②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$

③  $D \rightarrow \varepsilon$

④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$

⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$

⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$

⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$

⑧  $C \rightarrow \varepsilon \{ C.\text{type} = t; C.\text{width} = w; \}$

⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

- Examples:

- *real x; int i;*

- *int[2][3];*

- *type, width*

- Syn attributes

- *t, w*

- Vars to pass type and width from B node to the node for  $C \rightarrow \varepsilon$

- *offset*

- The next relative address

# Example

- Input: `real x; int i;`

- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
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# Example

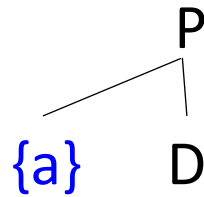
- Input: `real x; int i;`



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}(\text{id.lexeme}, T.\text{type}, \text{offset}); \text{offset} = \text{offset} + T.\text{width}; \} D_1$
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# Example

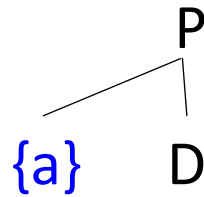
- Input: **real x; int i;**



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- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
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 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

# Example

- Input: **real x; int i;**

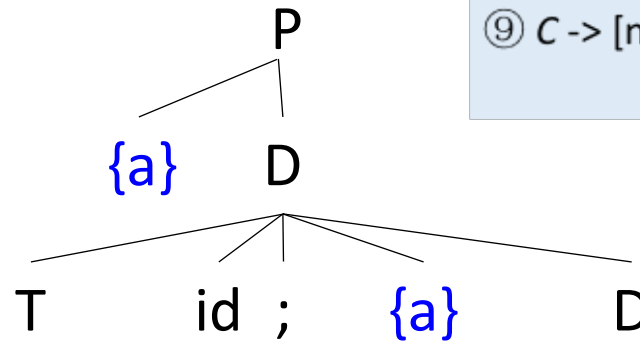


- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
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 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

*offset = 0*

# Example

- Input: **real x; int i;**

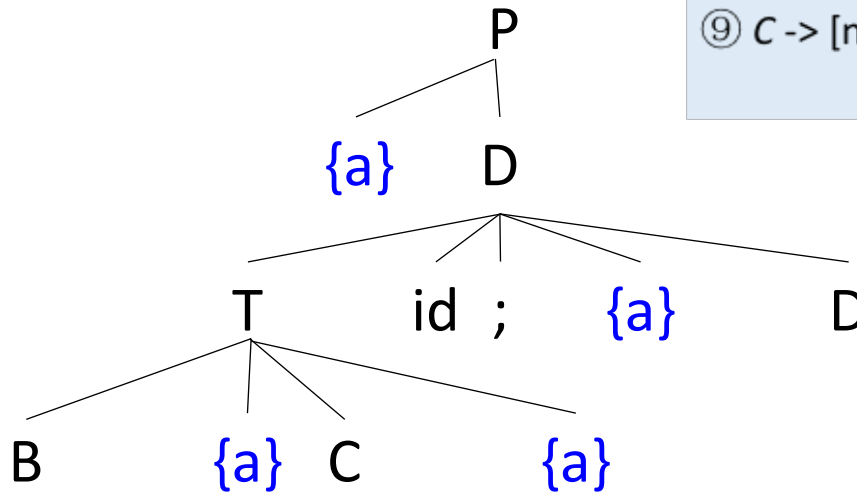


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 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

offset = 0

# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

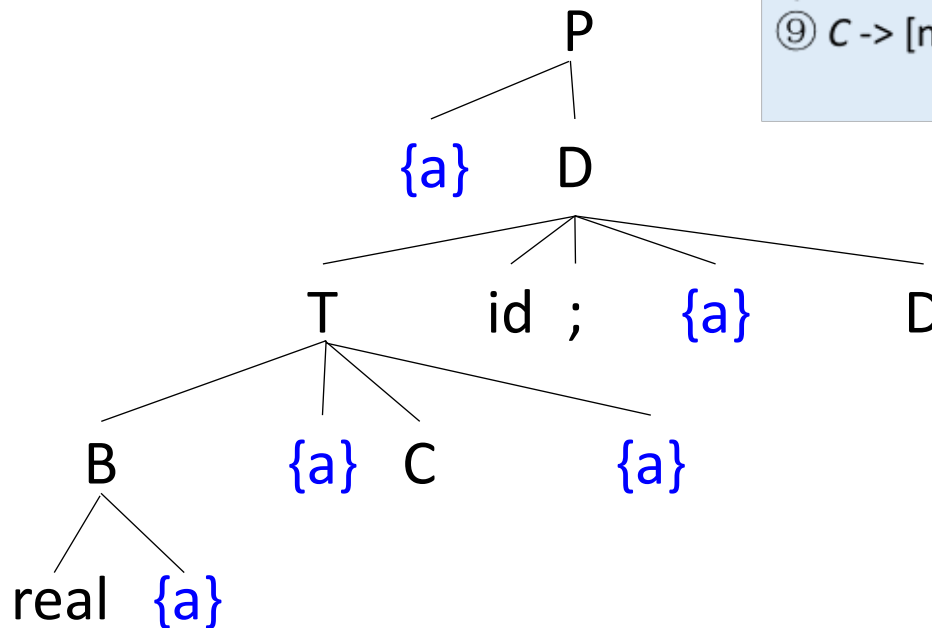
offset = 0

# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



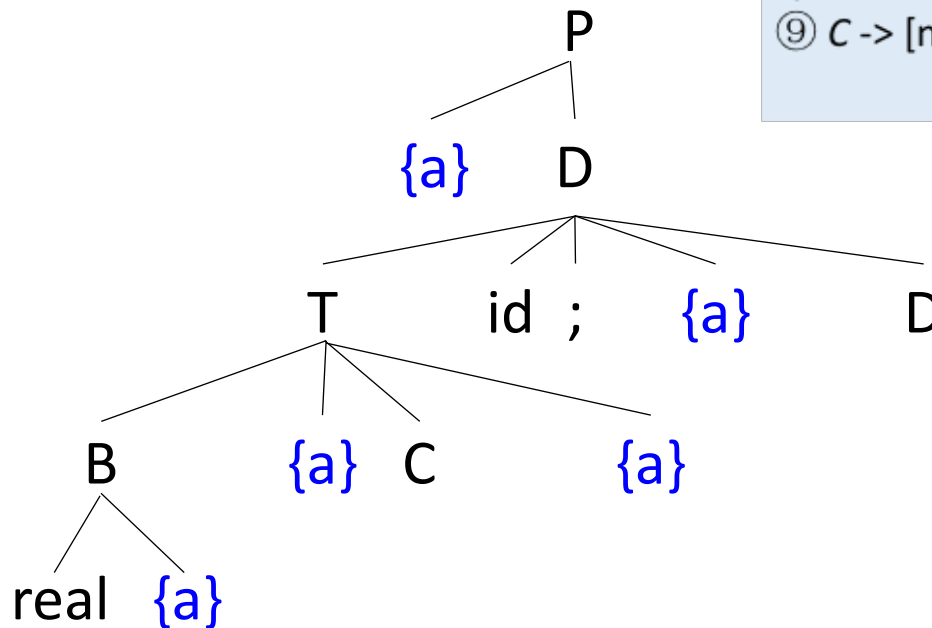
offset = 0

# Example

- Input: **real x; int i;**



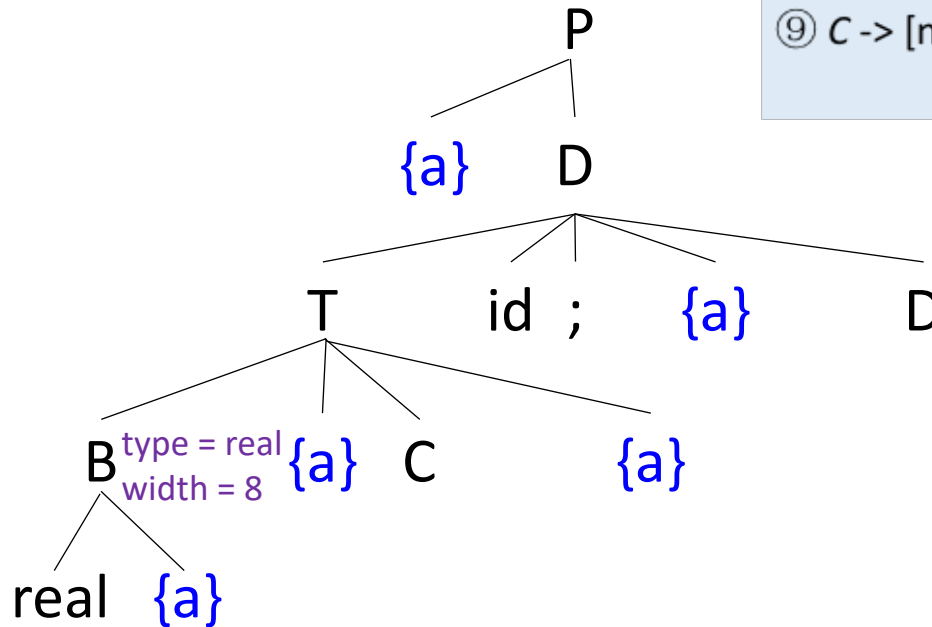
- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



offset = 0

# Example

- Input: **real x; int i;**



offset = 0

- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

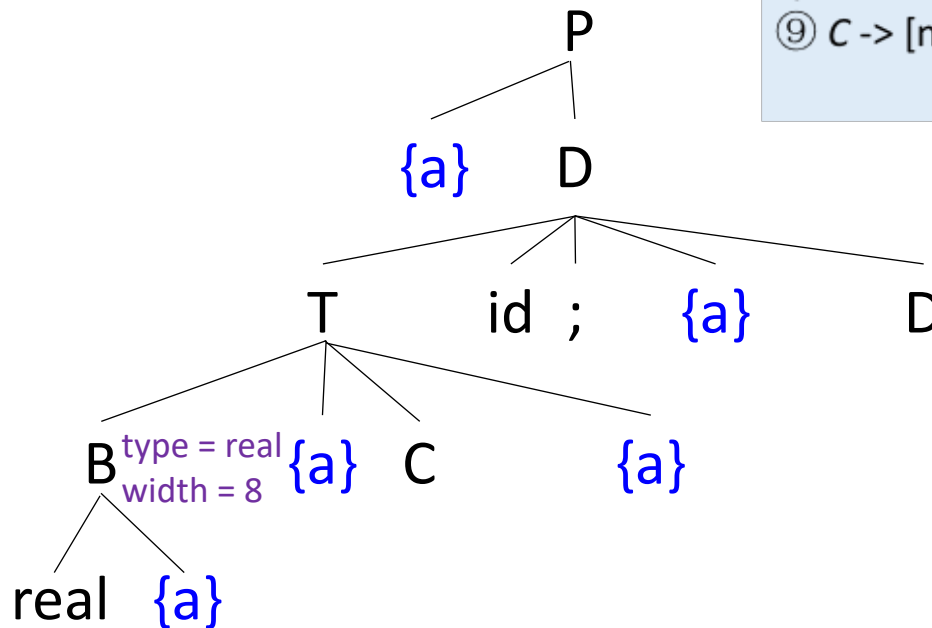


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



$\text{offset} = 0$

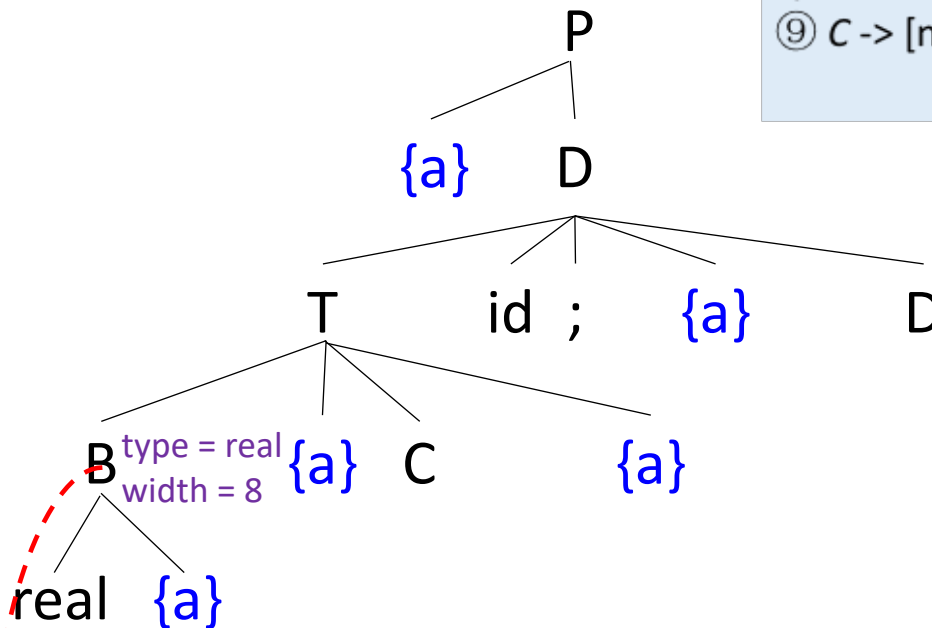
$t = \text{real}$   
 $w = 8$

# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



$\text{offset} = 0$

$t = \text{real}$   
 $w = 8$

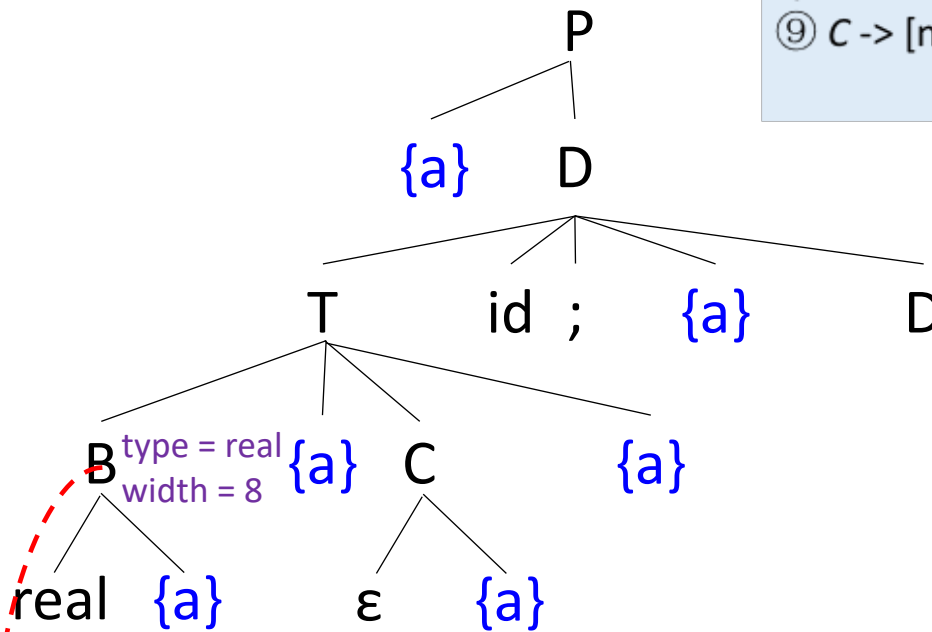


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



$\text{offset} = 0$

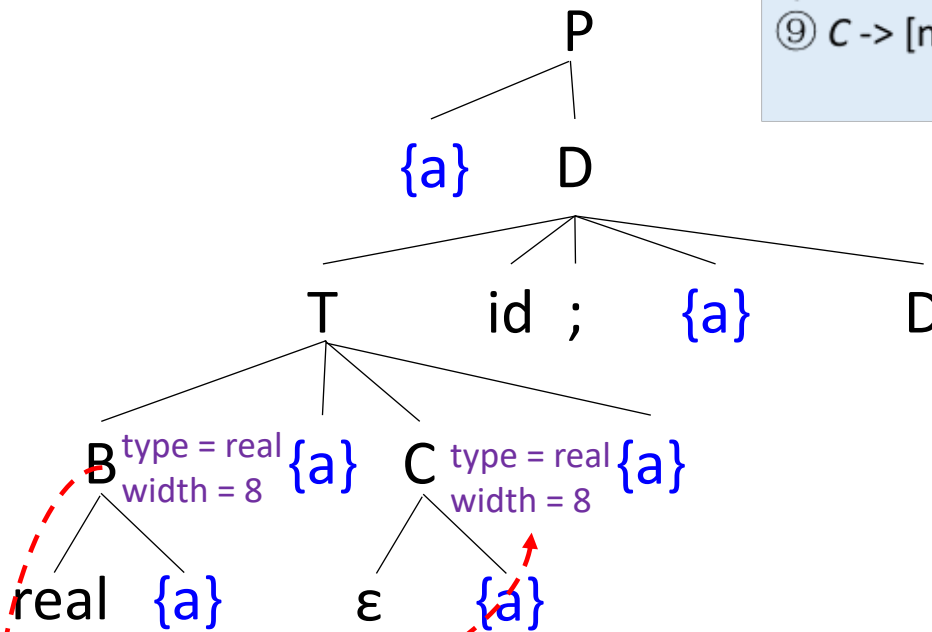
$t = \text{real}$   
 $w = 8$

# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



offset = 0

t = real  
w = 8

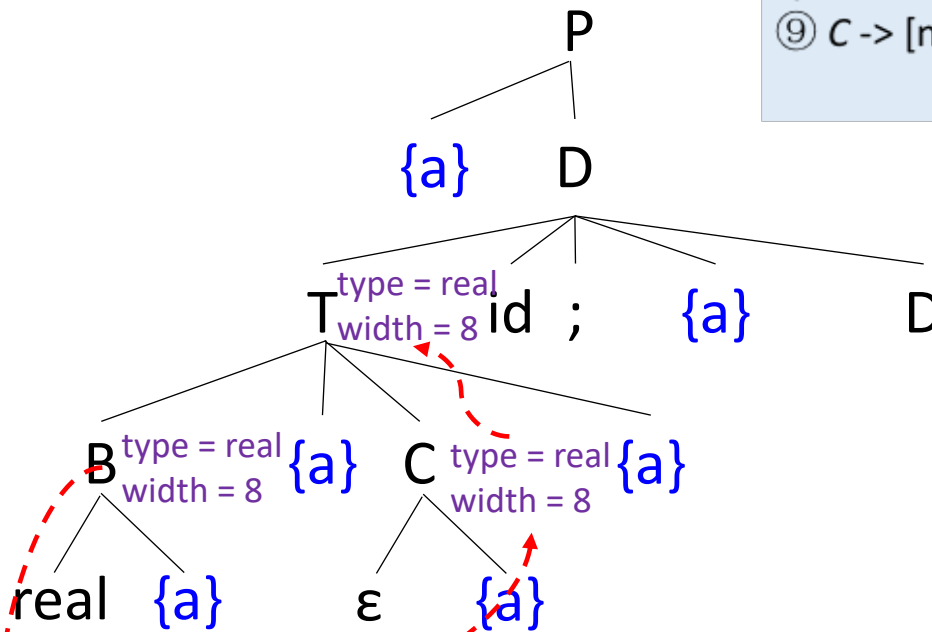


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



offset = 0

t = real  
w = 8

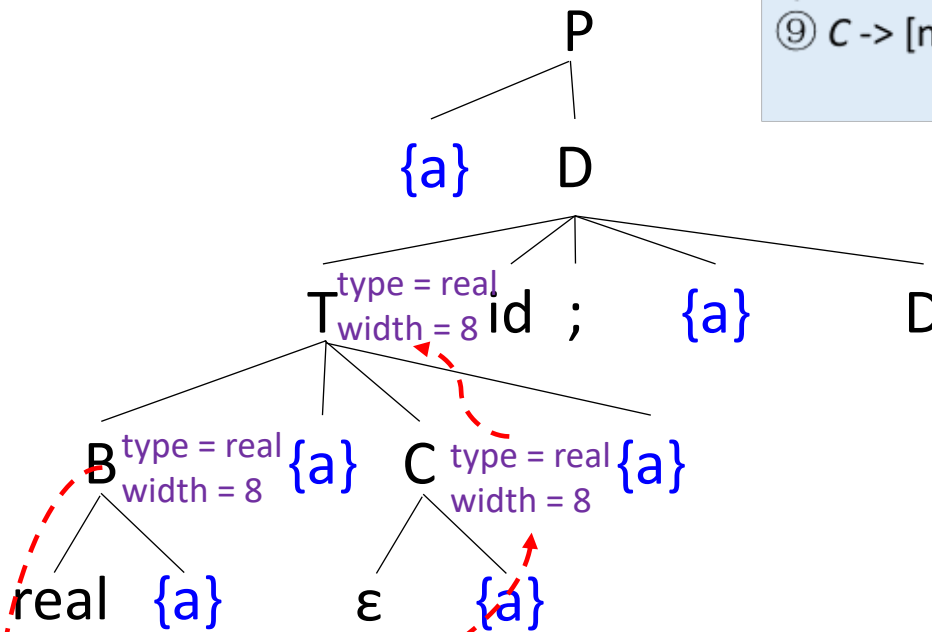


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



offset = 0

t = real  
w = 8

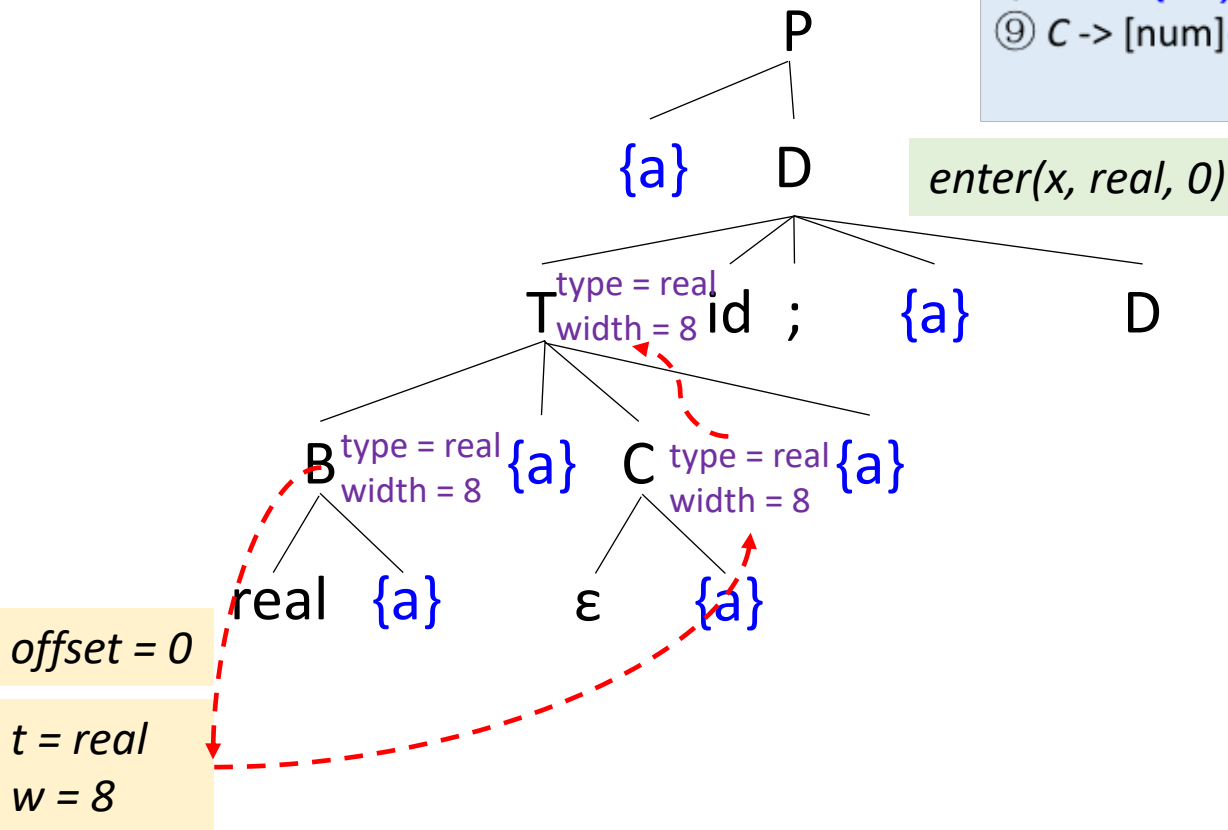


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}(\text{id.lexeme}, T.\text{type}, \text{offset}); \text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}(T_1.\text{type}); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}(\text{num.val}, C_1.\text{type}); C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

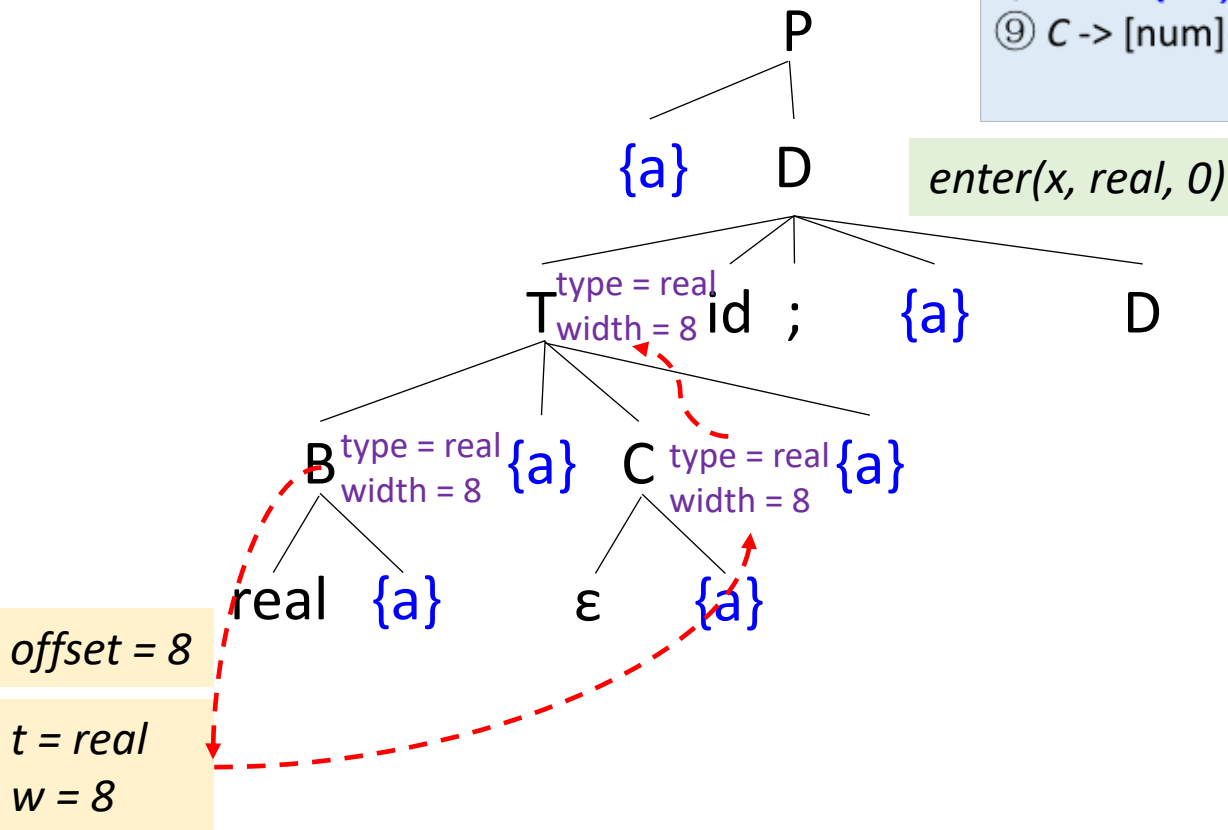


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$



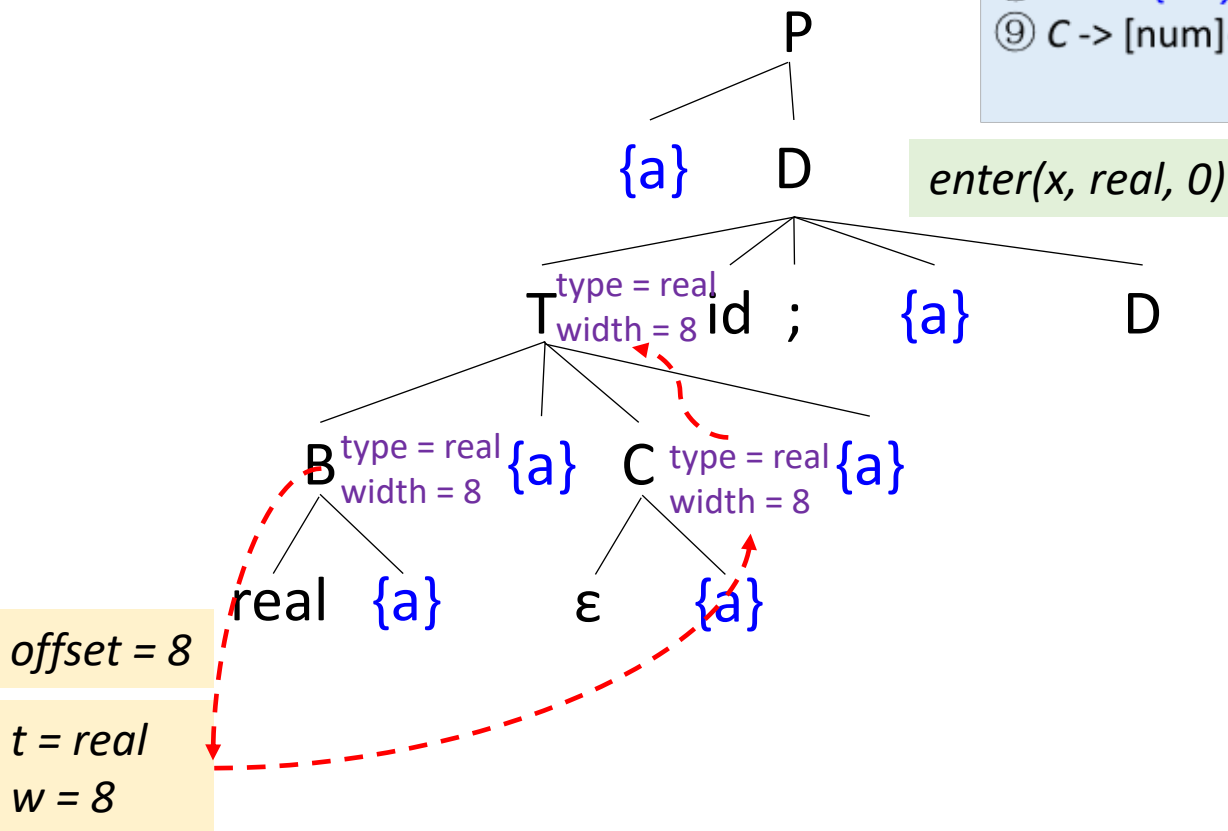


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} ); \text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $\quad C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} ); C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

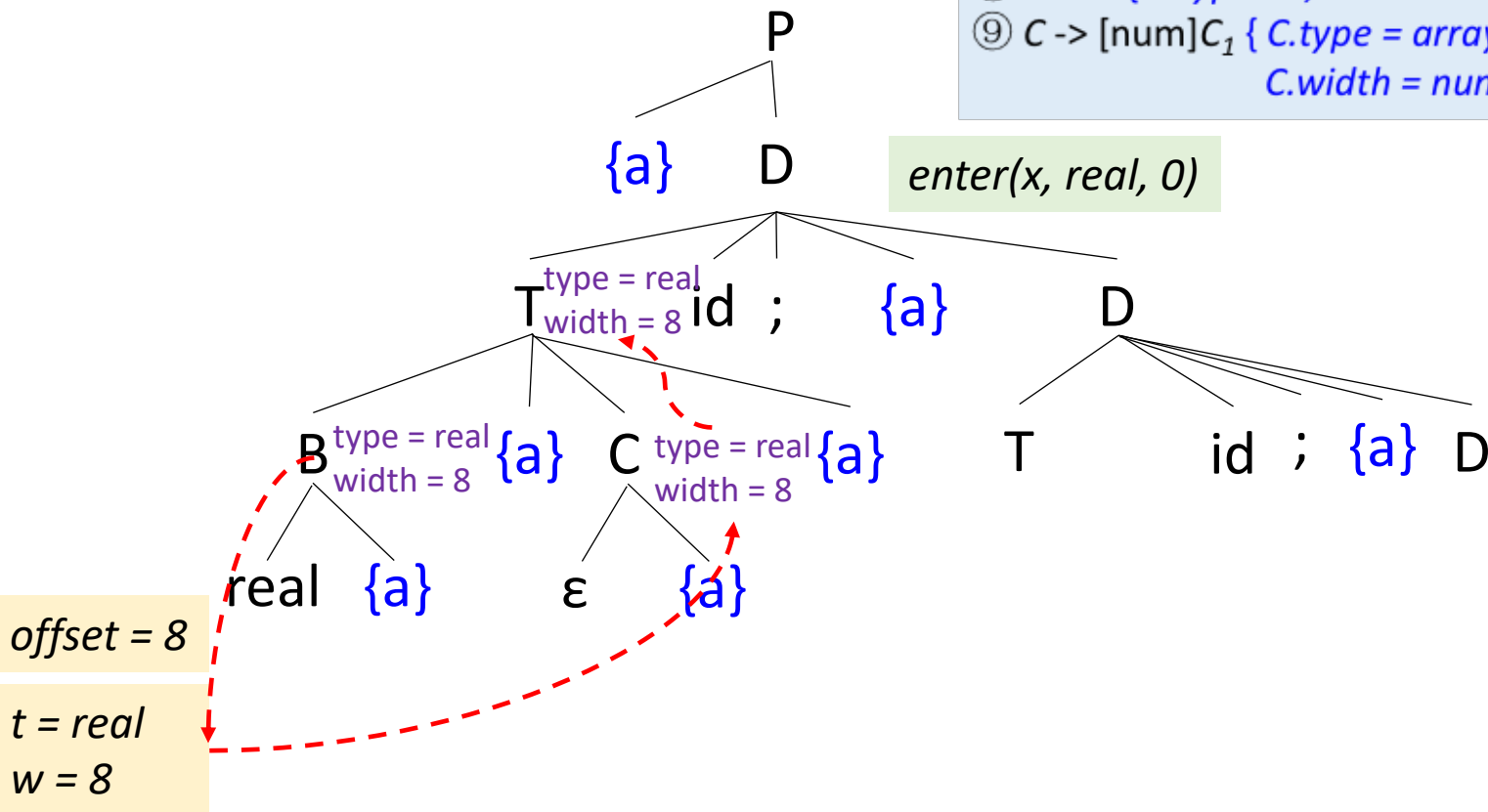


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

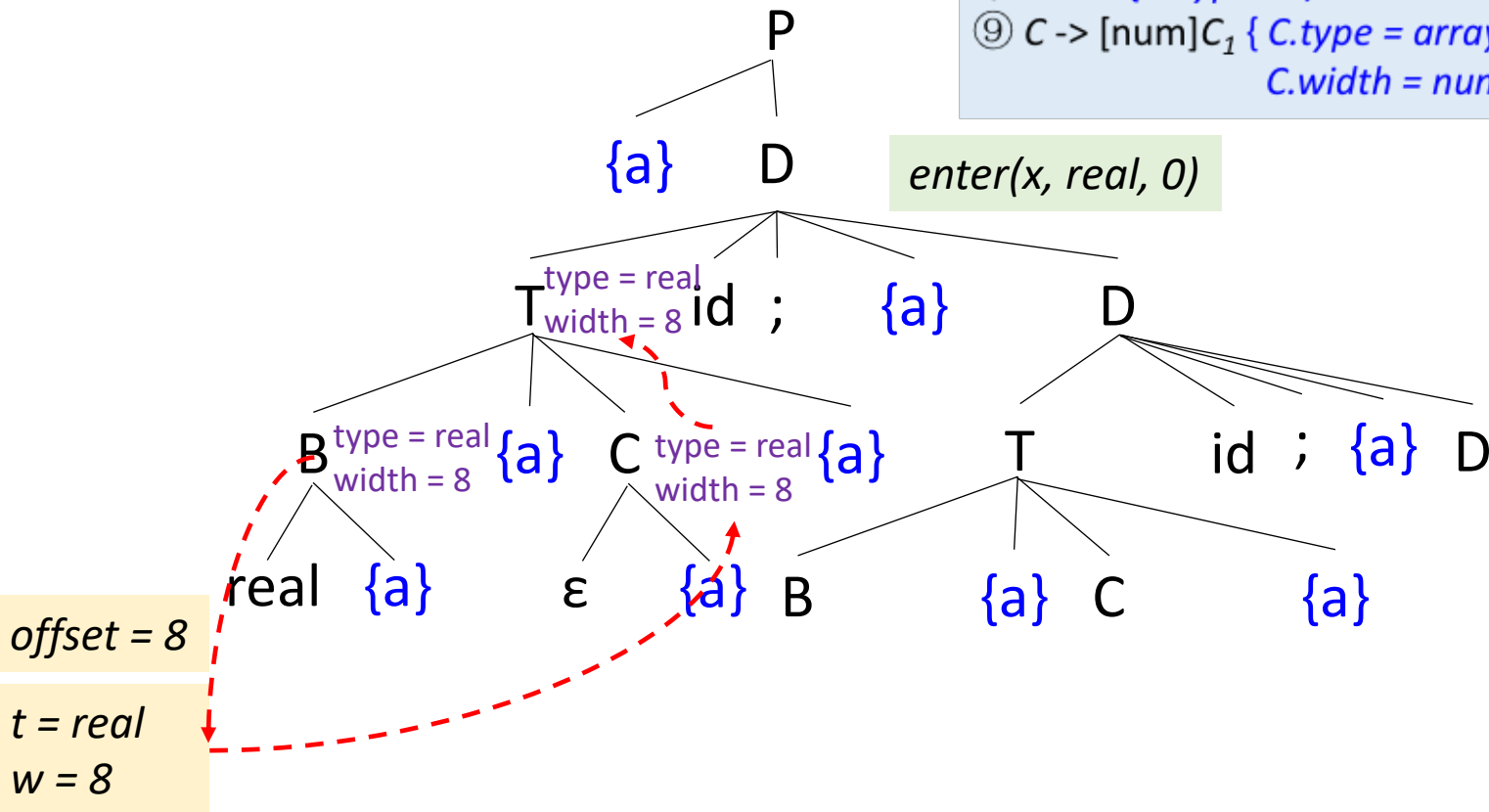


# Example

- Input: `real x; int i;`



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
- ④  $T \rightarrow B \{ t = B.\text{type}; w = B.\text{width}; \}$   
 $C \{ T.\text{type} = C.\text{type}; T.\text{width} = C.\text{width}; \}$
- ⑤  $T \rightarrow *T_1 \{ T.\text{type} = \text{pointer}( T_1.\text{type} ); T.\text{width} = 4; \}$
- ⑥  $B \rightarrow \text{int} \{ B.\text{type} = \text{int}; B.\text{width} = 4; \}$
- ⑦  $B \rightarrow \text{real} \{ B.\text{type} = \text{real}; B.\text{width} = 8; \}$
- ⑧  $C \rightarrow \epsilon \{ C.\text{type} = t; C.\text{width} = w; \}$
- ⑨  $C \rightarrow [\text{num}]C_1 \{ C.\text{type} = \text{array}( \text{num.val}, C_1.\text{type} );$   
 $C.\text{width} = \text{num.val} * C_1.\text{width}; \}$

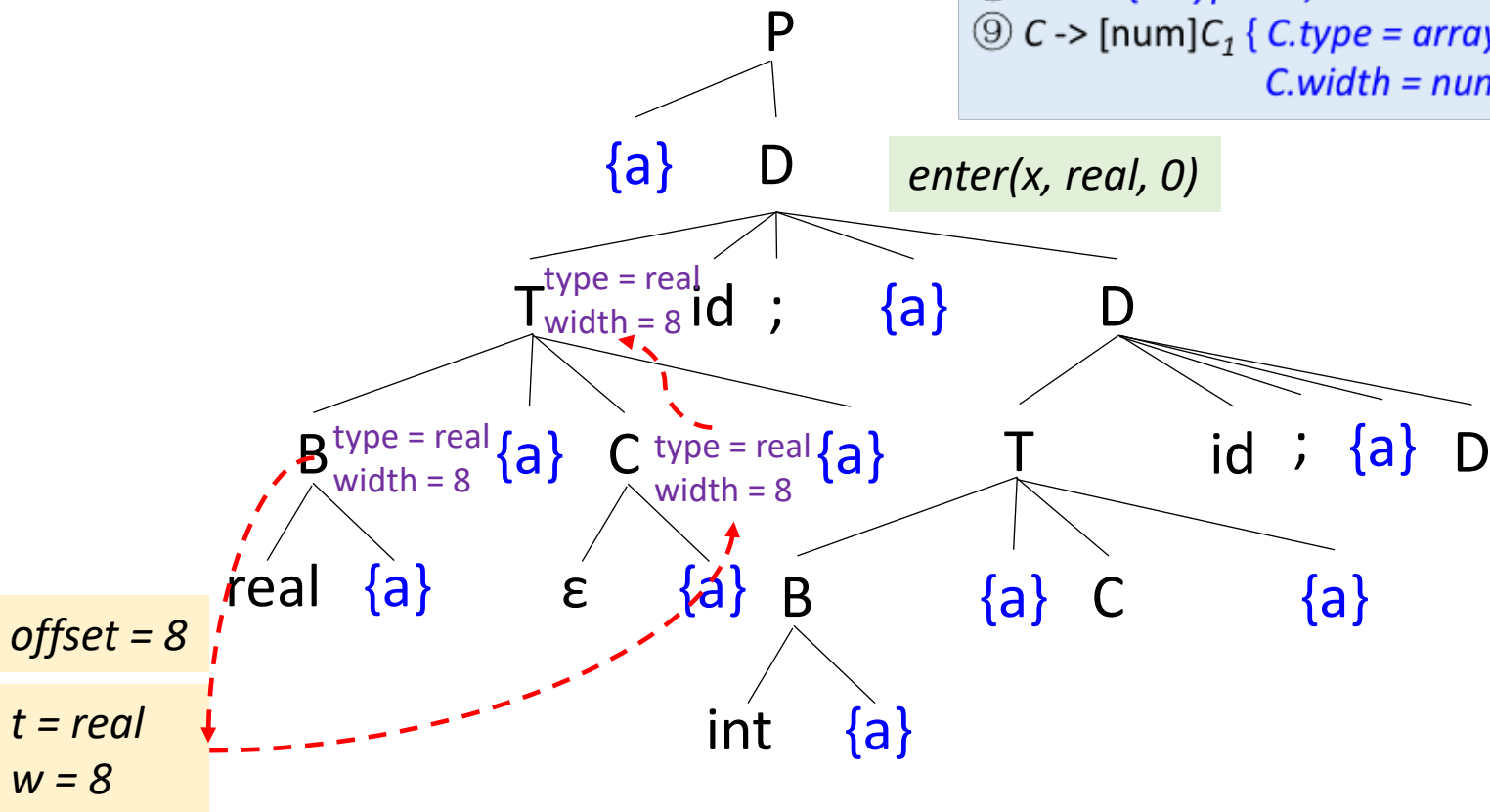


# Example


- Input: **real x; int i;**



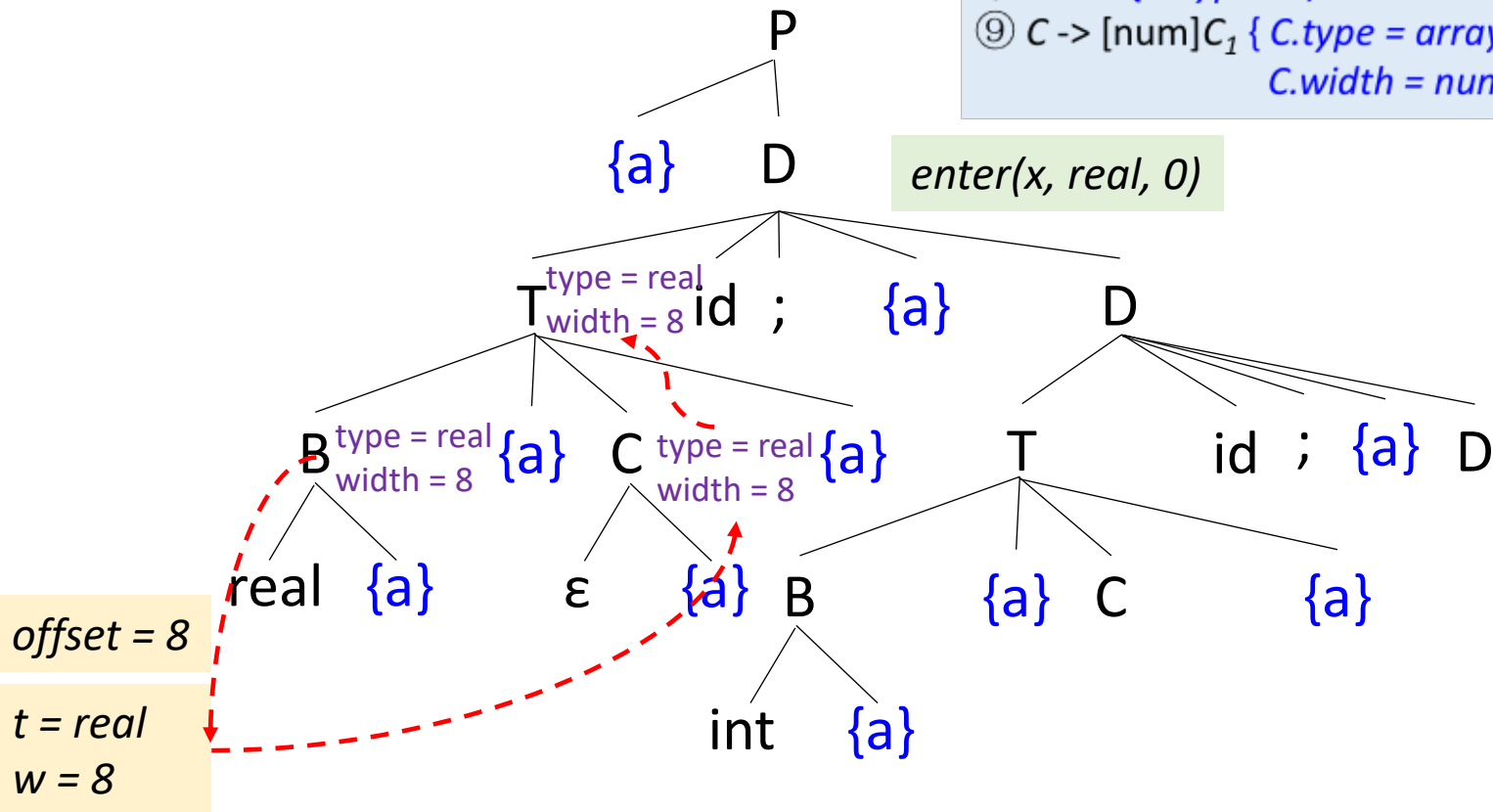
- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}(\text{id.lexeme}, T.\text{type}, \text{offset}); \text{offset} = \text{offset} + T.\text{width}; \} D_1$
- ③  $D \rightarrow \epsilon$
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# Example

- Input: **real x; int i;**  


- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}(\text{id.lexeme}, T.\text{type}, \text{offset}); \text{offset} = \text{offset} + T.\text{width}; \} D_1$
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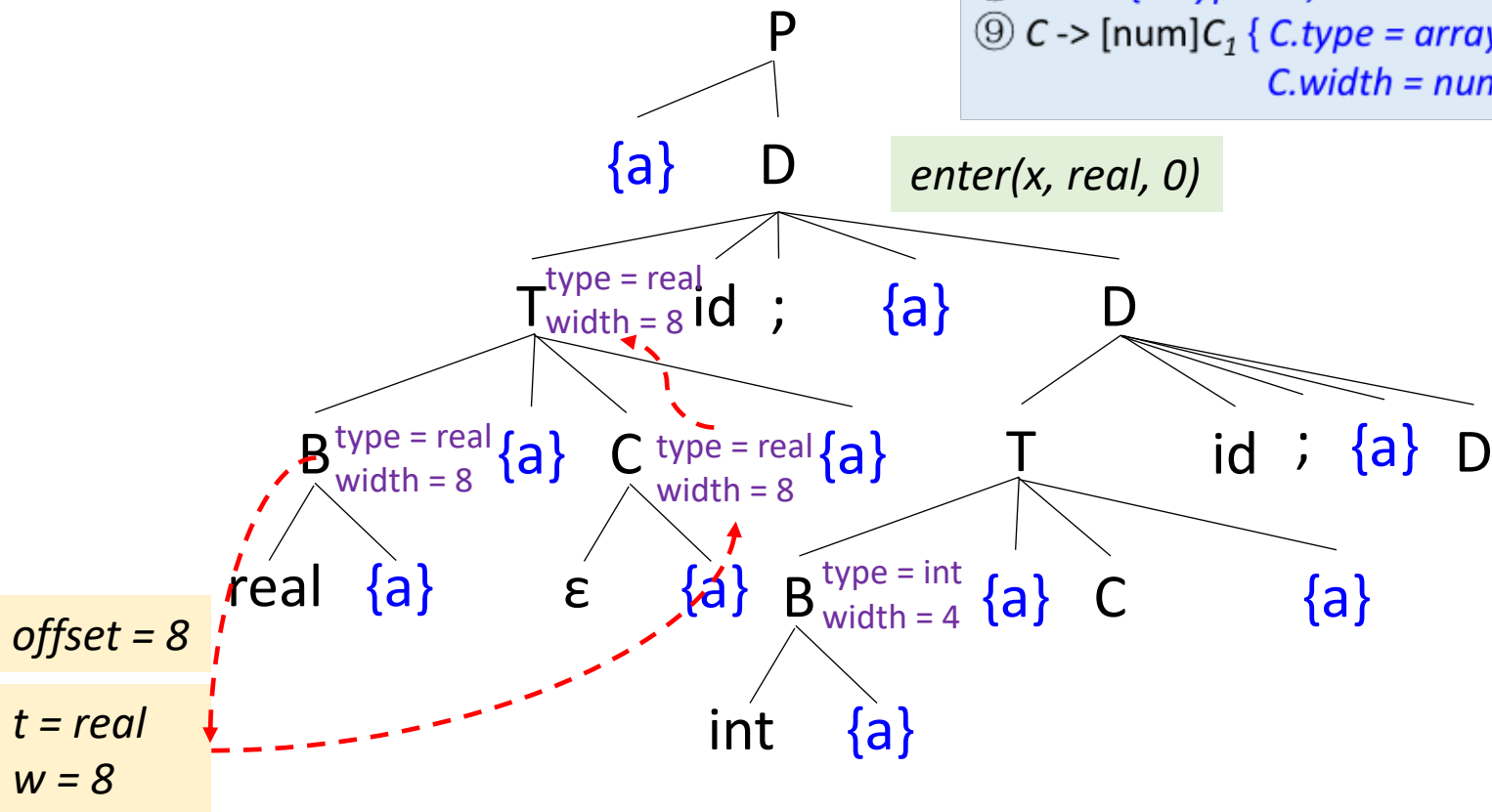


# Example

- Input: **real x; int i;**



- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
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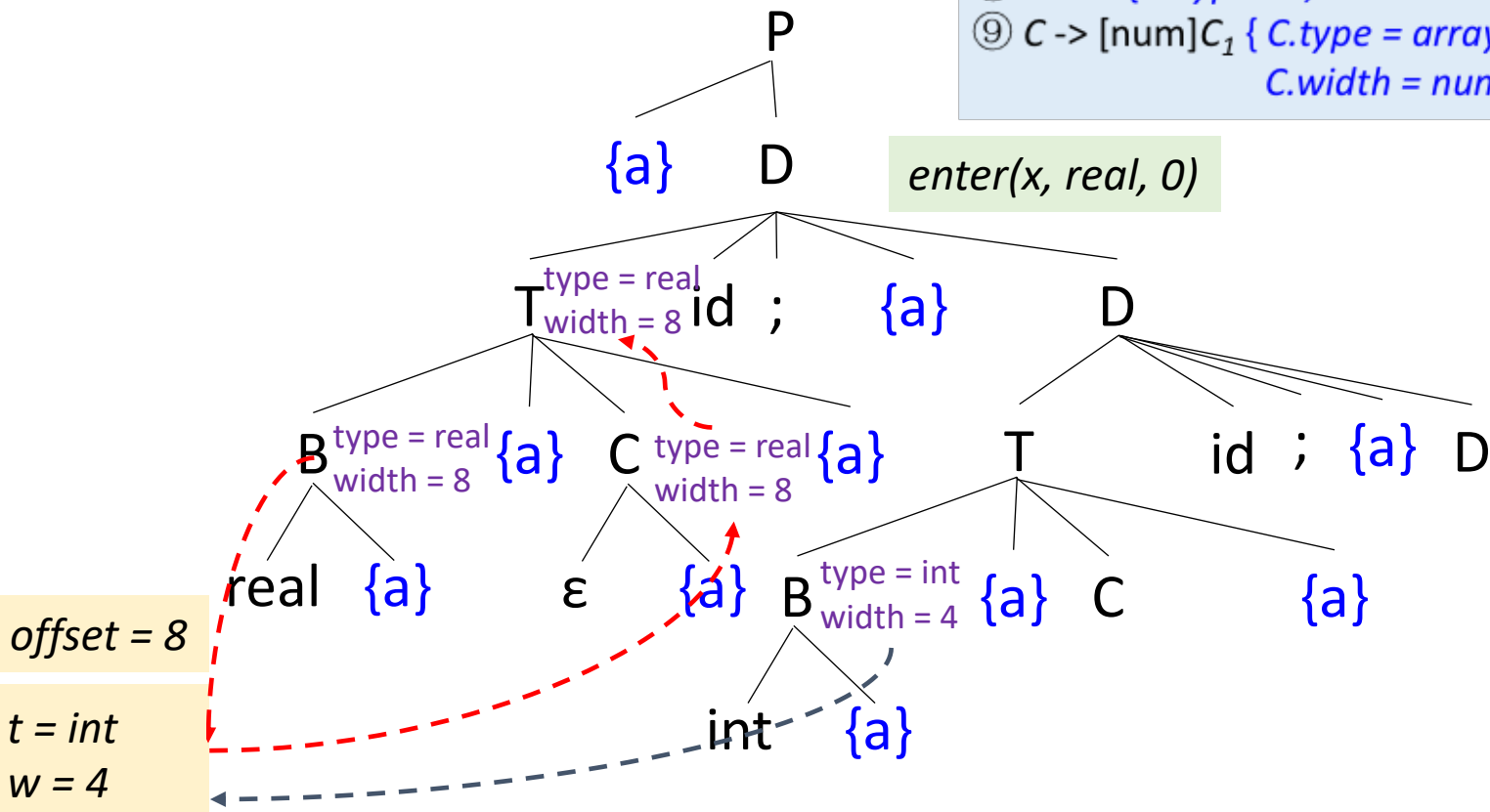


# Example

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- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}(\text{id.lexeme}, T.\text{type}, \text{offset}); \text{offset} = \text{offset} + T.\text{width}; \} D_1$
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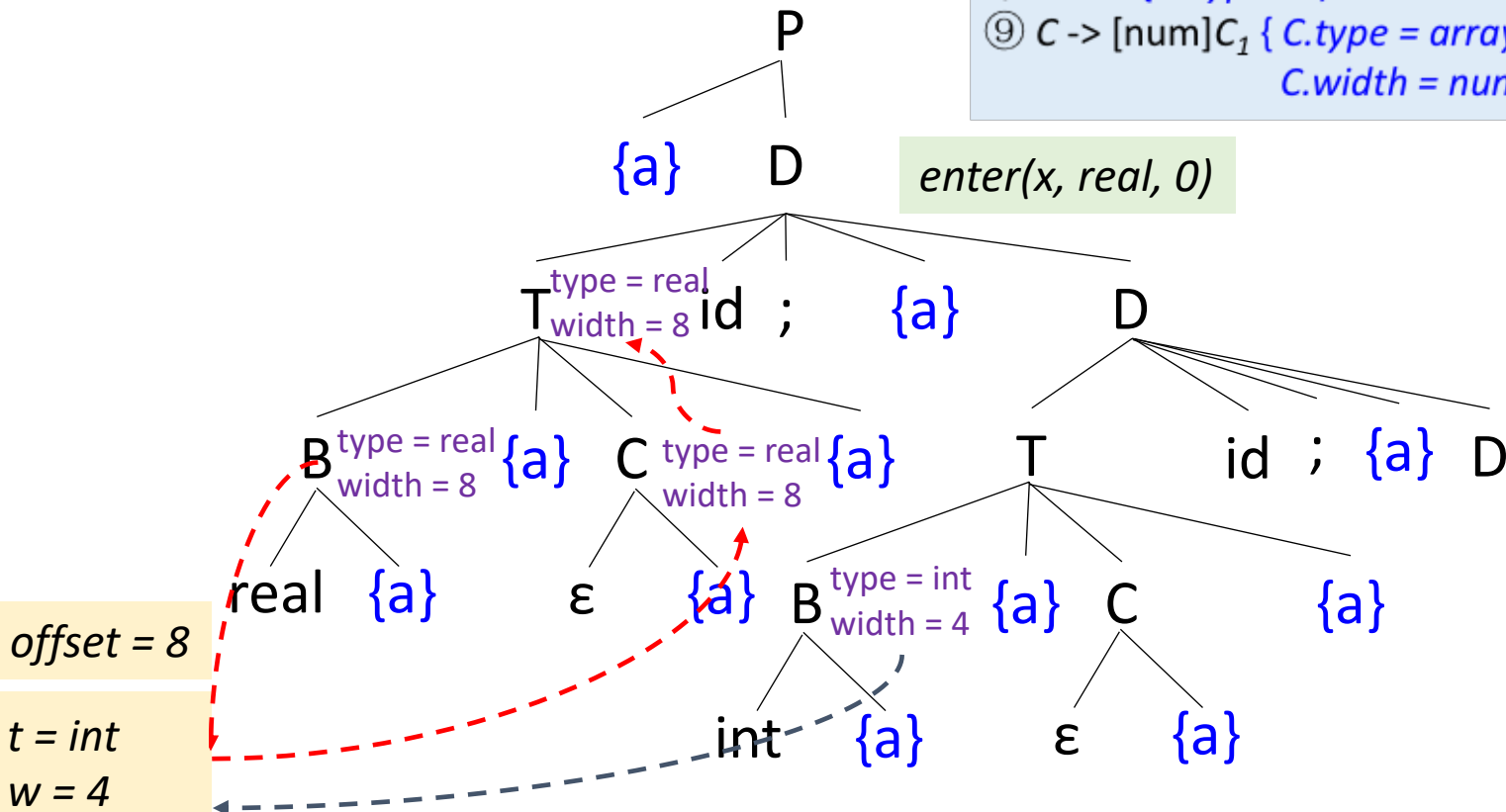


# Example

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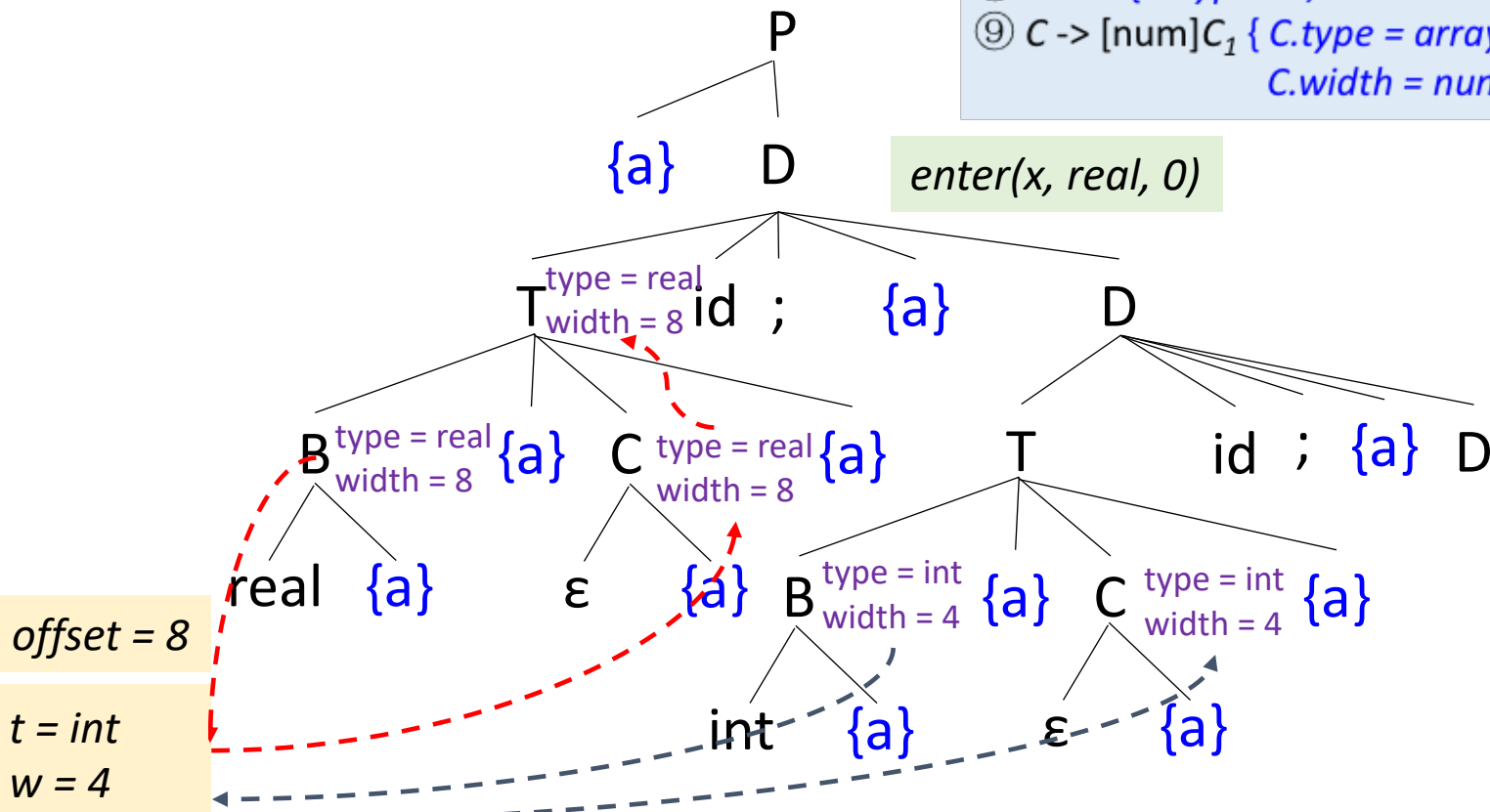


# Example

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- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
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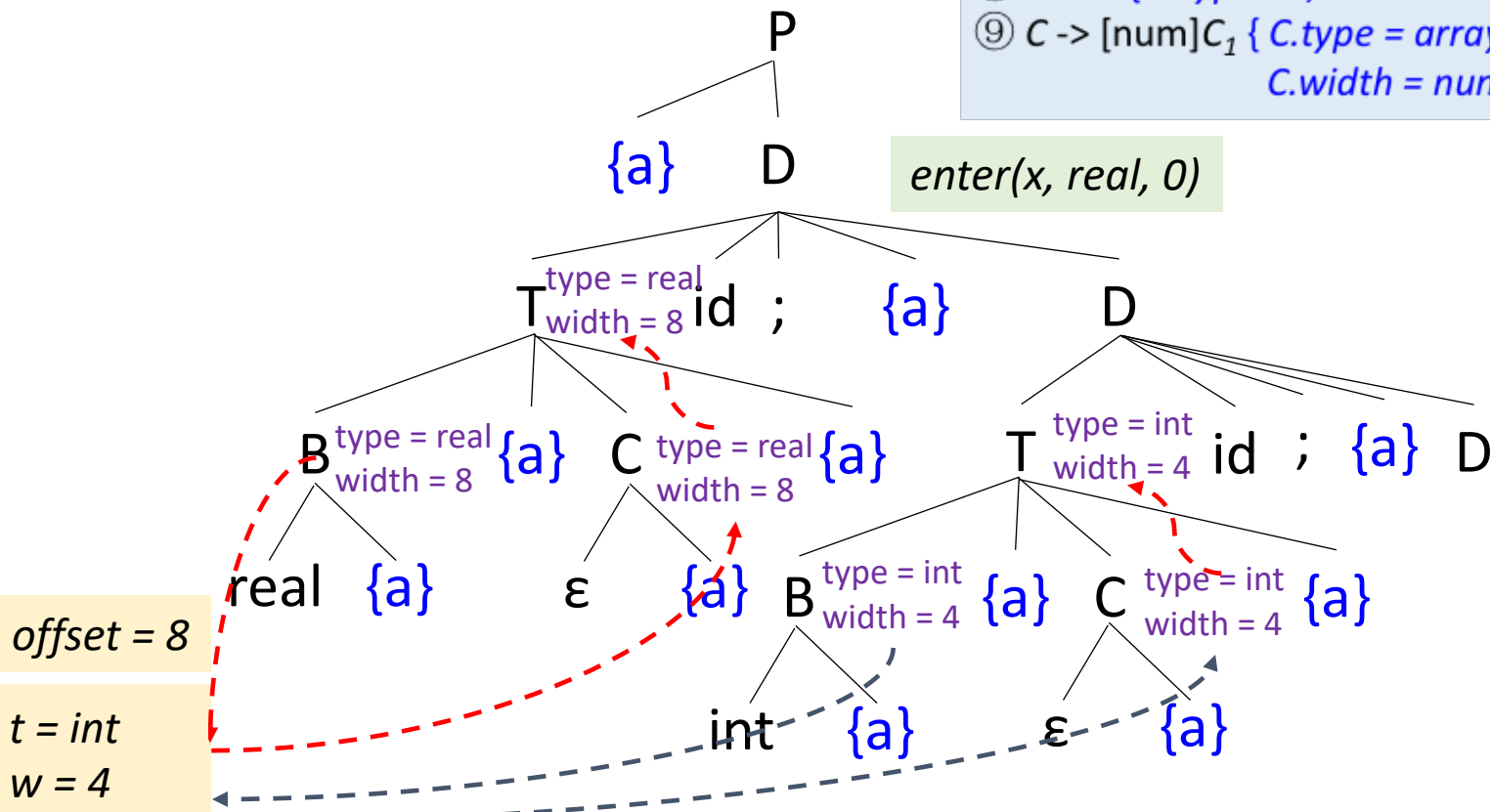


# Example

- Input: **real x; int i;**



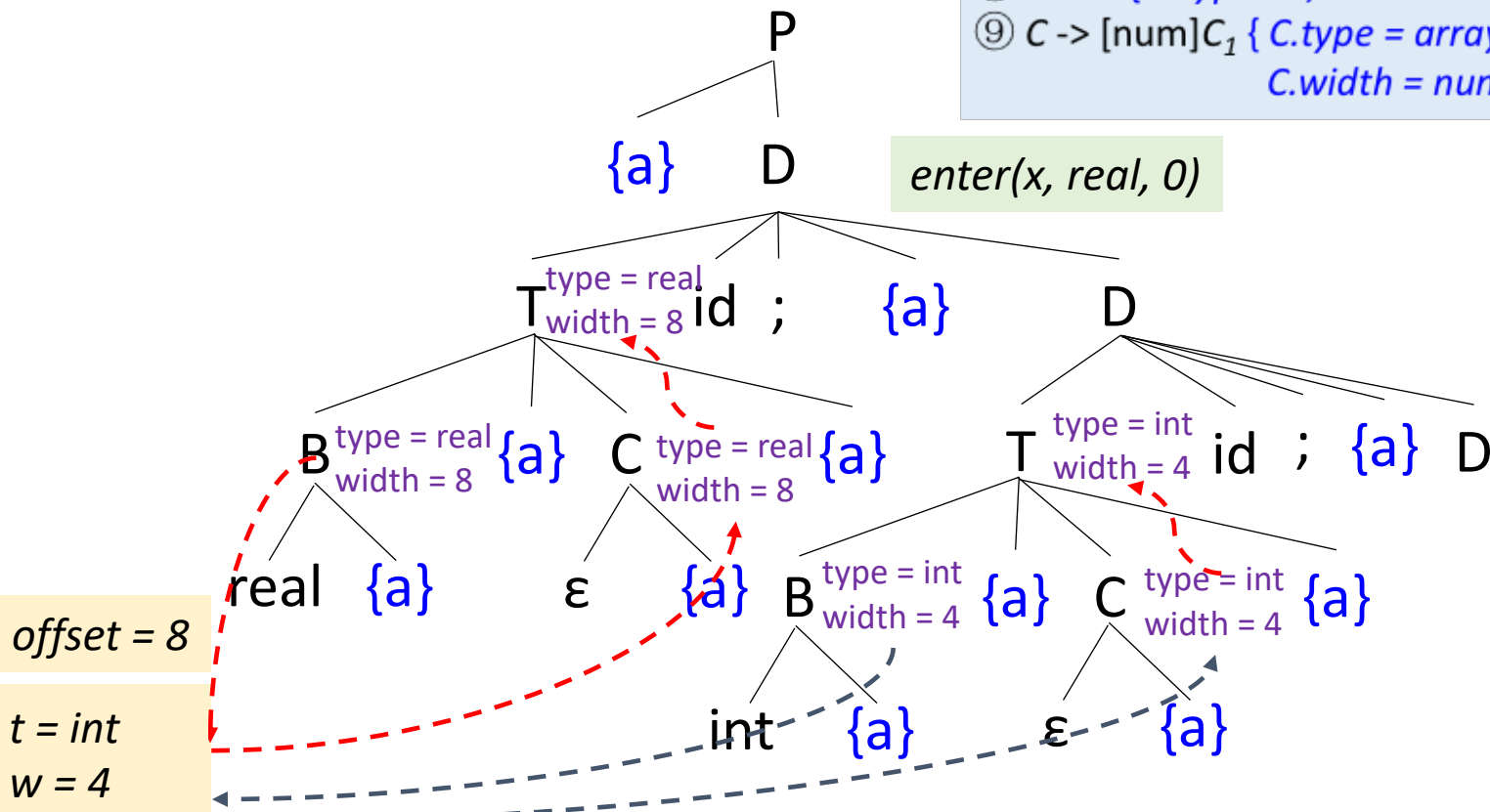
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# Example

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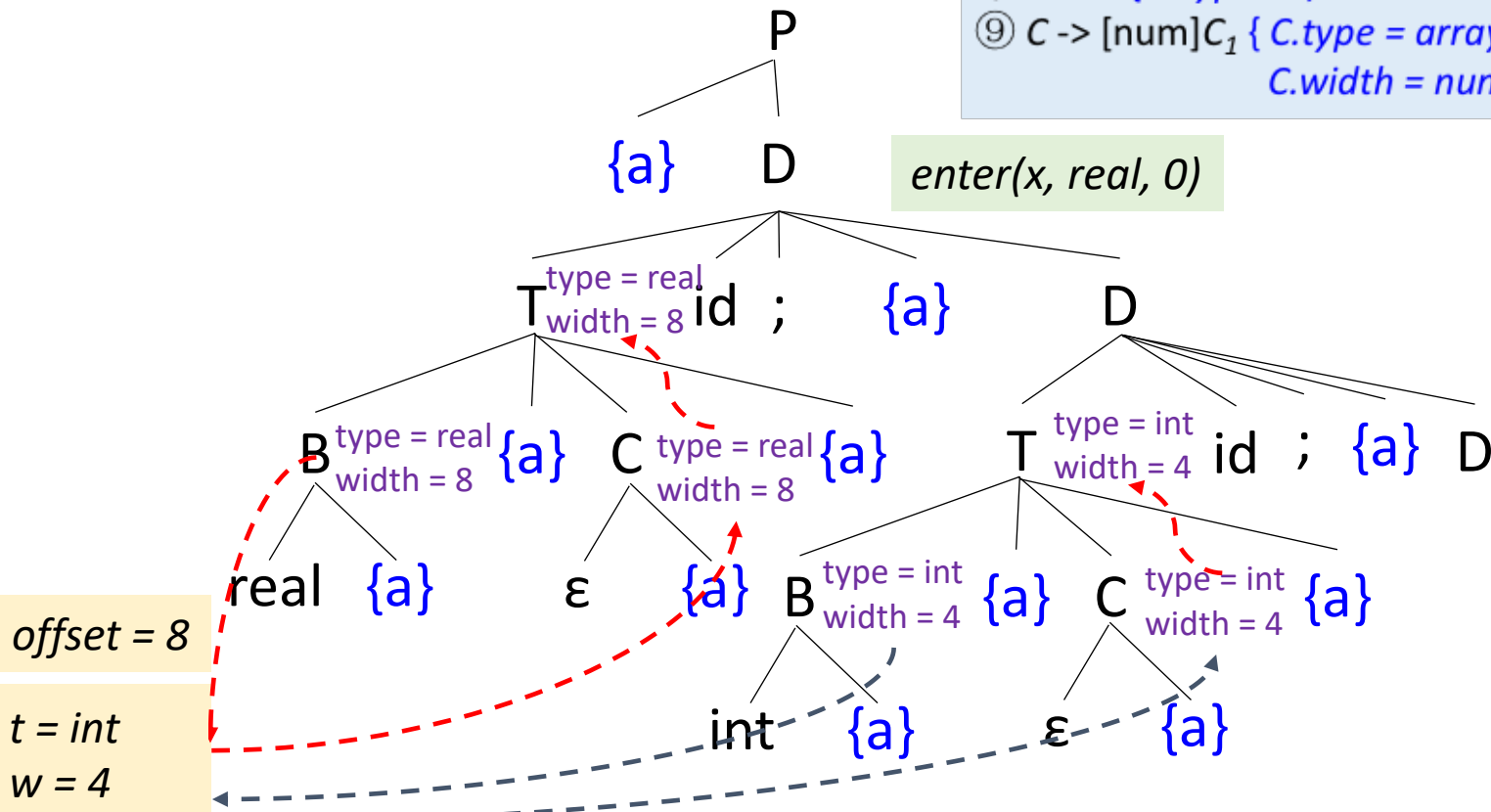
- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
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# Example

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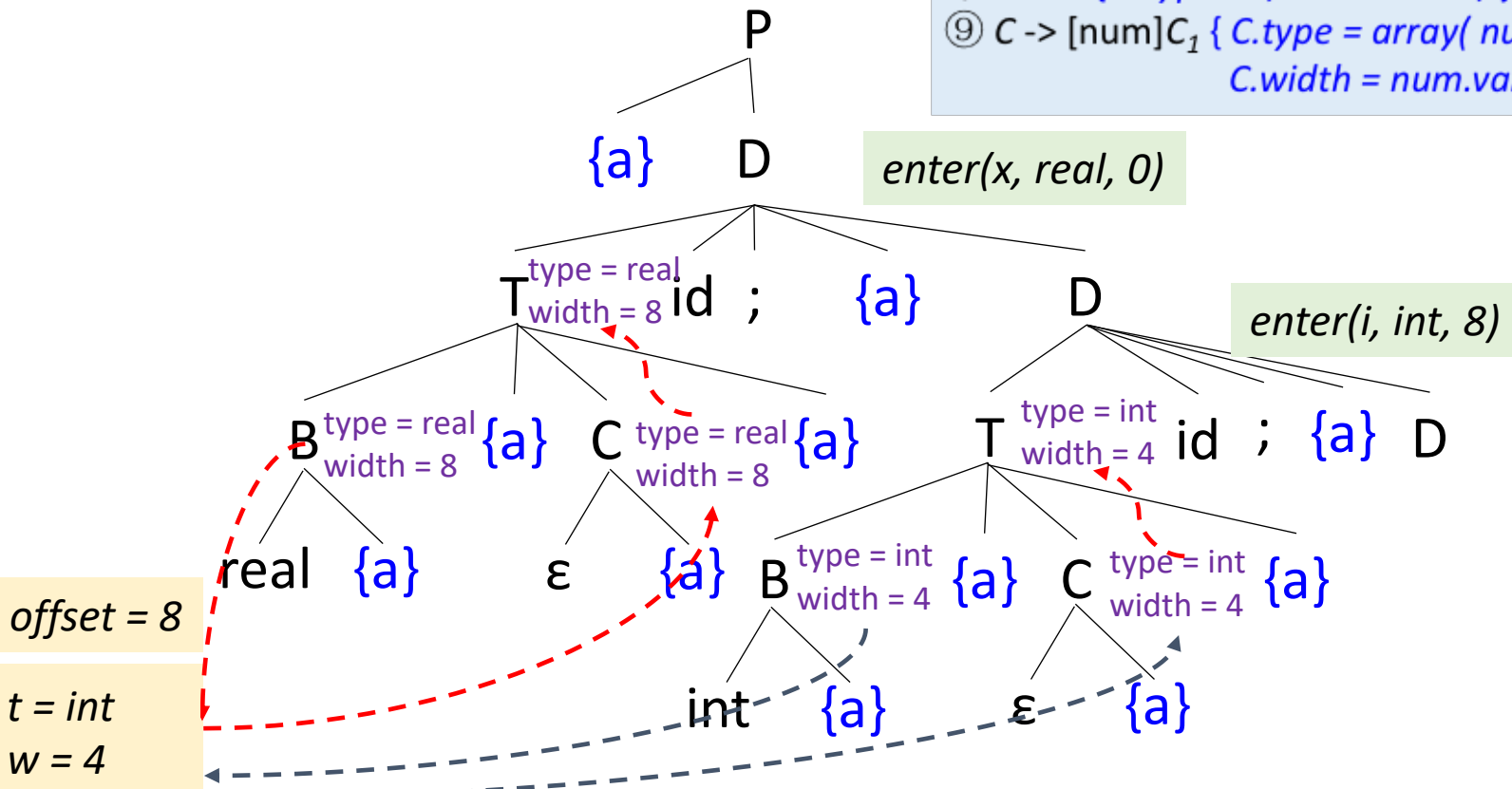
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# Example

- Input: `real x; int i;`  


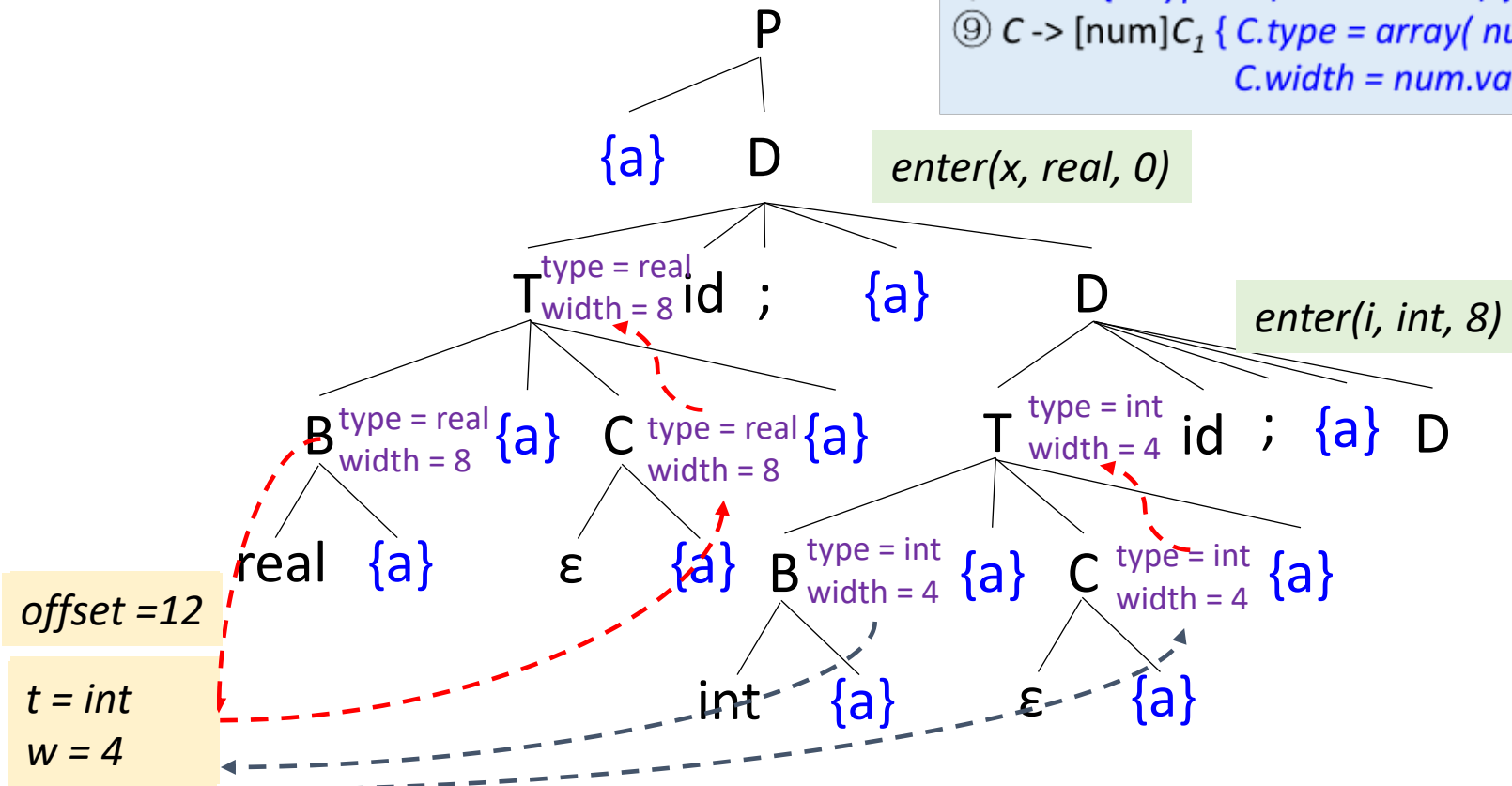
- ①  $P \rightarrow \{ \text{offset} = 0 \} D$
- ②  $D \rightarrow T \text{ id}; \{ \text{enter}( \text{id.lexeme}, T.\text{type}, \text{offset} );$   
 $\text{offset} = \text{offset} + T.\text{width}; \} D_1$
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# Example

- Input: **real x; int i;**  


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# Code Generation[代码生成]

---

- We will use the syntax-directed formalisms to specify translation
  - Variable definitions[变量定义]
  - Assignment[赋值]
  - Array references[数组引用]
  - Boolean expressions[布尔表达式]
  - Control-flow statements[控制流语句]
- To generate three-address codes (TACs)
  - Lay out variables in memory
  - Generate TAC for any subexpressions or substatements
  - Using the result, generate TAC for the overall expression

# CodeGen: Assignment Statement

- Translate into three-address code[赋值语句]
  - An expression with more than one operator will be translated into instructions with at most one operator per instruction
- Helper functions in translation
  - *lookup(id)*: search *id* in symbol table, return null if none
  - *emit()/gen()*: generate three-address IR
  - *newtemp()*: get a new temporary location

- ①  $S \rightarrow id = E;$
- ②  $E \rightarrow E_1 + E_2;$
- ③  $E \rightarrow - E_1$
- ④  $E \rightarrow (E_1)$
- ⑤  $E \rightarrow id$

Assignment statement:

$a = b + (-c)$

Three-address code:



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- ⑤  $E \rightarrow id$

Assignment statement:

$a = b + (-c)$

Three-address code:

$t_1 = \text{minus } c$

$t_2 = b + t_1$

$a = t_2$

# SDT Translation of Assignment

- Attributes **code** and **addr**

- *S.code* and *E.code* denote the TAC for *S* and *E*, respectively
- *E.addr* denotes the address that will hold the value of *E* (can be a name, constant, or a compiler-generated temporary)

- ①  $S \rightarrow id = E; \{ p = lookup(id.lexeme); \text{if } !p \text{ then error};$   
     $S.code = E.code \parallel$   
     $gen(p '=' E.addr); \}$
- ②  $E \rightarrow E_1 + E_2; \{ E.addr = newtemp();$   
     $E.code = E_1.code \parallel E_2.code \parallel$   
     $gen(E.addr '=' E_1.addr '+' E_2.addr); \}$
- ③  $E \rightarrow - E_1 \{ E.addr = newtemp();$   
     $E.code = E_1.code \parallel$   
     $gen(E.addr '=' 'minus' E_1.addr); \}$
- ④  $E \rightarrow (E_1) \{ E.addr = E_1.addr;$   
     $E.code = E_1.code; \}$
- ⑤  $E \rightarrow id \{ E.addr = lookup(id.lexeme); \text{if } !E.addr \text{ then error};$   
     $E.code = ''; \}$

# Incremental Translation[增量翻译]

- Generate only the new three-address instructions
  - *gen()* not only constructs a three-address inst, it appends the inst to the sequence of insts generated so far

- ①  $S \rightarrow id = E; \{ p = \text{lookup}(id.lexeme); \text{if } !p \text{ then error};$   
 $S.code = E.code \parallel$   
 $\text{gen}(p '=' E.addr); \}$
- ②  $E \rightarrow E_1 + E_2; \{ E.addr = \text{newtemp}();$   
 $E.code = E_1.code \parallel E_2.code \parallel$   
 $\text{gen}(E.addr '=' E_1.addr '+' E_2.addr); \}$
- ③  $E \rightarrow - E_1 \{ E.addr = \text{newtemp}();$   
 $E.code = E_1.code \parallel$   
 $\text{gen}(E.addr '=' \text{'minus'} E_1.addr); \}$
- ④  $E \rightarrow (E_1) \{ E.addr = E_1.addr;$   
 $E.code = E_1.code; \}$
- ⑤  $E \rightarrow id \{ E.addr = \text{lookup}(id.lexeme); \text{if } !E.addr \text{ then error};$   
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- ②  $E \rightarrow E_1 + E_2; \{ E.addr = \text{newtemp}();$   
 $E.code = E_1.code \parallel E_2.code \parallel$   
 $\text{gen}(E.addr '=' E_1.addr '+' E_2.addr); \}$
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Code attributes can  
be long strings

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①  $S \rightarrow id = E; \{ p = lookup(id.lexeme); \text{ if } !p \text{ then error};$

$\text{ gen}(p \text{ '=' } E.addr); \}$

②  $E \rightarrow E_1 + E_2; \{ E.addr = newtemp();$

$\text{ gen}(E.addr \text{ '=' } E_1.addr \text{ '+' } E_2.addr); \}$

③  $E \rightarrow - E_1 \{ E.addr = newtemp();$

$\text{ gen}(E.addr \text{ '=' 'minus' } E_1.addr); \}$

④  $E \rightarrow (E_1) \{ E.addr = E_1.addr;$   
 $\}$

⑤  $E \rightarrow id \{ E.addr = lookup(id.lexeme); \text{ if } !E.addr \text{ then error};$   
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Code attributes can  
be long strings

# Example

- ①  $S \rightarrow id = E; \{ p = \text{lookup}(id.\text{lexeme}); \text{if } !p \text{ then error; } \\ \text{gen}(p = 'E.\text{addr}'); \}$
- ②  $E \rightarrow E_1 + E_2; \{ E.\text{addr} = \text{newtemp}(); \\ \text{gen}(E.\text{addr} = 'E_1.\text{addr} + ' E_2.\text{addr}); \}$
- ③  $E \rightarrow - E_1 \{ E.\text{addr} = \text{newtemp}(); \\ \text{gen}(E.\text{addr} = 'minus' E_1.\text{addr}); \}$
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id	=	(	id
$x$			$a$

+	b	)	+	c
---	---	---	---	---

- Input

$$x = (a + b) + c$$

# Example

- ①  $S \rightarrow id = E; \{ p = \text{lookup}(id.\text{lexeme}); \text{if } !p \text{ then error; } \text{gen}(p := E.\text{addr}); \}$
- ②  $E \rightarrow E_1 + E_2; \{ E.\text{addr} = \text{newtemp}(); \text{gen}(E.\text{addr} := E_1.\text{addr} + E_2.\text{addr}); \}$
- ③  $E \rightarrow - E_1 \{ E.\text{addr} = \text{newtemp}(); \text{gen}(E.\text{addr} := \text{'minus'} E_1.\text{addr}); \}$
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R5

id	=	(	id
x			a

id	=	(	E
x			a

+	b	)	+	c
---	---	---	---	---

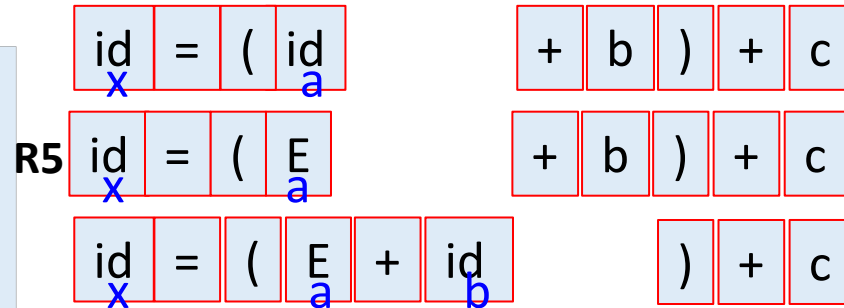
+	b	)	+	c
---	---	---	---	---

- Input

$$x = (a + b) + c$$

# Example

- ①  $S \rightarrow id = E; \{ p = \text{lookup}(id.\text{lexeme}); \text{if } !p \text{ then error; } \text{gen}(p := E.\text{addr}); \}$
- ②  $E \rightarrow E_1 + E_2; \{ E.\text{addr} = \text{newtemp}(); \text{gen}(E.\text{addr} := E_1.\text{addr} + E_2.\text{addr}); \}$
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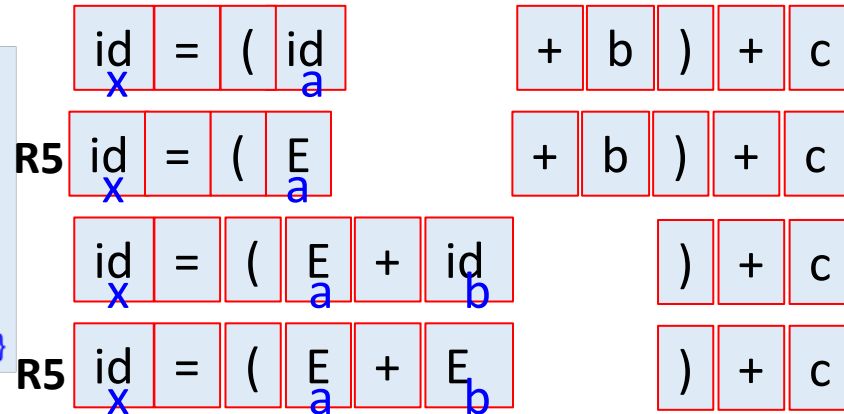
- Input

$$x = (a + b) + c$$



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## • Input

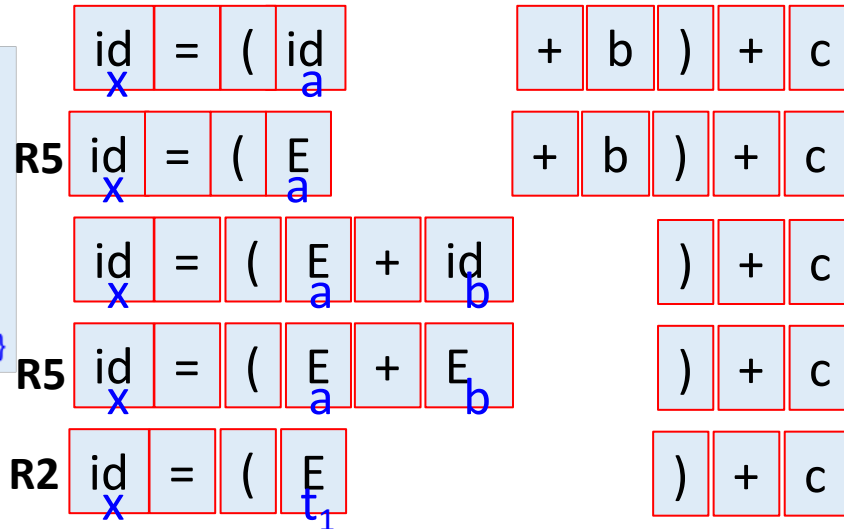
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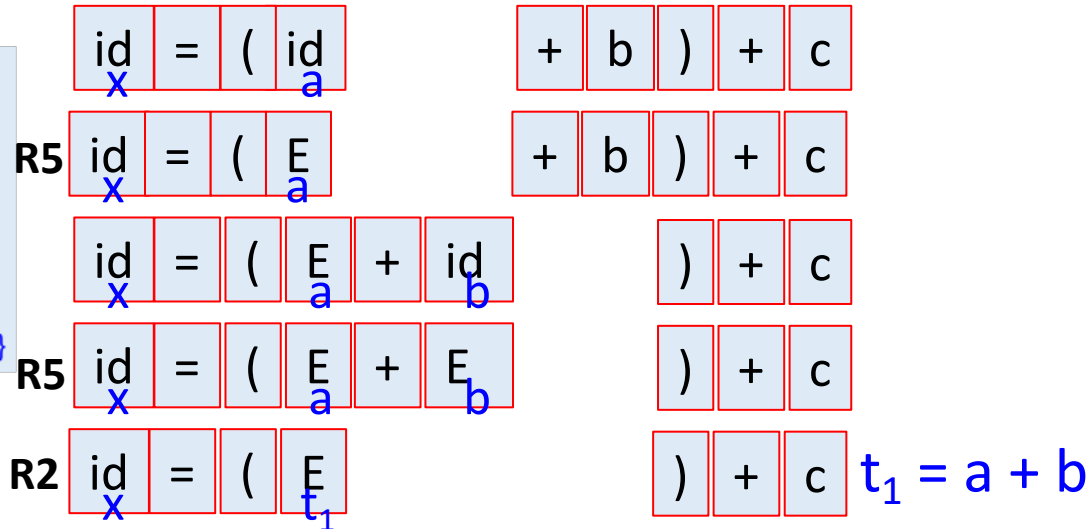


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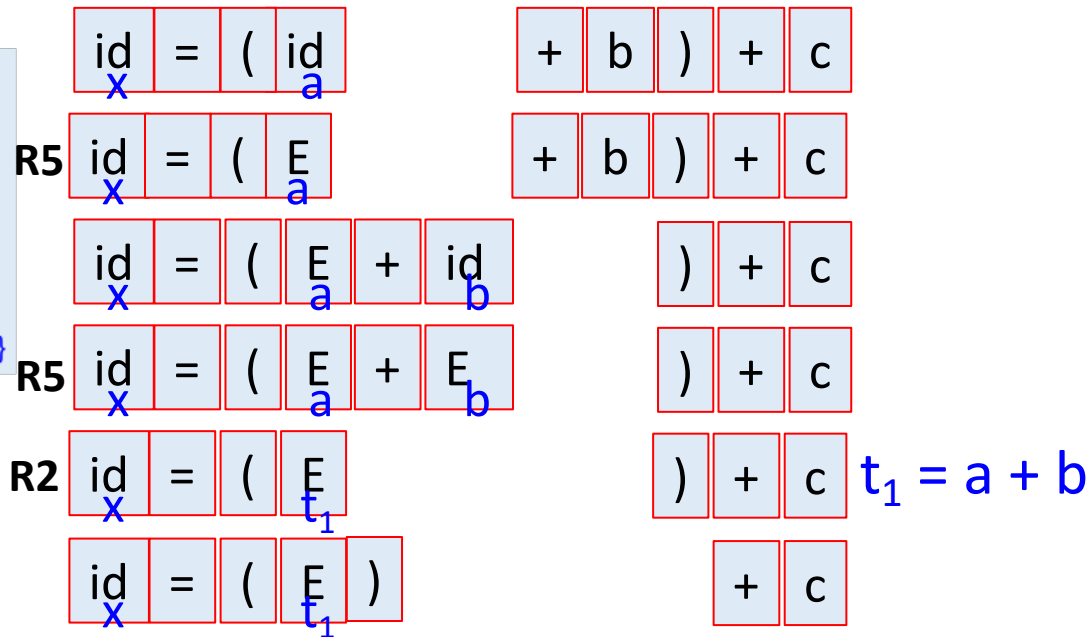


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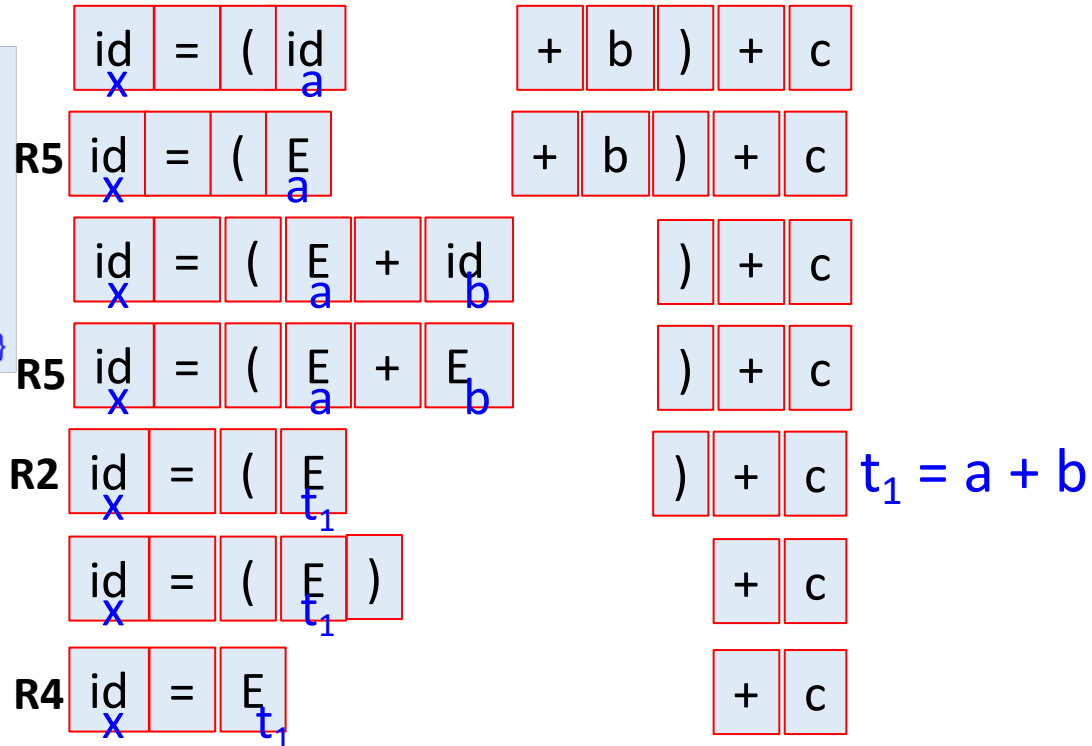


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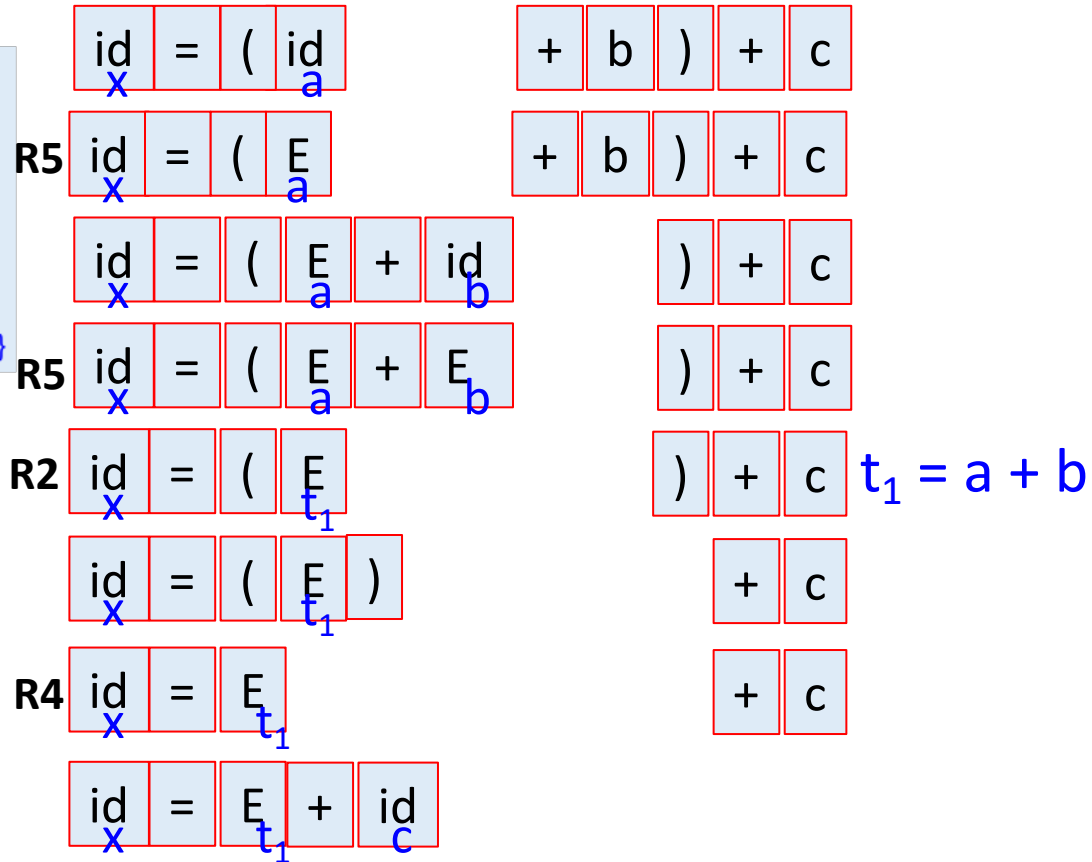


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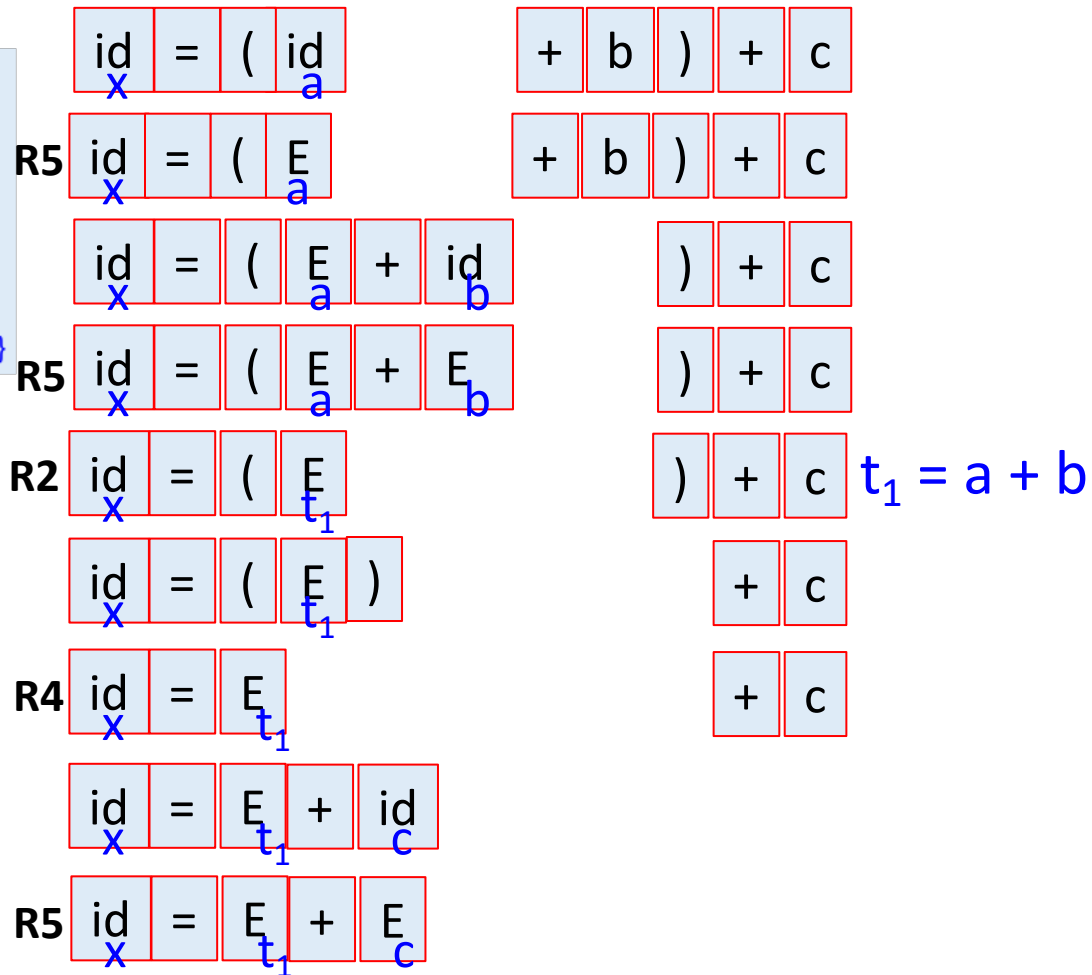


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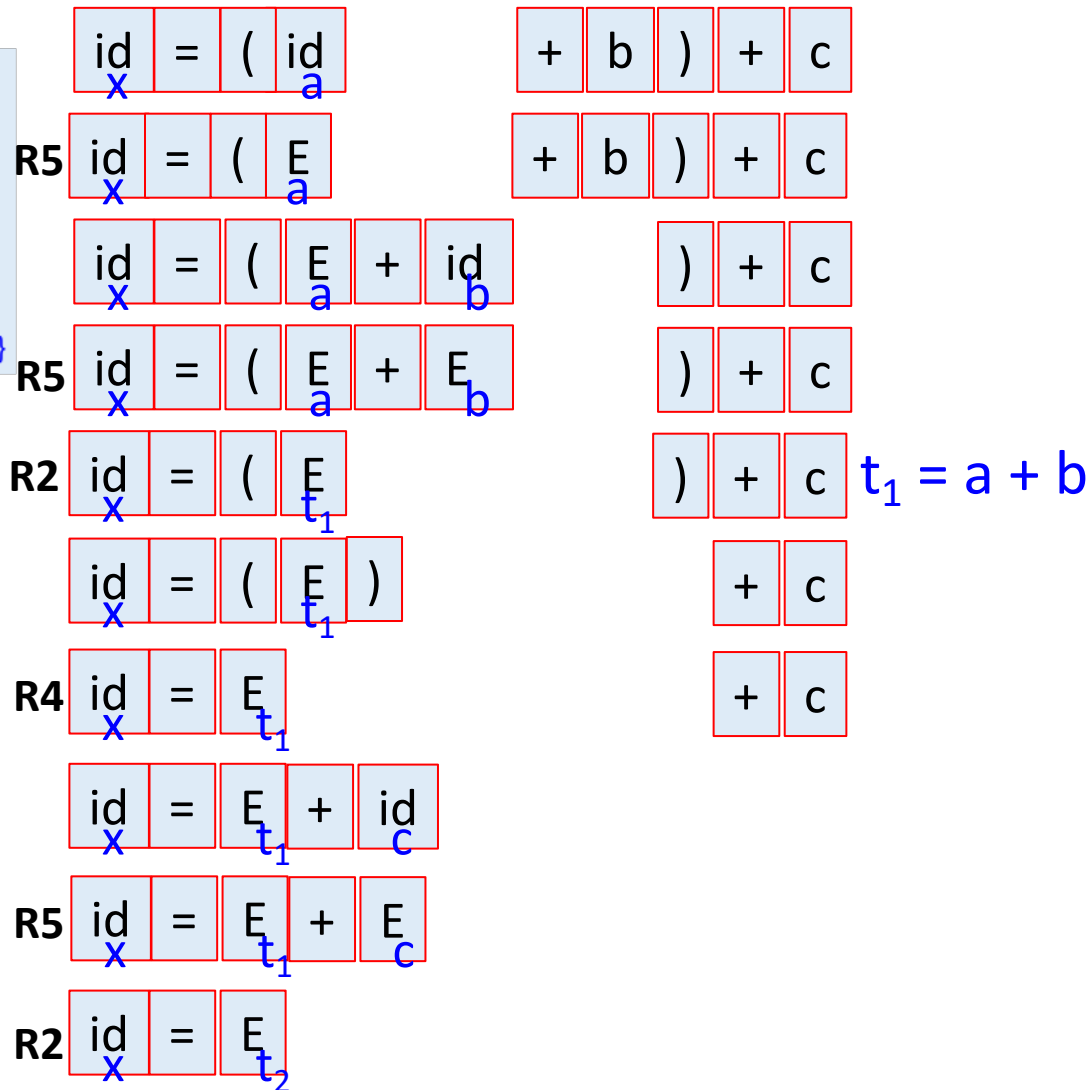


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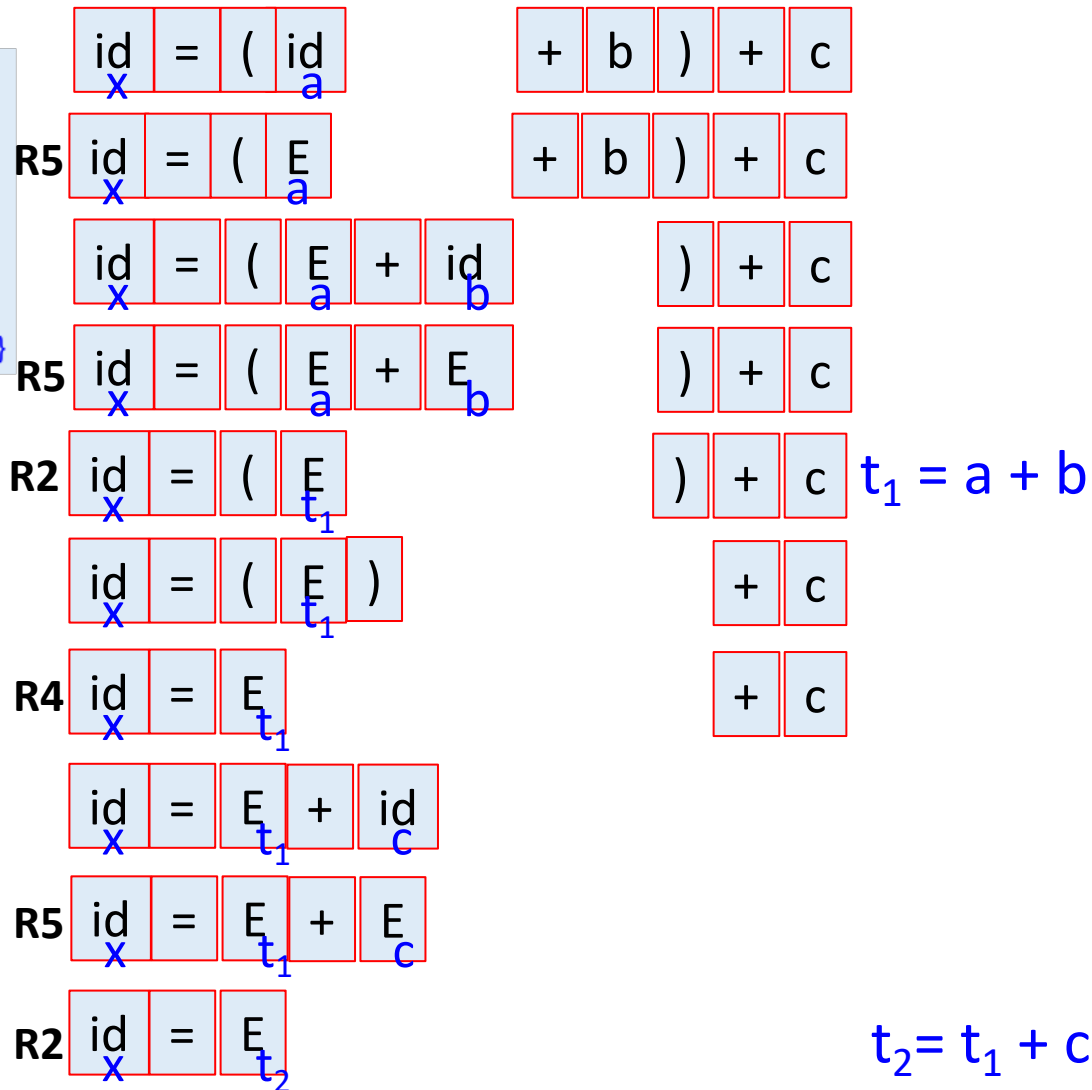


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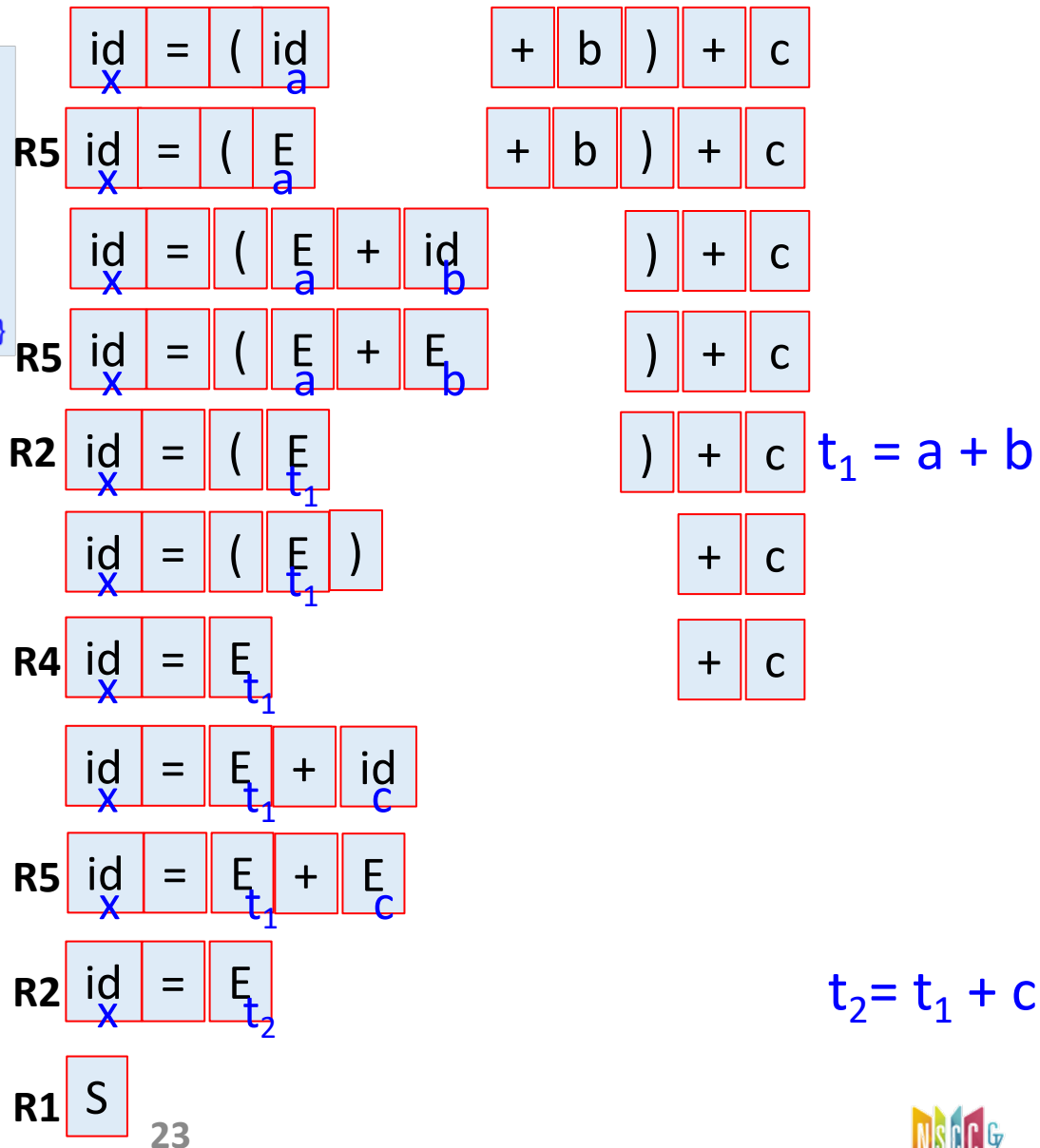


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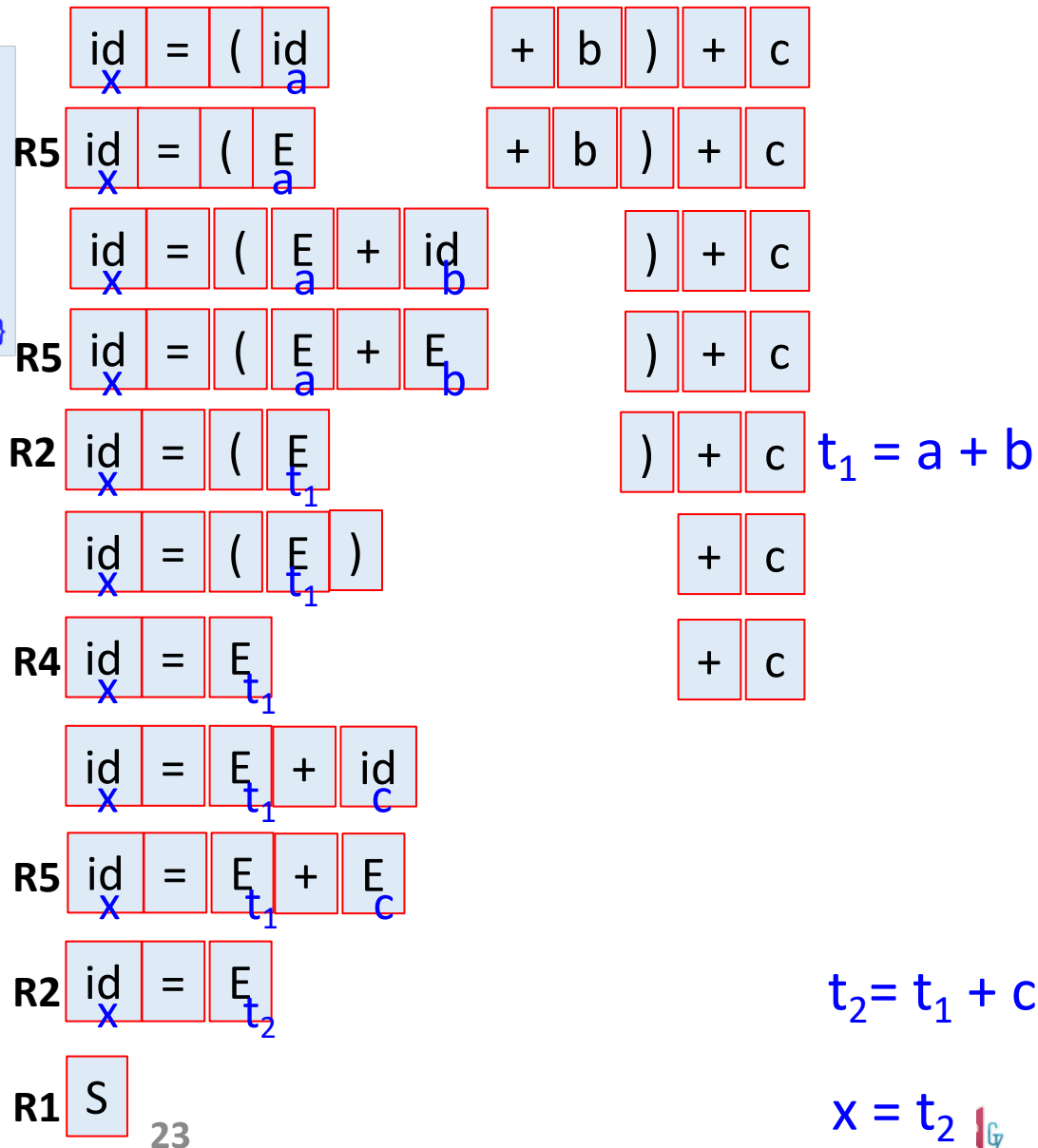


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# Example

- ①  $S \rightarrow id = E; \{ p = \text{lookup}(id.\text{lexeme}); \text{if } !p \text{ then error;}$   
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 $\text{gen}(E.\text{addr} \text{ '=' } E_1.\text{addr} \text{ '+' } E_2.\text{addr}); \}$
- ③  $E \rightarrow - E_1 \{ E.\text{addr} = \text{newtemp}();$   
 $\text{gen}(E.\text{addr} \text{ '=' 'minus' } E_1.\text{addr}); \}$
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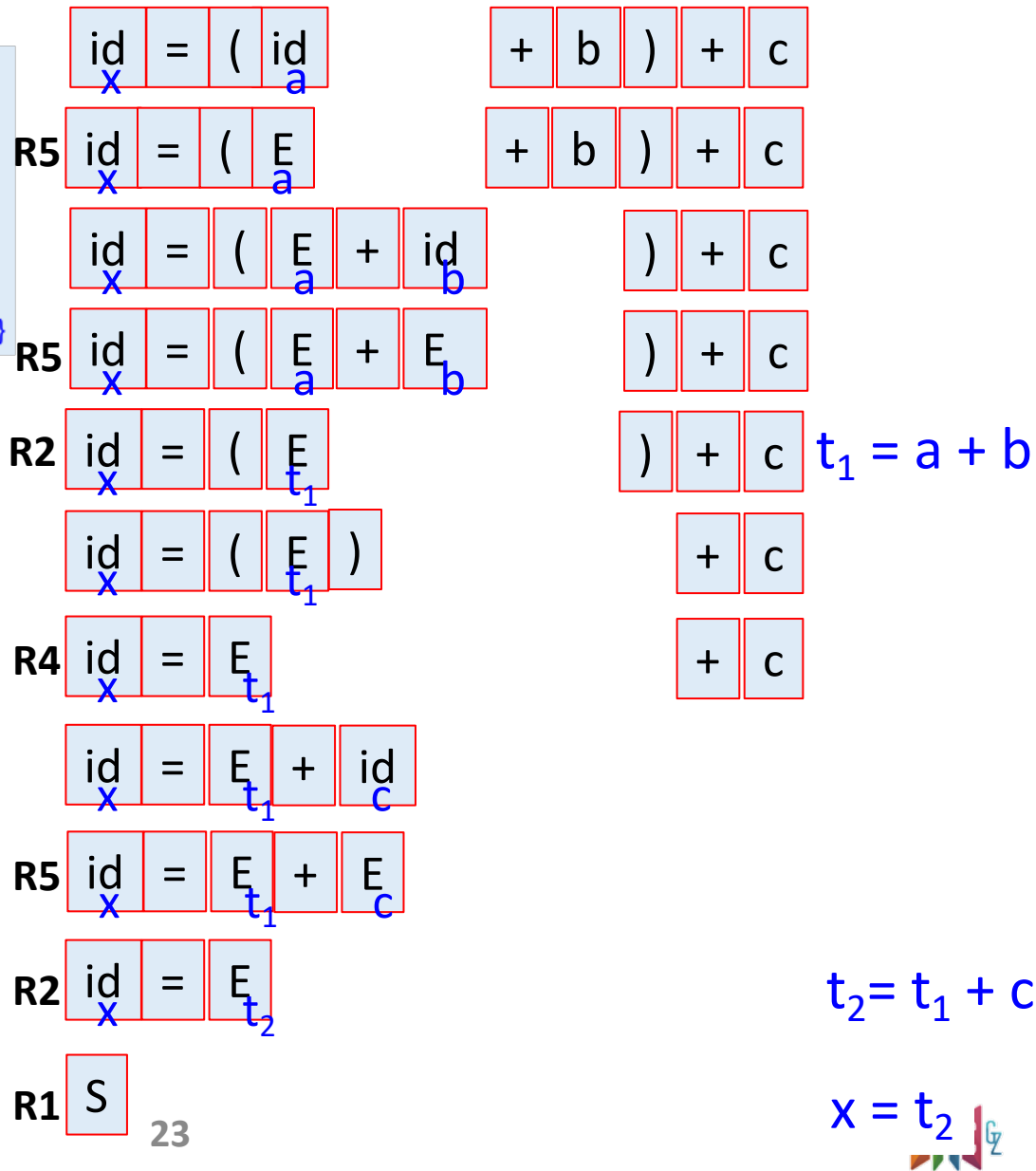


- Translated TAC

$$t_1 = a + b$$

$$t_2 = t_1 + c$$

$$x = t_2$$



# CodeGen: Array Reference[数组引用]

- Primary problem in generating code for array references is to determine the address of element

- 1D array

*int A[N];*

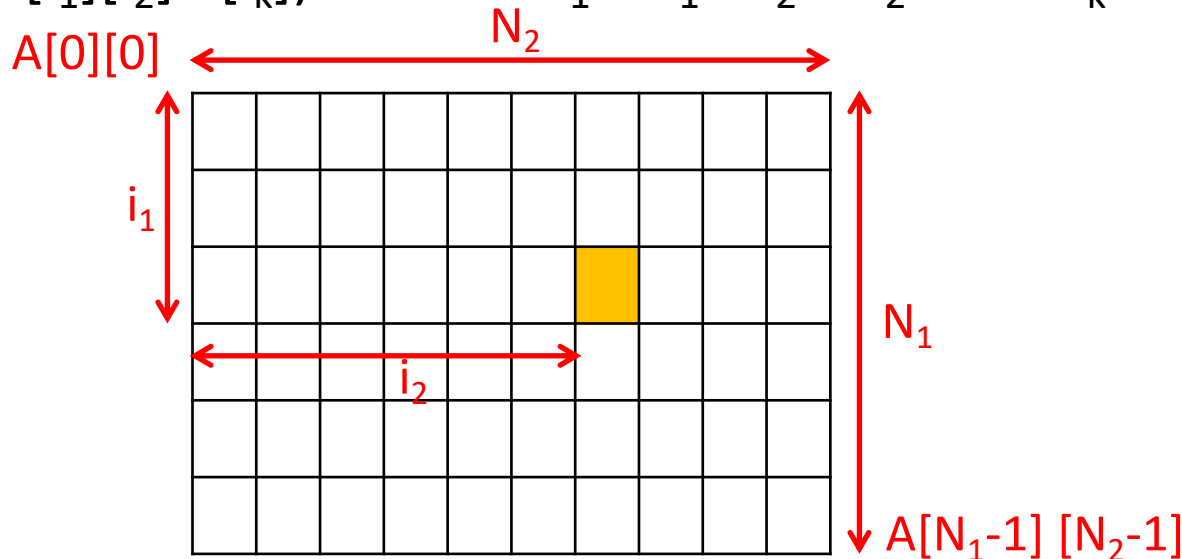
*A[i] ++;*



- *base*: address of the first element
  - *width*: width of each element
    - $i \times \text{width}$  is the offset
- Addressing an array element
  - $\text{addr}(A[i]) = \text{base} + i \times \text{width}$

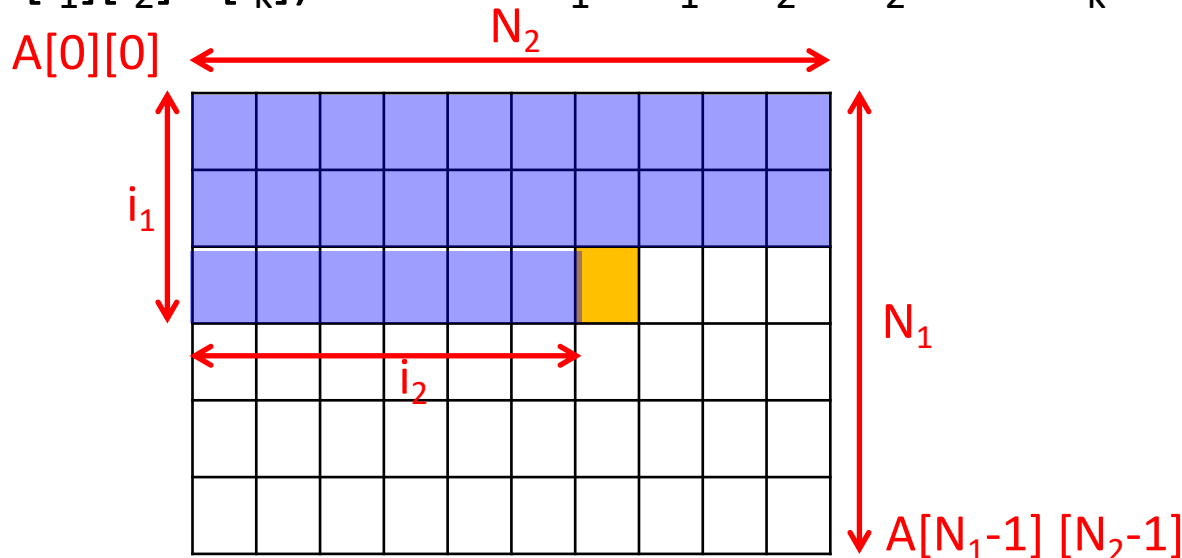
# N-dimensional Array

- Laying out 2D array in 1D memory
  - `int A[N1][N2]; /* int A[0..N1][0..N2] */`
  - `A[i1][i2] ++;`
- The organization can be row-major or column-major
  - C language uses row major (i.e., stored row by row)
  - Row-major:  $\text{addr}(A[i_1, i_2]) = \text{base} + (i_1 \times \underbrace{N_2 \times \text{width}}_{w_1} + i_2 \times \underbrace{\text{width}}_{w_2})$
- *k*-dimensional array
  - $\text{addr}(A[i_1][i_2] \dots [i_k]) = \text{base} + i_1 \times w_1 + i_2 \times w_2 + \dots + i_k \times w_k$



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# Translation of Array References

- $\text{Type}(a) = \text{array}(10, \text{int})$ 
  - $c = a[i];$

$$\text{addr}(a[i]) = \text{base} + i * 4$$

$$\begin{aligned} t_1 &= i * 4 \\ t_2 &= a[t_1] \\ c &= t_2 \end{aligned}$$

- $\text{Type}(a) = \text{array}(3, \text{array}(5, \text{int}))$ 
  - $c = a[i_1][i_2];$
- $\text{Type}(a) = \text{array}(3, \text{array}(5, \text{array}(8, \text{int})))$ 
  - $c = a[i_1][i_2][i_3]$



# Translation of Array References

- `Type(a) = array(10, int)`  
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$$\text{addr}(a[i]) = \text{base} + i * 4$$

$$\begin{aligned} t_1 &= i * 4 \\ t_2 &= a[t_1] \\ c &= t_2 \end{aligned}$$

$$\text{addr}(a[i_1][i_2]) = \text{base} + i_1 * 20 + i_2 * 4$$

- `Type(a) = array(3, array(5, int))`  
– `c = a[i1][i2];`

$$\begin{aligned} t_1 &= i_1 * 20 \\ t_2 &= i_2 * 4 \\ t_3 &= t_1 + t_2 \\ t_4 &= a[t_3] \\ c &= t_4 \end{aligned}$$

- `Type(a) = array(3, array(5, array(8, int)))`  
– `c = a[i1][i2][i3]`

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–  $c = a[i_1][i_2][i_3]$

$$\begin{aligned} \text{addr}(a[i_1][i_2][i_3]) &= \text{base} + i_1 * w_1 + i_2 * w_2 + i_3 * w_3 \\ &= \text{base} + i_1 * 160 + i_2 * 32 + i_3 * 4 \end{aligned}$$

# Translation of Array References (cont.)

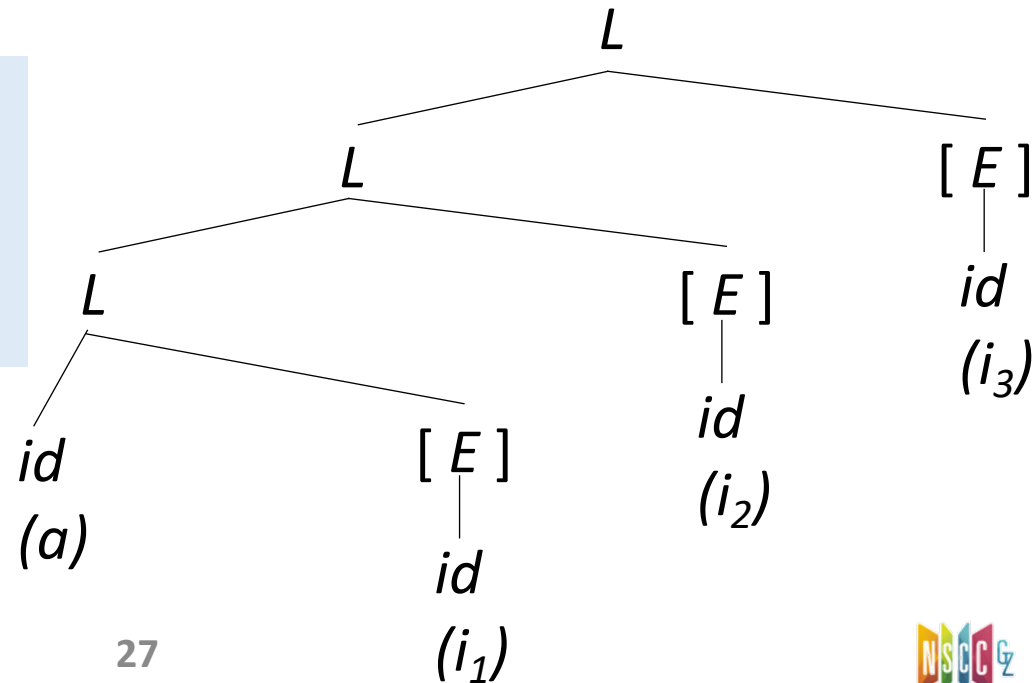
- $A[i_1][i_2][i_3]$ ,  $\text{type}(a) = \text{array}(3, \text{array}(5, \text{array}(8, \text{int})))$ 
  - $L.\text{type}$ : the type of the subarray generated by  $L$
  - $L.\text{addr}$ : a temporary that is used while computing the offset for the array reference by summing the terms  $i_j \times w_j$
  - $L.\text{array}$ : a pointer to the symbol-table entry for the array name
    - $L.\text{array}.\text{base}$  gives the array's base address

①  $S \rightarrow \text{id} = E; \mid L = E;$

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③  $L \rightarrow \text{id} [E] \mid L_1 [E]$

$\text{base} + i_1 \times w_1 + i_2 \times w_2 + \dots + i_k \times w_k$



# Translation of Array References (cont.)

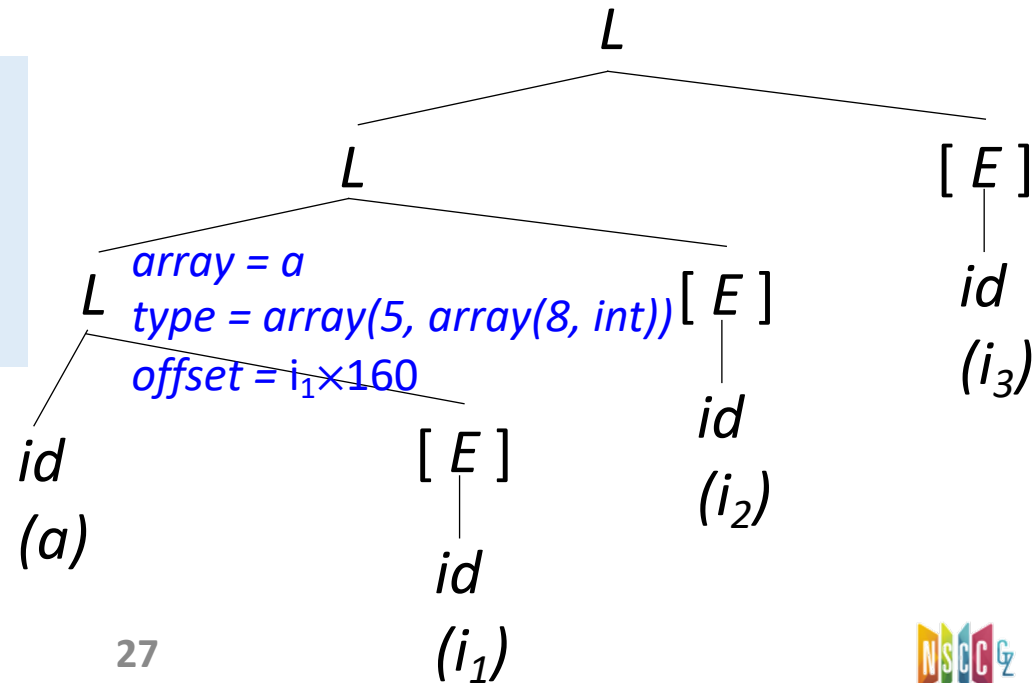
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# Translation of Array References (cont.)

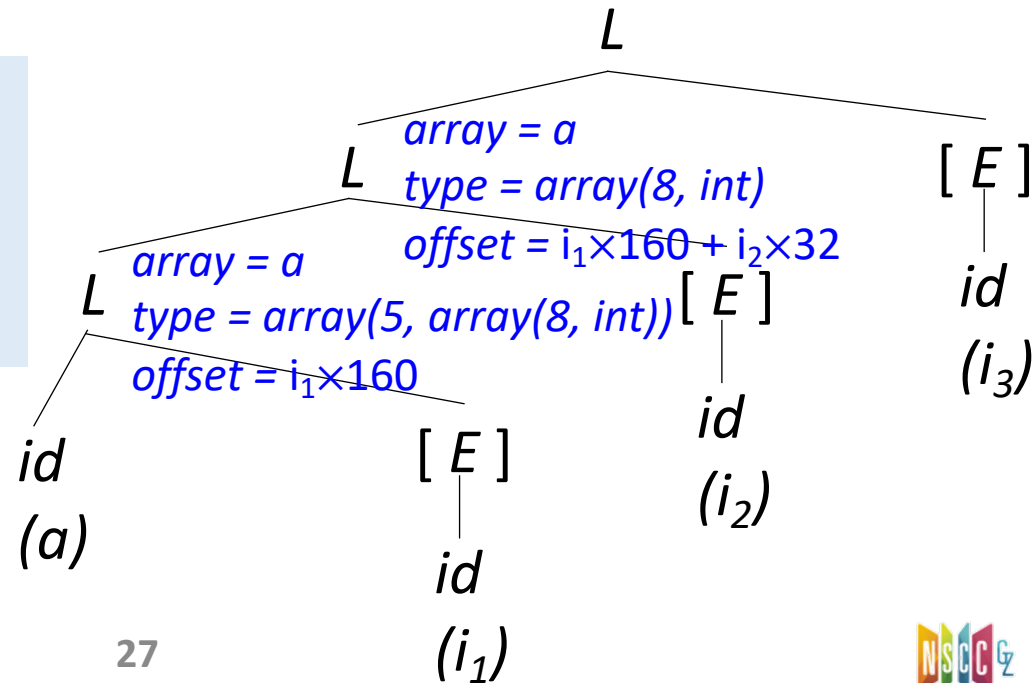
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# Translation of Array References (cont.)

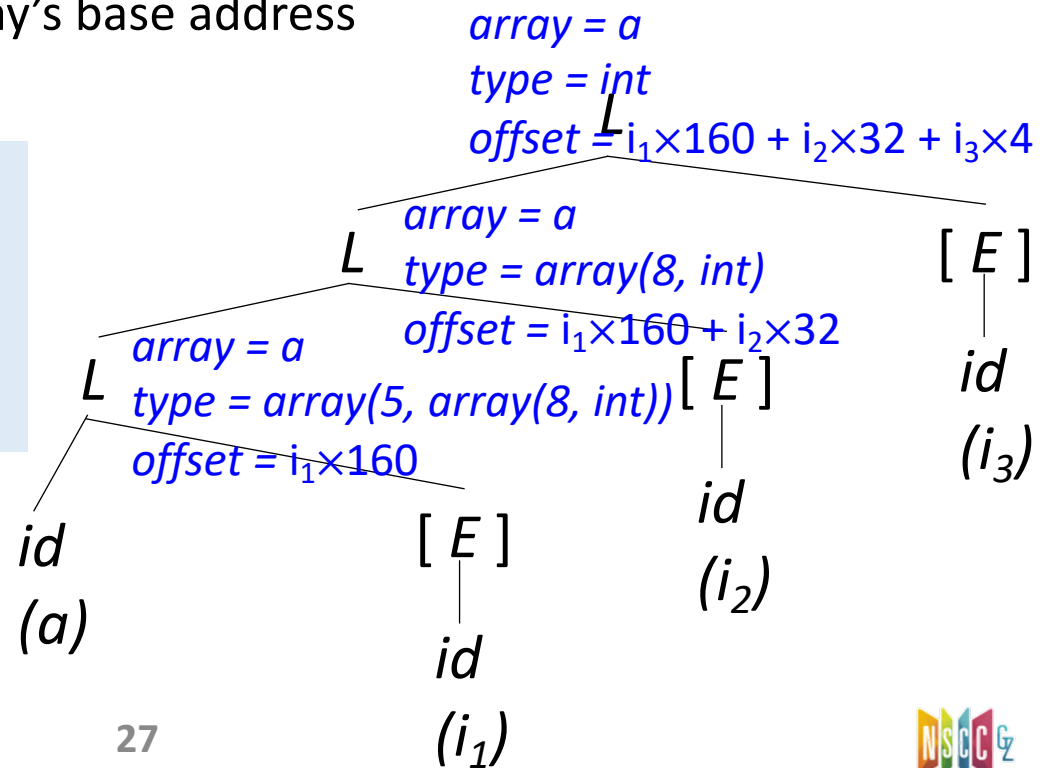
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# Translation of Array References (cont.)

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```
①  $S \rightarrow \text{id} = E; \mid L = E; \{ \text{gen}(L.\text{array}.\text{base}['L.\text{addr}'] \text{'=' } E.\text{addr}); \}$   
②  $E \rightarrow E_1 + E_2 \mid - E_1 \mid (E_1) \mid \text{id} \mid L \{ E.\text{addr} = \text{newtemp}();$   
     $\text{gen}(E.\text{addr} \text{'=' } L.\text{array}.\text{base}['L.\text{addr}']); \}$   
③  $L \rightarrow \text{id} [E] \{ L.\text{array} = \text{lookup}(\text{id}.\text{lexeme}); \text{if } !L.\text{array} \text{ then error};$   
     $L.\text{type} = L.\text{array}.\text{type}.\text{elem};$   
     $L.\text{offset} = \text{newtemp}();$   
     $\text{gen}(L.\text{addr} \text{'=' } E.\text{addr} \text{'*'} L.\text{type}.\text{width}); \}$   
     $\mid L_1 [E] \{ L.\text{array} = L_1.\text{array};$   
     $L.\text{type} = L_1.\text{type}.\text{elem};$   
     $t = \text{newtemp}();$   
     $\text{gen}(t \text{'=' } E.\text{addr} \text{'*'} L.\text{type}.\text{width});$   
     $L.\text{addr} = \text{newtemp}();$   
     $\text{gen}(L.\text{addr} \text{'=' } L_1.\text{addr} \text{'+' } t; \}$ 
```

# Translation of Array References (cont.)

- $A[i_1][i_2][i_3]$ ,  $\text{type}(a) = \text{array}(3, \text{array}(5, \text{array}(8, \text{int})))$

```
①  $S \rightarrow \text{id} = E; \mid L = E; \{ \text{gen}(L.\text{array.base}['L.\text{addr}'] \text{'=' } E.\text{addr}); \}$   
②  $E \rightarrow E_1 + E_2 \mid - E_1 \mid (E_1) \mid \text{id} \mid L \{ E.\text{addr} = \text{newtemp}();$   
     $\text{gen}(E.\text{addr} \text{'=' } L.\text{array.base}['L.\text{addr}']); \}$   
③  $L \rightarrow \text{id} [E] \{ L.\text{array} = \text{lookup}(\text{id.lexeme}); \text{if } !L.\text{array} \text{ then error};$   
     $L.\text{type} = L.\text{array.type.elem};$   
     $L.\text{offset} = \text{newtemp}();$   
     $\text{gen}(L.\text{addr} \text{'=' } E.\text{addr} \text{'*'} L.\text{type.width}); \}$   
 $\mid L_1 [E] \{ L.\text{array} = L_1.\text{array};$   
     $L.\text{type} = L_1.\text{type.elem};$   
     $t = \text{newtemp}();$   
     $\text{gen}(t \text{'=' } E.\text{addr} \text{'*'} L.\text{type.width});$   
     $L.\text{addr} = \text{newtemp}();$   
     $\text{gen}(L.\text{addr} \text{'=' } L_1.\text{addr} \text{'+' } t; \}$ 
```

```
 $t_1 = i_1 * 160$   
 $t_2 = i_2 * 32$   
 $t_3 = t_1 + t_2$   
 $t_4 = i_3 * 4$   
 $t_5 = t_3 + t_4$   
 $c = a[t_5]$ 
```



# CodeGen: Boolean Expressions

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- Boolean expression: *a op b*
  - where op can be <, <=, =, !=, > or >=, &&, ||, ...
- **Short-circuit** evaluation[短路计算]: to skip evaluation of the rest of a boolean expression once a boolean value is known
  - Given following C code: *if (flag || foo()) { bar(); }*
    - If *flag* is true, *foo()* never executes
    - Equivalent to: *if (flag) { bar(); } else if (foo()) { bar(); }*
  - Given following C code: *if (flag && foo()) { bar(); }*
    - If *flag* is false, *foo()* never executes
    - Equivalent to: *if (!flag) { } else if (foo()) { bar(); }*
  - Used to alter control flow, or compute logical values
    - Examples: *if (x < 5) x = 1; x = true; x = a < b*
    - For control flow, boolean operators translate to **jump** statements

# Boolean Exprs (w/o Short-Circuiting)

- Computed just like any other arithmetic expression

$E \rightarrow (a < b) \text{ or } (c < d \text{ and } e < f)$

$t_1 = a < b$   
 $t_2 = c < d$   
 $t_3 = e < f$   
 $t_4 = t_2 \ \&\& \ t_3$   
 $t_5 = t_1 \ || \ t_4$

- Then, used in control-flow statements

– *S.next*: label for code generated after *S*

$S \rightarrow \text{if } E \ S_1$

if (! $t_5$ ) goto *S.next*  
*S*<sub>1</sub>.code  
*S.next*: ...

# Boolean Exprs (w/ Short-Circuiting)

- Implemented via a series of jumps[利用跳转]
  - Each relational op converted to two gotos (*true* and *false*)
  - Remaining evaluation skipped when result known in middle
- Example
  - *E.true*: label for code to execute when *E* is '*true*'
  - *E.false*: label for code to execute when *E* is '*false*'
  - E.g. if above is condition for a *while* loop
    - *E.true* would be label at beginning of loop body
    - *E.false* would be label for code after the loop

$E \rightarrow (a < b) \text{ or } (c < d \text{ and } e < f)$

```
if (a < b) goto E.true
goto L1
L1: if (c < d) goto L2
      goto E.false
L2: if (e < f) goto E.true
      goto E.false
```

E为真: 只要a < b真

a < b假: 继续评估

a < b假、c < d真: 继续评估

E为假: a < b假, c < d假

E为真: a < b假, c < d真, e < f真

E为假: a < b假, c < d真, e < f假