

# Pacemaker CIS541

ARIEL EIZENBERG

[arieleiz@seas.upenn.edu](mailto:arieleiz@seas.upenn.edu)



# Project Highlights - Pacemaker

- A DDD-R pacemaker using an **atrial-based lower rate timing (A-A interval)** modelled in UPPAAL, and a heart model used for correctness testing.
- The pacemaker includes several extra features not required by the project specification:
  - PVAB timing cycle,
  - PVC support (required by A-A interval),
  - Dynamic AVI extension,
  - Alarm buzzer

# Project Highlights – Code Generation

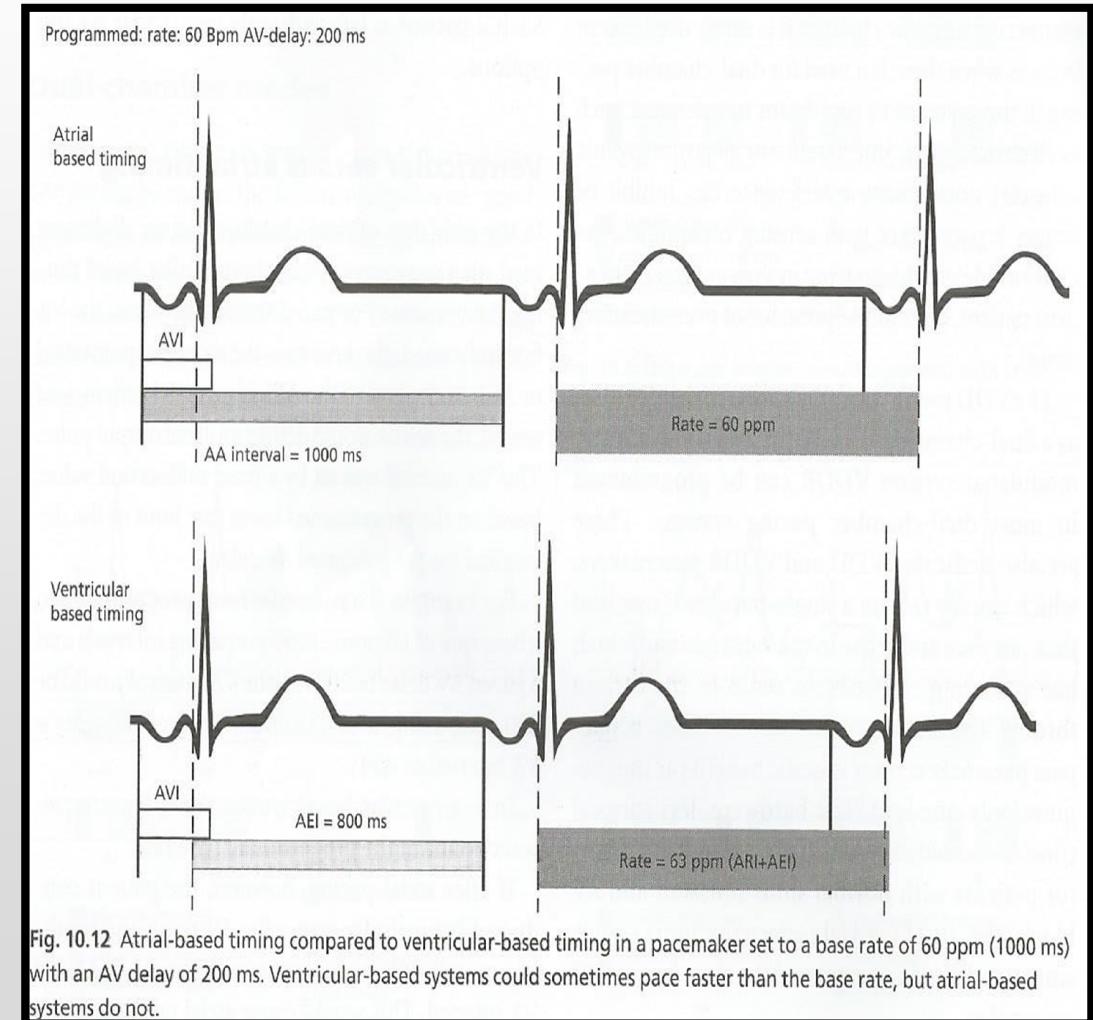
- Developed a versatile UPPAAL toMBED C++ automatic code generator
  - Direct support for binding UPPAAL channels to hardware,
  - Accepts UPPAAL XML files directly,
  - Supports most of the UPPAAL language,
  - Verified with several models (incl. from class),
  - Used to generate code for the pacemaker and heart.
- The auto-generated code was used with no changes, as-is, for the final implementation of both pacemaker and heart.

# Project Highlights – Simulator

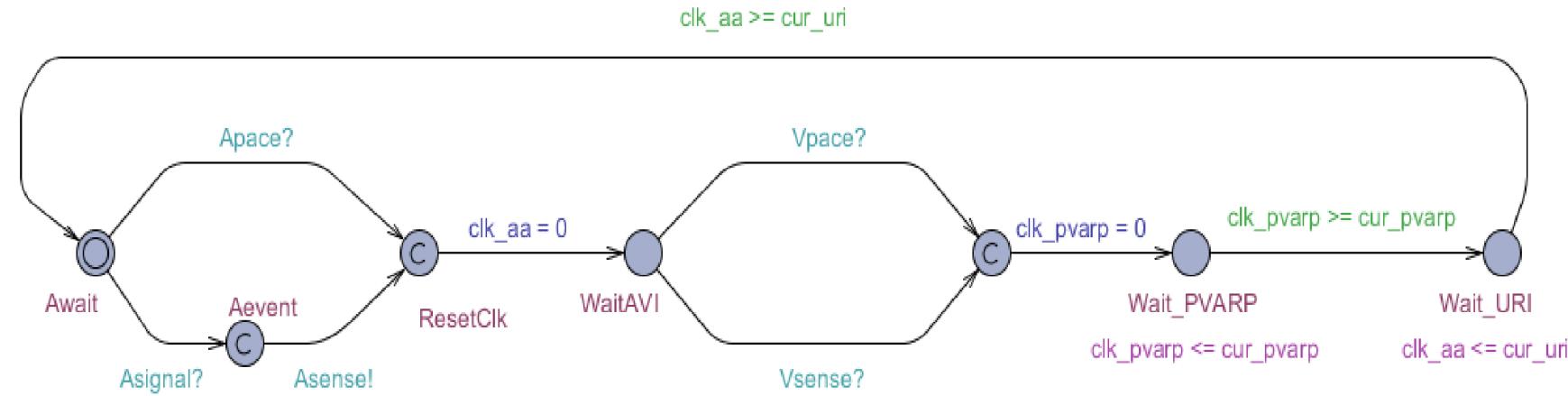
- Developed A CMSIS OS simulator for Windows
  - (OS Used by MBED devices)
  - Rapid development and testing on a PC before deployment to MBED,
  - **Does not require any code changes in the MBED program**
    - just compiling it using a PC compiler and linking with a stub library.
  - Allows for comprehensive testing using tools available for PC
    - Automatic testing, code analysis, code coverage tools, etc'
  - Supports multiple MBED programs running concurrently,
  - Simulates multiple hardware devices/sensors, and supports interconnects between the simulated MBED platforms,
  - Supports visualization/debug plugins

# A-A timing

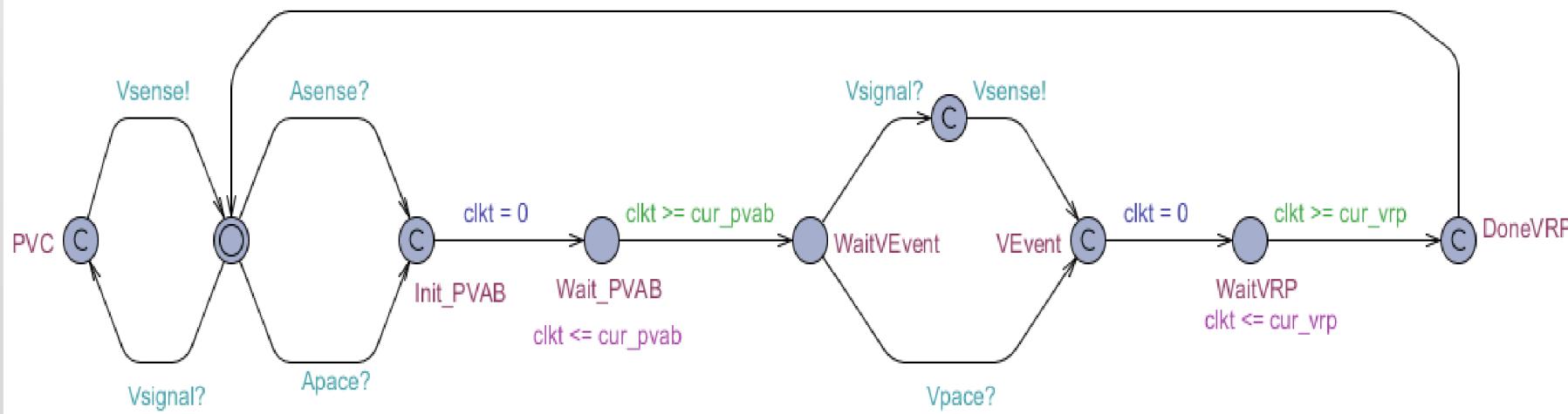
- “In the early days of dual-chamber pacing, all devices used what is commonly called ventricular-based timing. .... . In the mid-1980s, pacemaker manufacturers decided to fix this timing quirk by changing to what is now called atrial-bases timing. In atrial-based timing, it is an atrial event (senses or paced) that starts the interval knows as the AA interval.”
- “The Nuts and Bolts of Cardiac Pacing”, 2<sup>nd</sup> Edition, by Tom Kenny, in Chapter 10 (page 86)



# Modelling – Sensing

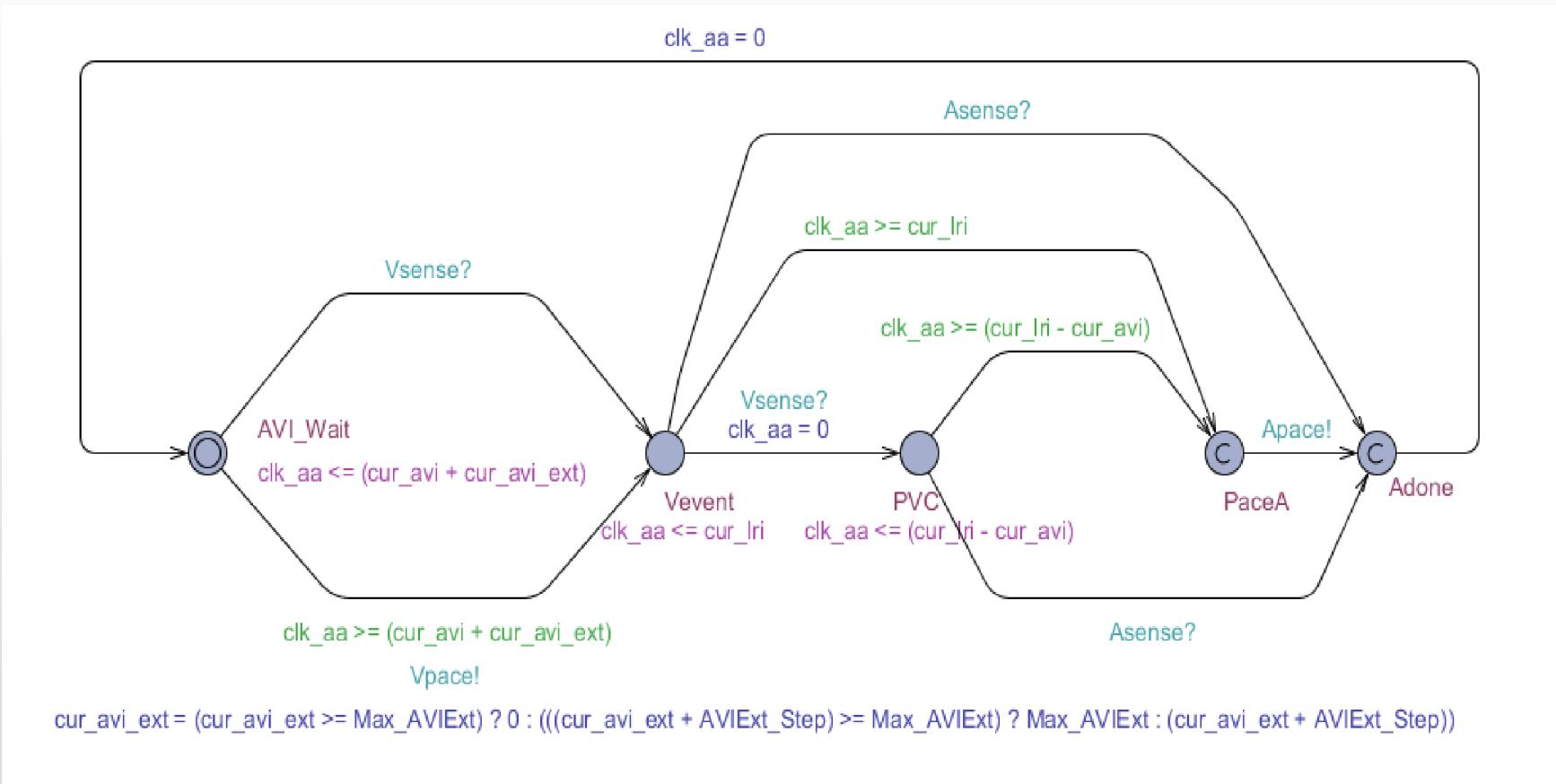


Atrial

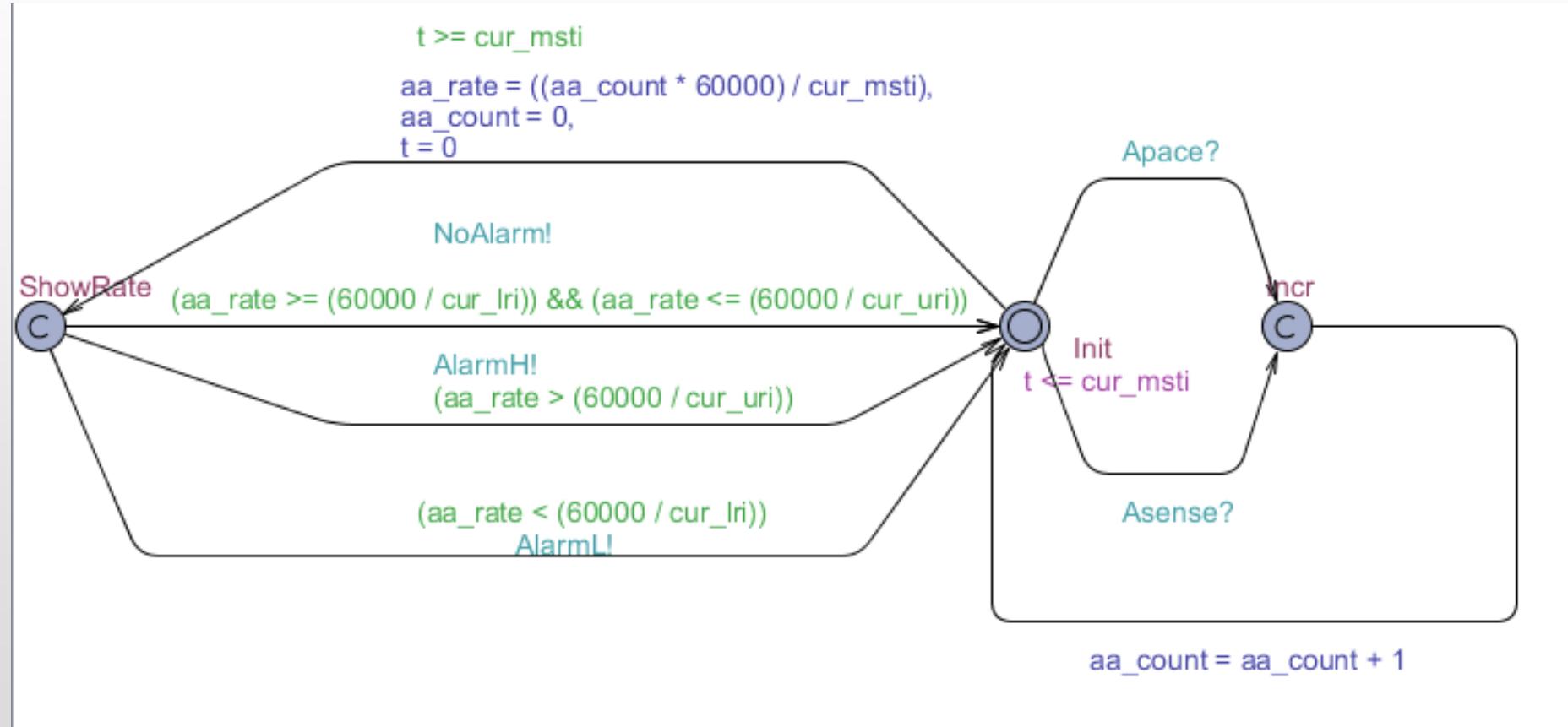


Ventricular

# Modelling – Pacing



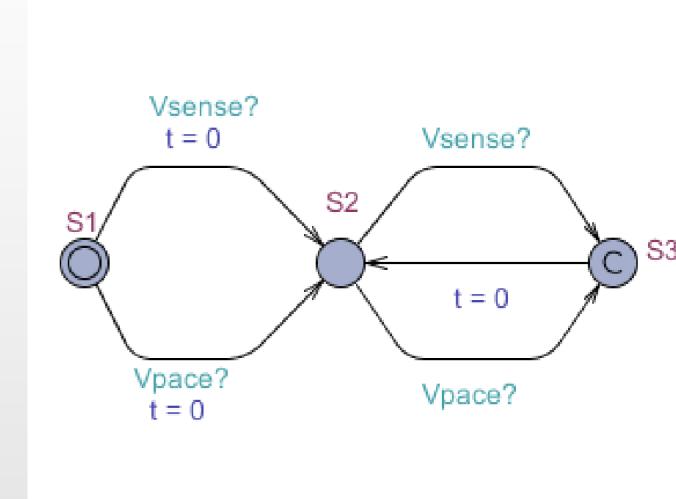
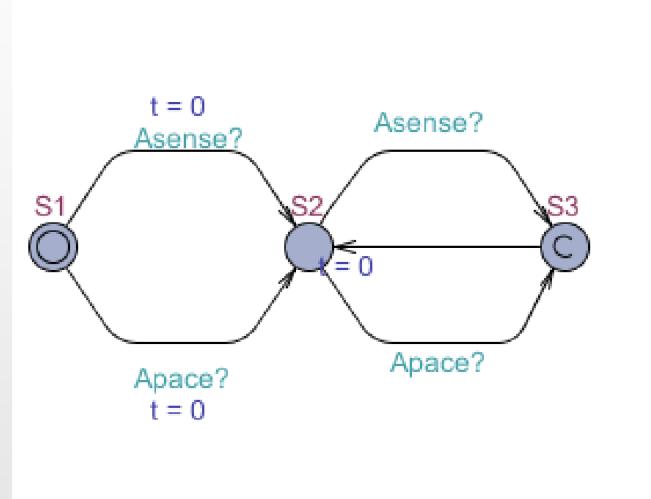
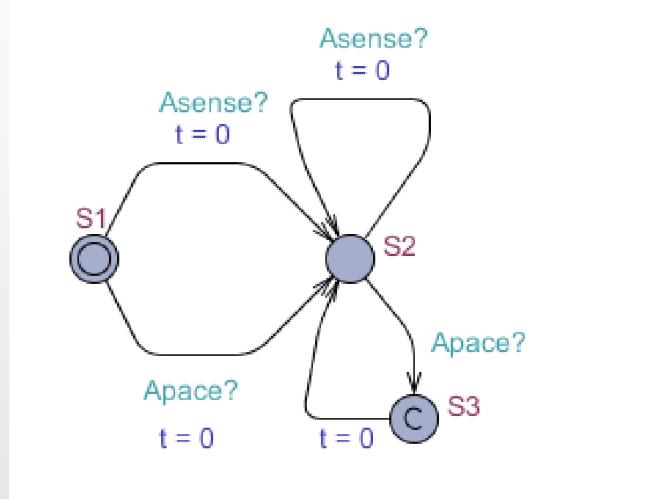
# Modelling – Rate & Alarm



# Verification - Basic

- No Deadlock
  - $A[] \neg deadlock$
- LEDs
  - $A <> PM\_Pacer\_AA.PaceA \text{ imply } PM\_Leds\_Apace.On$
- More interesting verification requires helper “observer” process

# Verification with Observers



**AVI, URI, PVARP**

$A[\cdot] \text{Observer\_URI}.S3 \text{ imply } \text{Observer\_URI}.t \geq (\text{cur\_avi} + \text{cur\_avi\_ext} + \text{cur\_pvarp})$

...

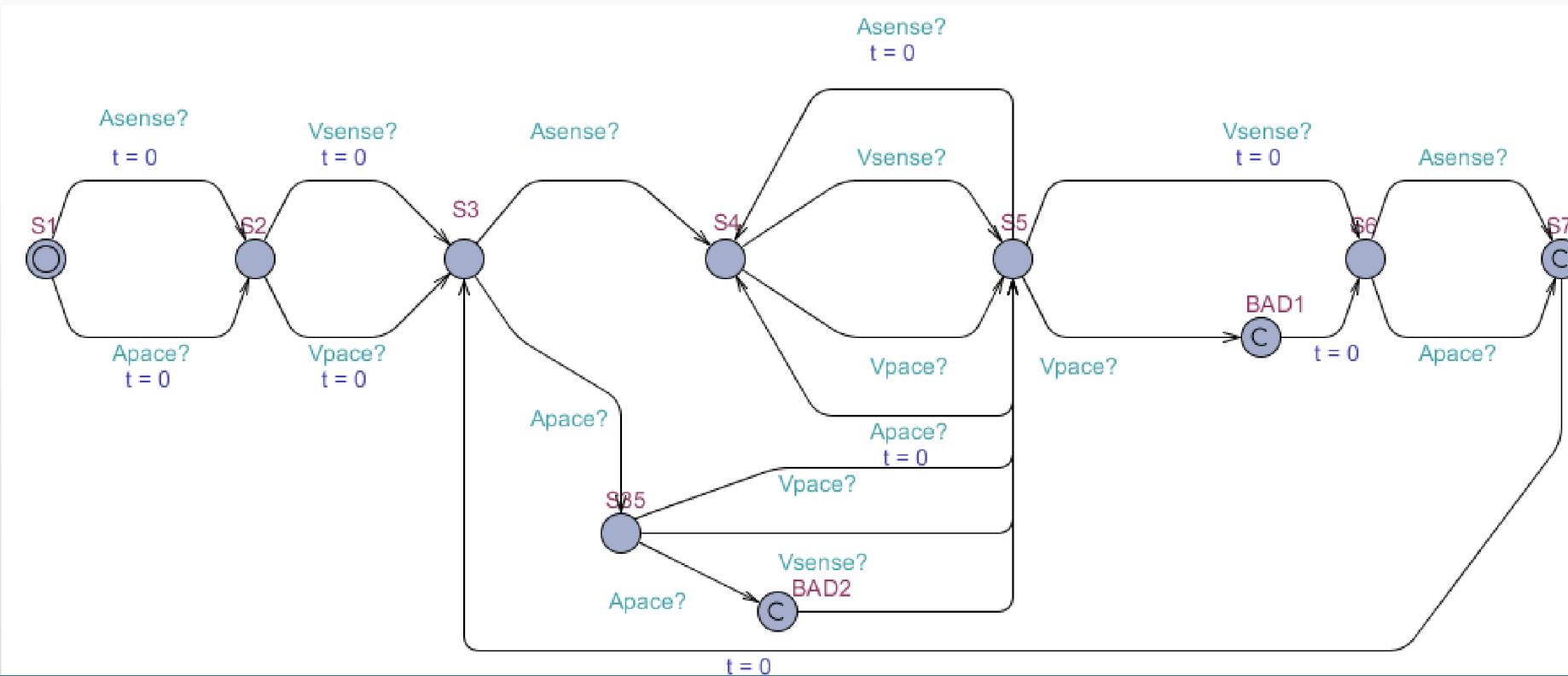
**LRI**

$A[\cdot] \text{Observer\_LRI}.S3 \text{ imply } \text{Observer\_LRI}.t \leq \text{cur\_lri}$

**VRP**

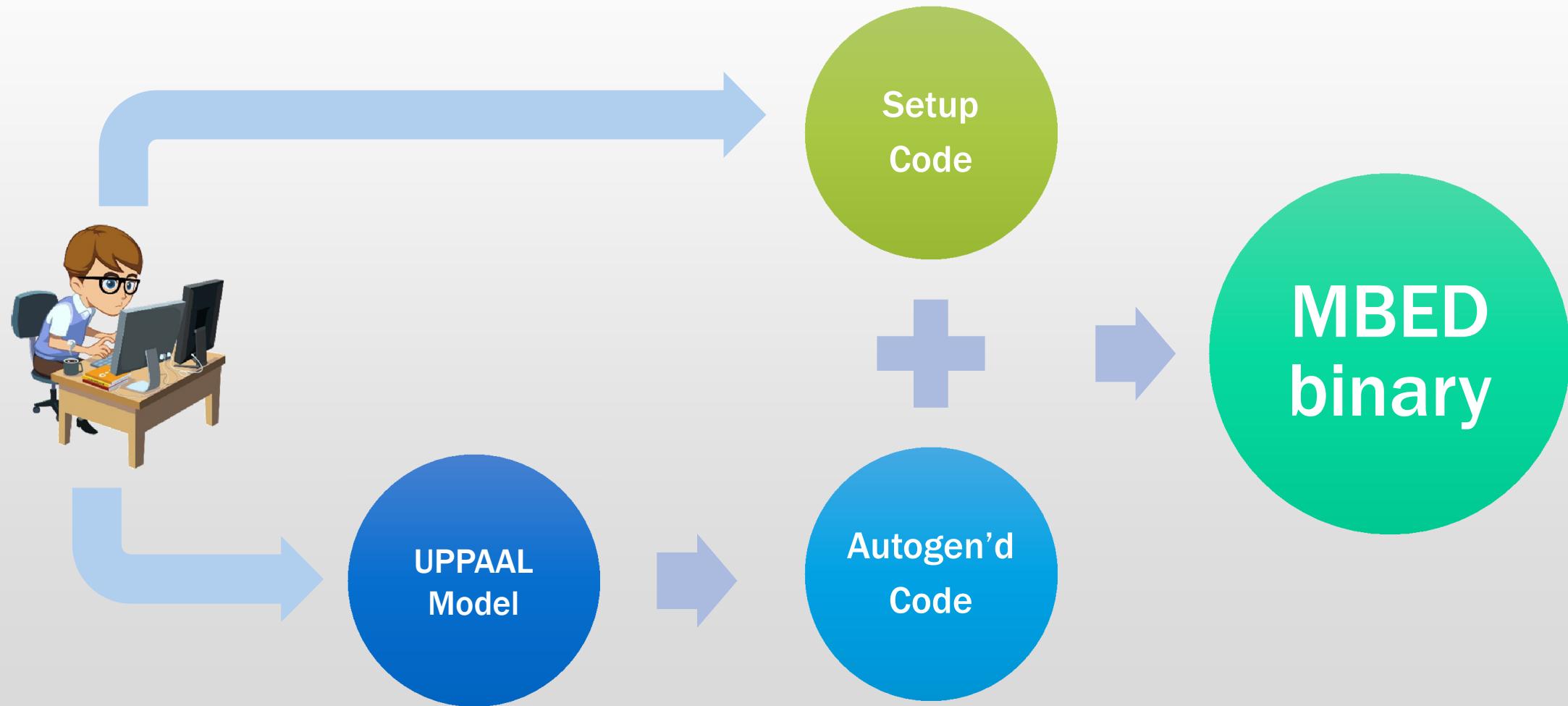
$A[\cdot] \text{Observer\_VRP}.S3 \text{ imply } \text{Observer\_VRP}.t \geq \text{cur\_vrp}$

# Verification with Observers



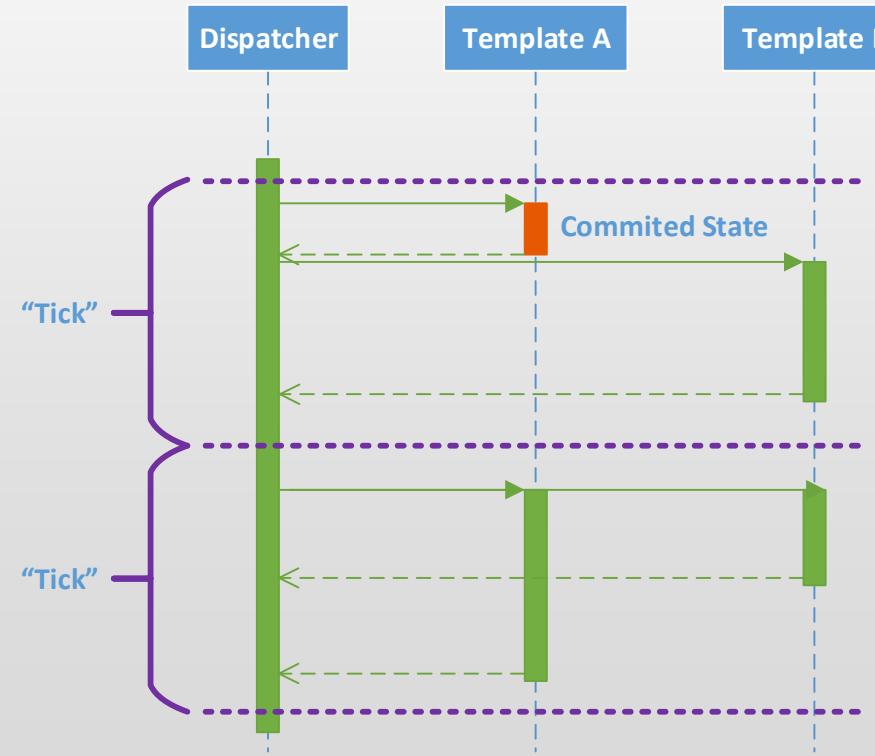
**BAD1** - The pacemaker will never generate two ventricular paces without an atrial event in-between  
**BAD2** - The pacemaker will never generate two atrial paces without an ventricular event in-between

# Code Generation



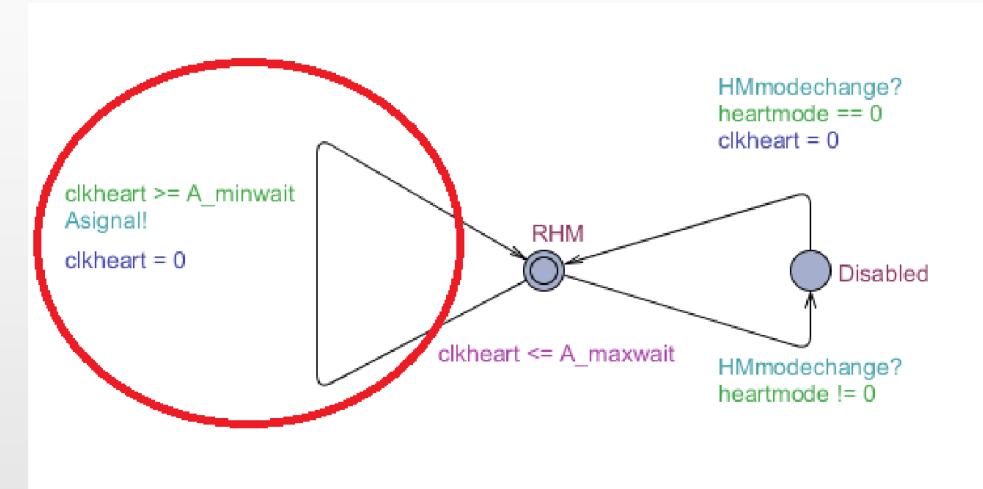
# Code Generation

- Main challenge was handling committed/urgent/regular semantics as accurately as possible



# Code Generation – Invariants, Guards and Updates

- Generator fully supports invariants, guards and updates
  - Generate function to calculate them at runtime
- Variables (int, bool, clocks, ...) stored in a global object



```
static bool guardfunc_HM_Asignal_TRANS_RHM_RHM_0() {  
    return (((int)SystemVariables.clk_GLOBAL_clkheart) >=  
           ((int)SystemVariables.int_GLOBAL_A_minwait));  
};  
static void updatefunc_HM_Asignal_TRANS_RHM_RHM_0() {  
    SystemVariables.clk_GLOBAL_clkheart = (0);  
};
```

# Code Generation - Integration with Hardware

```
void set_channel_action(SYNCHRONIZATION_CHANNEL* channel,  
                       PinName pin,  
                       SendChannelMode mode,  
                       int pulse_length_us = 0);
```

<b>SendChannelModeNone</b>	<b>Do nothing</b>
<b>SendChannelModeSet</b>	<b>Set the pin to 1</b>
<b>SendChannelModeToggle</b>	<b>Toggle the value of the pin</b>
<b>SendChannelModeReset</b>	<b>Set the pin to 0</b>
<b>SendChannelModePulseUp</b>	<b>Pulse up to 1 for the time specified, then reset to 0</b>
<b>SendChannelModePulseDown</b>	<b>Pulse down to 0 for the time specified, then reset to 1</b>

# Code Generation - Integration with Hardware

```
void set_receive_input(SYNCHRONIZATION_CHANNEL* channel,  
                      PinName pin,  
                      PinMode mode, // pull up, pull down, ...  
                      ReceiveChannelMode rcmode)
```

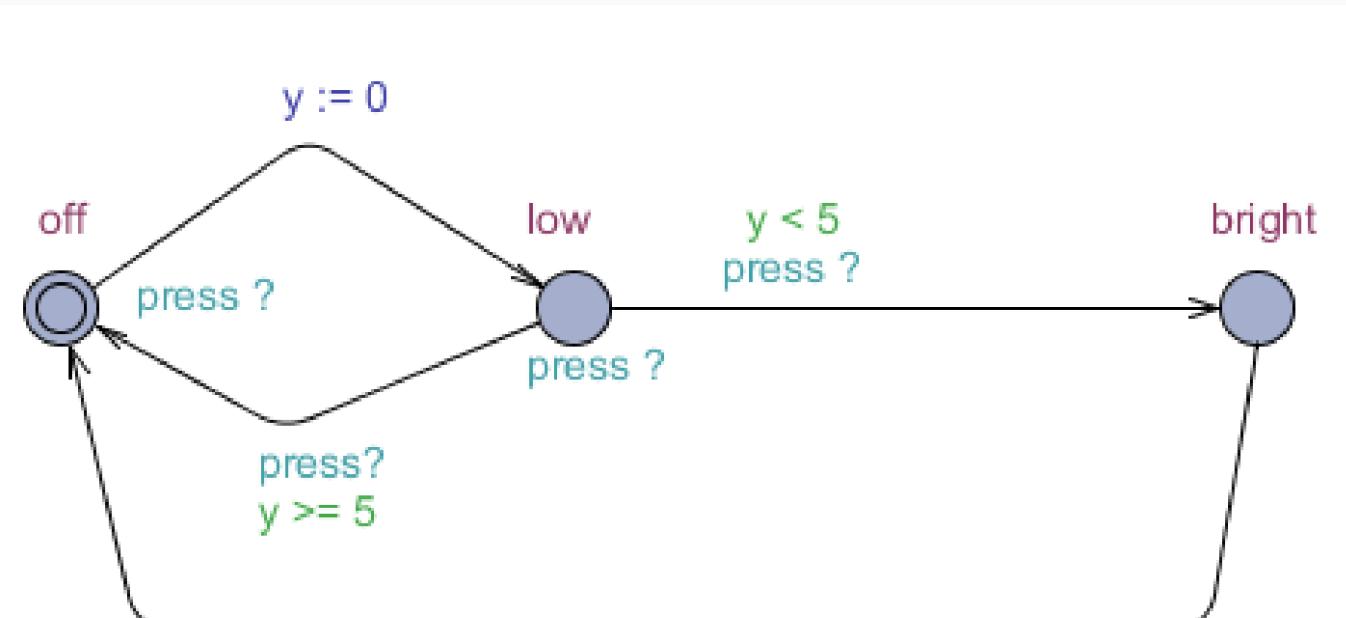
<b>ReceiveChannelModeNone</b>	<b>Do nothing</b>
<b>ReceiveChannelInterruptRise</b>	<b>Fire the channel on rise</b>
<b>ReceiveChannelInterruptFall</b>	<b>Fire the channel on fall</b>

# Code Generation - Callbacks

- All operations support adding user callbacks
  - Enter state
  - Leave state
  - Take transition
  - State template
  - ...
- Callbacks can do anything but should be fast (like ISRs)

# Code Generation – Example

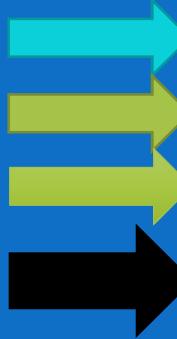
## *Light with Two Levels*



```
// Place global declarations here.  
chan press;  
clock y;
```

# Code Generation – Example

## *Light with Two Levels*

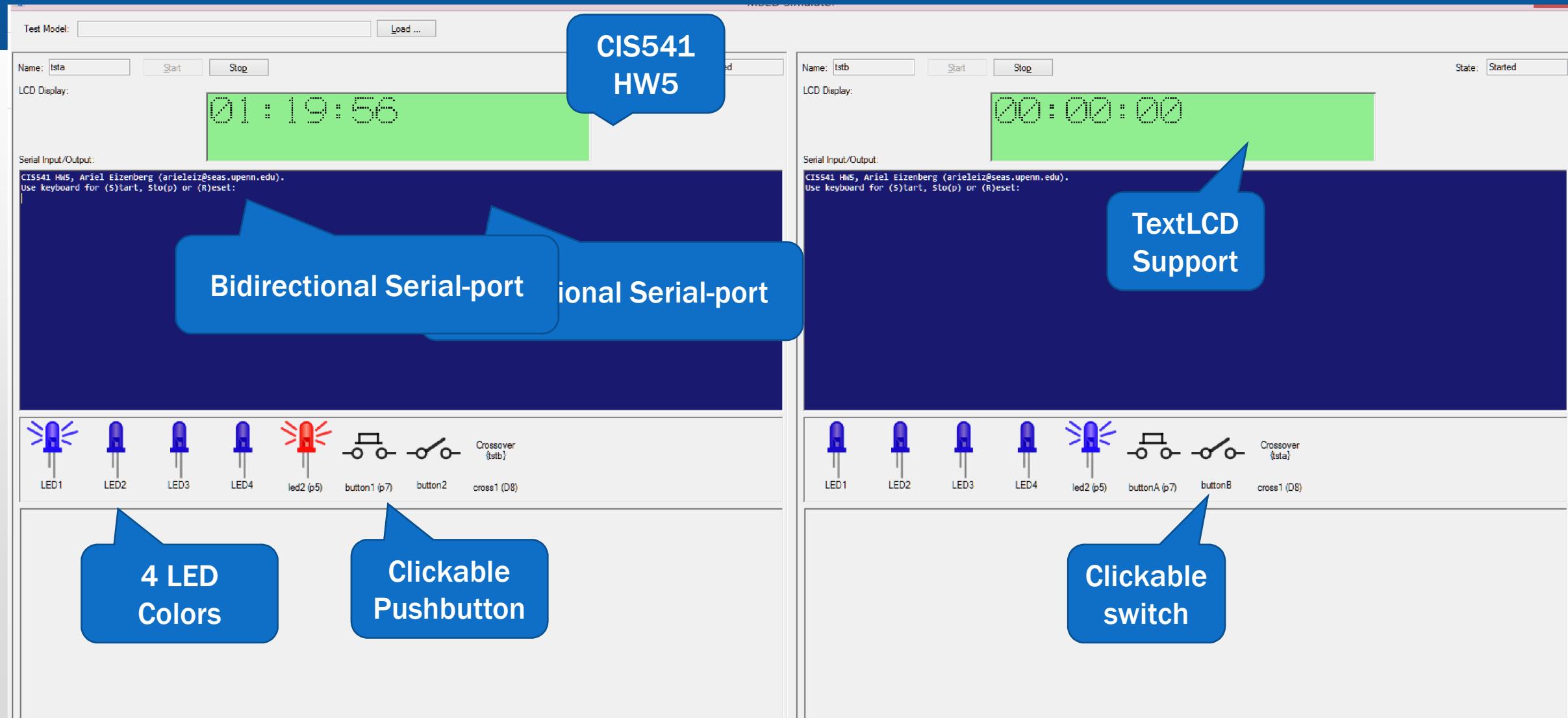
```
int main (void) {
    Dispatcher d;
    d.set_clock_multiplier(1000000 /*us*/); // a tick each second

    d.set_channel_action(&Light_TRANS_off_low_0, LED1, SendChannelModeSet);
    d.set_channel_action(&Light_TRANS_low_off_0, LED1, SendChannelModeReset);
    d.set_channel_action(&Light_TRANS_low_bright_0, LED2, SendChannelModeSet);
    d.set_channel_action(&Light_TRANS_bright_off_0, LED1, SendChannelModeReset);
    d.set_channel_action(&Light_TRANS_bright_off_0, LED2, SendChannelModeReset);
    disp.add_process(&Model::Light_PROCESS_STATE);
    disp.run();
}
```

Emulates two levels with two LEDs

# MBED Simulator

- MBED development environment is early 1990s style
- Does not require any change to code, just compile & link with library
  - Develop in modern IDE
  - PC Debugging tools, Code Analysis, Code Coverage, ...
- Supports:
  - Threads, including RTOS style signaling
  - Timers, Tickers, RTOSTimer, etc' – reading and interrupts,
  - Mutexes,
  - GPIO read, write, interrupts,
  - TextLCD,
  - Serial Port – reading, writing, interrupts, break,
  - Sleep,
  - NVIC\_SystemReset.

# MBED Simulator



## MBED Simulator

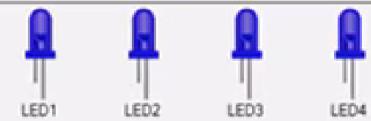
Test Model:  Load ...

Name: pacemaker State: Initialized

LCD Display:

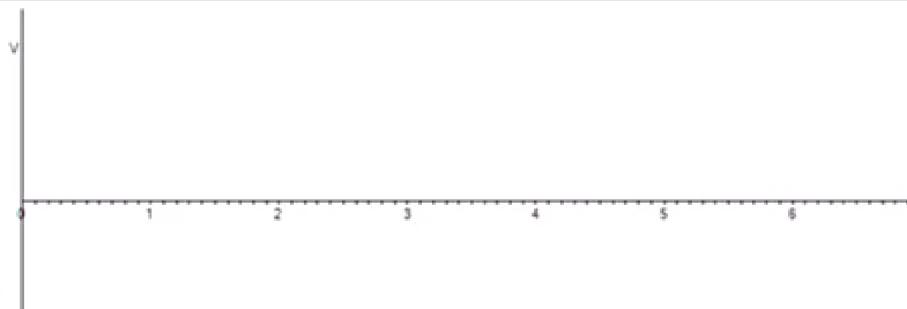


Serial Input/Output:



Crossover (heat) Crossover (heat) Crossover (heat) GROUND (0)  
SignalA (D8) SignalV PaceA PaceV HM/PM

Pace  
Signal  
Sense

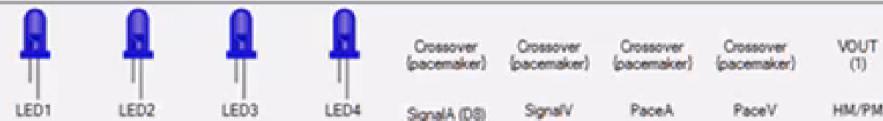


Name: heart State: Initialized

LCD Display:

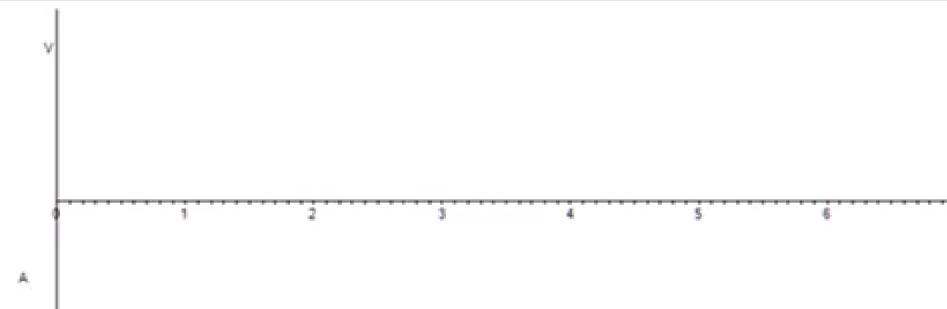


Serial Input/Output:

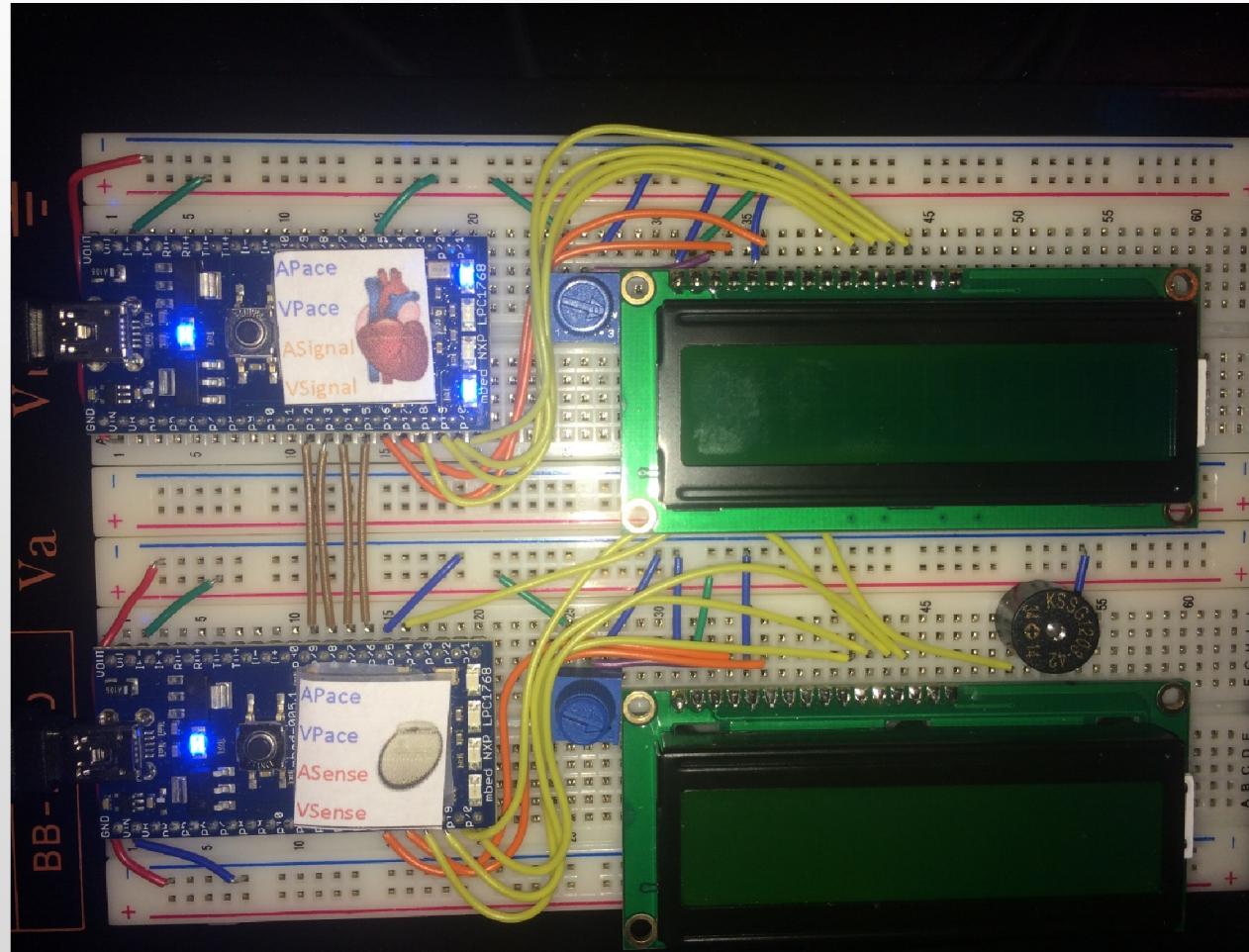


Crossover (pacemaker) Crossover (pacemaker) Crossover (pacemaker) VOUT (1)  
SignalA (D8) SignalV PaceA PaceV HM/PM

Pace  
Signal  
Sense



# The Hardware

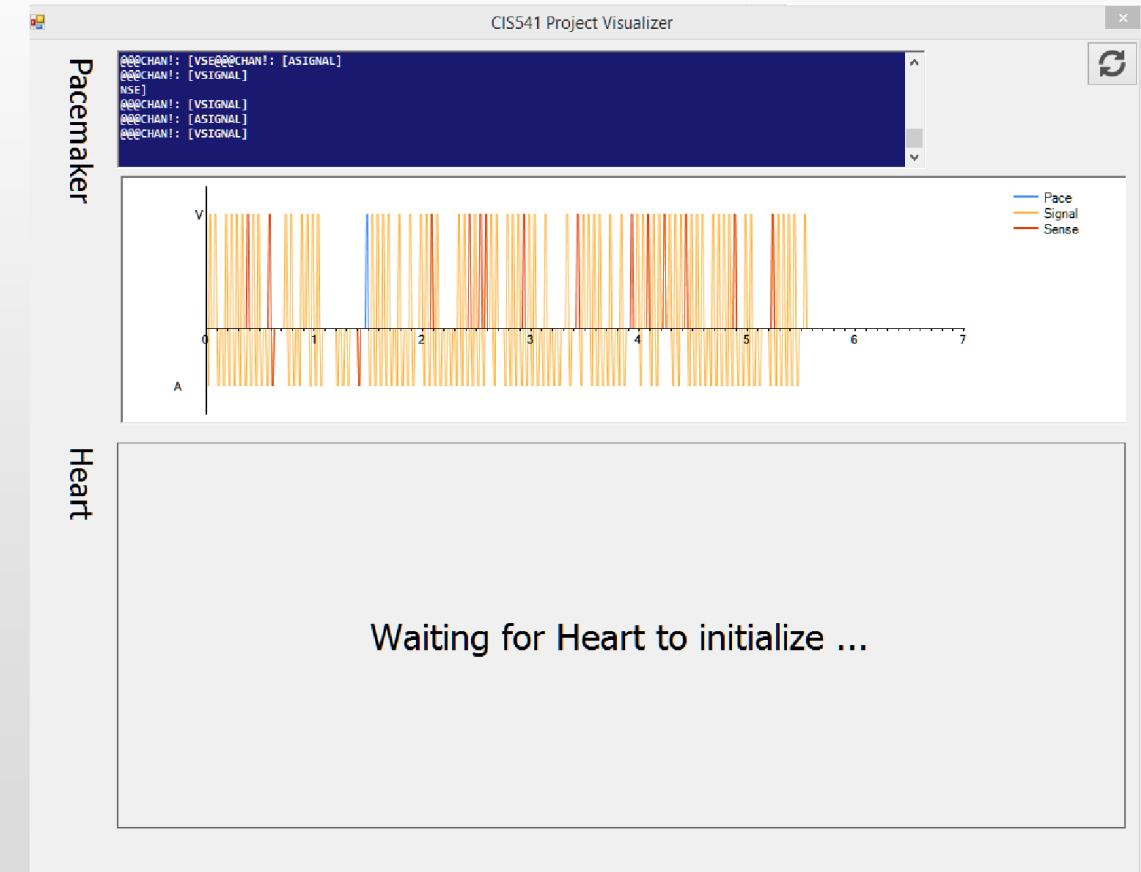


# Debugging on actual MBED

- MBED IDE does not support debugging ...
- And I have no JTAG connector, so ...
- Build project using **GCC-ARM-EMBEDDED 4.8**
  - Generates bad code with -Os, so no optimizing
- Debug using gdb+PyOCD

# Testing

- Developed Serial logger + visualizer
  - Automatically parses serial output from both devices
  - Add timing information to logs
  - Provide a quick way to see if everything is OK



# Testing - Code Coverage

- Use simulator in conjunction with PC Code Coverage Tools

The screenshot shows the Microsoft Visual Studio interface with the following details:

- Solution Explorer:** Displays the project structure under "mbedsimulator". The "pacemaker" folder is expanded, showing files like main\_pacemaker.cpp, ModelImpl.cpp, ModelImpl.h, ModelPriv.h, and ModelPriv.h.
- Code Editor:** Shows a snippet of C++ code from main\_pacemaker.cpp. The code handles alarm and input events. Some lines are highlighted in red, indicating they were not covered by the test cases.
- Code Coverage Results:** A table titled "Code Coverage Results" provides a detailed breakdown of coverage across various modules and functions. The table includes columns for "Not Covered (Blocks)", "Not Covered (% Blocks)", "Covered (Blocks)", and "Covered (% Blocks)".

Hierarchy	Not Covered (Blocks)	Not Covered (% Blocks)	Covered (Blocks)	Covered (% Blocks)
mbdb&cc.dll	453	26.92%	1230	73.08%
{ } AESIM	102	41.30%	145	58.70%
{ } Global Classes	135	25.91%	386	74.09%
{ } Model	84	29.79%	198	70.21%
Global Functions	84	30.43%	192	69.57%
SYSTEM_VARIABLES	0	0.00%	6	100.00%
{ } ModelImpl	81	18.62%	354	81.38%
DelayedThread	17	45.95%	20	54.05%
Dispatcher	2	7.14%	26	92.86%
DispatcherPrivate	54	16.56%	272	83.44%
PROCESS_ENTRY_THREAD	0	0.00%	4	100.00%
RecvData	1	20.00%	4	80.00%
SYNCHRONIZATION_CHANNEL_STATE	0	0.00%	4	100.00%
SendData	7	36.84%	12	63.16%
clk	0	0.00%	12	100.00%
{ } mbed	46	26.14%	130	73.86%
{ } std	5	22.72%	17	77.27%

# Assurance Case

