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★ Course / 13. Building the Beta / Lecture Videos (39:19)





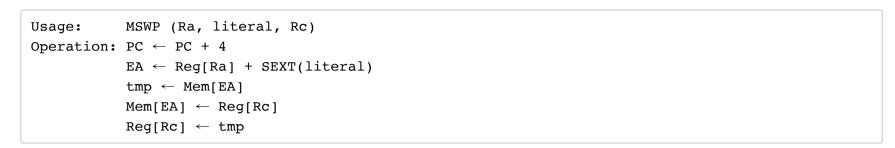
■ Calculator

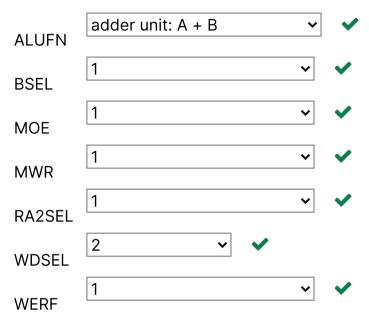
LE13.1.1 New Load/Store Instructions

1.0/1.0 point (ungraded)

For each of the following potential additions to the Beta instruction set, fill in the table with the control signal settings needed to execute these instructions on an unpipelined Beta. Assume the datapath and control signals as shown at the end of the previous video. Please choose "do not care" if the value of control signal doesn't matter when executing the instruction.

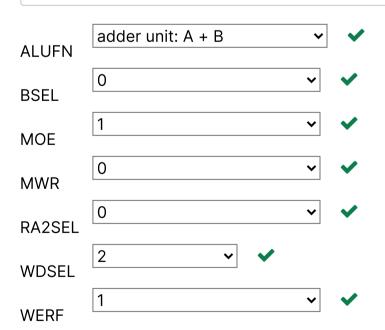
(A) Swap register contents with memory location





(B) Load indexed

Usage: LDX (Ra, Rb, Rc)
Operation: PC ← PC + 4
Reg[Rc] ← Mem[Reg[Ra] + Reg[Rb]]



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