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Tutorial : Leniency

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Leniency

1/1 point (ungraded)

In lecture we saw that a 2-input NOR gate might obey the static discipline (i.e., be a valid combinational device) without being a lenient combinational device. Is it possible for an inverter to be a valid combinational device without being lenient?

☐ Yes

☒ No

☐ Can't Tell

Submit

Leniency

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A 2-input AND gate is made from a lenient CMOS 2-input NAND gate followed by a lenient CMOS inverter. Is the AND gate necessarily lenient?

- ☒ Yes
- ☐ No
- ☐ Can't Tell

Submit

Leniency

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If an inverter conforms to our definition of a combinational device, is it necessarily lenient?

- ☒ Yes
- ☐ No

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<div><div><input checked="" type="checkbox"/></div><div><u>Why the inverter is lenient ?</u></div><div>Why in the following questions we are considering inverter to be lenient? Usually in lenient devices we ignore one or other inputs repl...</div><div>5</div></div>	
<div><div><input checked="" type="checkbox"/></div><div><u>Leniency tutorial - Problem 1</u></div><div>"Is it possible for an inverter to be a valid combinational device without being lenient?" Each inverter is always lenient. The currently...</div><div>6</div></div>	
<div><div><input checked="" type="checkbox"/></div><div><u>The Number of Inputs not specified.</u></div><div>In CMOS Tutorial, the last problem "If an inverter conforms to our definition of a combinational device, is it necessarily lenient?" Beca...</div><div>2</div></div>	

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