

# Computation Structures 3: Computer Organization

Resume course

Collapse all

<div><div>✔</div><div>Course Overview and Tool Documentation</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Welcome to 6.004.3x!</a> 3 min + 1 activity</div></div>
<div><div>✔</div><div><a href="#">Jade Documentation</a> 13 min + 1 activity</div></div>
<div><div>✔</div><div><a href="#">Jade Sandbox</a> 1 activity</div></div>
<div><div>✔</div><div><a href="#">BSim Documentation</a> 1 min + 1 activity</div></div>
<div><div>✔</div><div><a href="#">BSim Sandbox</a> 1 activity</div></div>
<div><div>✔</div><div>Entrance Survey</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Course Entrance Survey</a> 1 min + 1 activity</div></div>
<div><div>✔</div><div>15. Pipelining the Beta</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lecture Videos (50:06)</a> 51 min + 9 activities</div></div>
<div><div>✔</div><div><a href="#">Worked Examples</a> 19 min + 2 activities</div></div>
<div><div>✔</div><div><a href="#">Tutorial Problems</a> 1 min + 2 activities</div></div>
<div><div>✔</div><div>Assignment 1 (Exercises due April 10, Lab due April 24)</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lab 9: Optimizing the Beta (1 Question)</a> 1 activity</div><div>Design Project due Apr 25, 2017, 7:59 AM GMT+8</div></div>
<div><div>✔</div><div>16. Virtual Memory</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lecture Videos (49:01)</a> 50 min + 9 activities</div></div>
<div><div>✔</div><div><a href="#">Worked Examples</a> 12 min + 1 activity</div></div>
<div><div>✔</div><div>17. Virtualizing the Processor</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lecture Videos (38:19)</a> 39 min + 7 activities</div></div>
<div><div>✔</div><div><a href="#">Worked Examples</a> 8 min + 1 activity</div></div>
<div><div>✔</div><div>Assignment 2 (Exercises due April 17, Lab due April 24)</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lab 10: Instruction Emulation (1 Question)</a> 1 activity</div><div>Lab due Apr 25, 2017, 7:59 AM GMT+8</div></div>
<div><div>✔</div><div>18. Devices and Interrupts</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lecture Videos (46:07)</a> 47 min + 12 activities</div></div>
<div><div>✔</div><div><a href="#">Worked Examples</a> 7 min + 1 activity</div></div>
<div><div>✔</div><div>19. Concurrency and Synchronization</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lecture Videos (36:48)</a> 38 min + 8 activities</div></div>
<div><div>✔</div><div><a href="#">Worked Examples</a> 8 min + 1 activity</div></div>
<div><div>✔</div><div>Assignment 3 (due April 24)</div><div>—</div></div>
<div><div>✔</div><div><a href="#">Lab 11: Tiny OS (1 Question)</a> 1 activity</div><div>Lab due Apr 25, 2017, 7:59 AM GMT+8</div></div>
<div><div>✔</div><div>20. System-level Communication</div><div>—</div></div>

## Course Tools

 [Bookmarks](#)

## Important dates

 **Wed, May 3, 2017**

### Course ends

This course is archived, which means you can review course content but i

[View all course dates](#)

## Course Handouts

- [Syllabus](#)

### Lecture slides (pdf)

15.

[Pipelined Beta](#)
16.

[Virtual Memory](#)
17.

[Virtualizing the Processor](#)
18.

[Devices & Interrupts](#)
19.

[Concurrency & Synchronization](#)
20.

[System-level Communications](#)
21.










[Parallel Processing](#)

### 6.004 Handouts

- [Jade Frequently Asked Questions and Tips](#)
- [The Standard Cell Library](#)
- [Introduction to BSim](#)
- [Beta Documentation](#)
- [Summary of Beta Instruction Formats](#)
- [Beta Diagram](#)
- [Pipelined Beta Diagram](#)
- [A Brief Overview of C](#)

### edX Handouts

- [Discussion Forum](#)
- [Formula Response](#)

 <a href="#">Lecture Videos (39:30)</a> 40 min + 7 activities	
 <b>21. Parallel Processing</b>	
 <a href="#">Lecture Videos (47:01)</a> 48 min + 6 activities	
 <b>Exit Survey</b>	
 <a href="#">Course Exit Survey</a> 1 min + 1 activity	
 <b>Exam (due May 1)</b>	



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