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LE7.1

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LE7.1.1: Latency and Throughput

2 points possible (ungraded)

A synchronous pipeline has a latency of L and throughput of T . If we find a way to double the clock frequency by doubling the number of pipeline stages, what revised latency and throughput can we expect? Please provide your response as a function of L and T . (Note that multiplication must be explicitly stated, e.g., $L*8$, not $8L$).

Latency:

L

L

Throughput:

$2 \cdot T$

Sorry, couldn't parse formula

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