MITx 6.004.1x
Computation Structures 1: Digital Circuits

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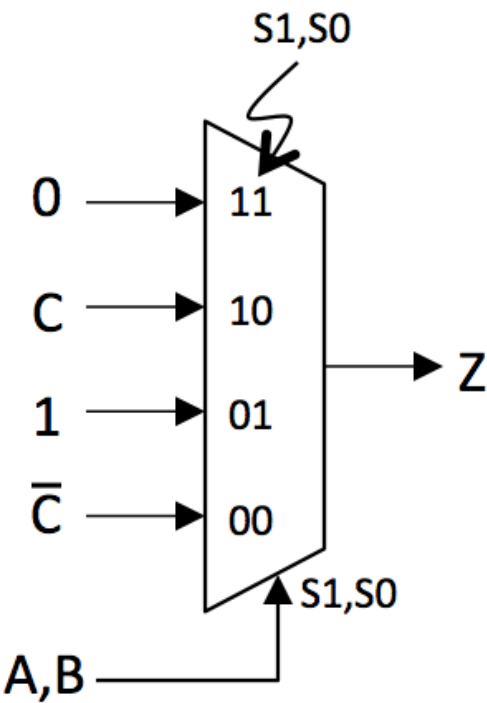
LE4.6

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LE4.6.1: Mux Logic

8/8 points (ungraded)

Consider the following schematic for a mux-based implementation for a 3-input, 1-output Boolean function.

Please fill in the truth table below, giving the value of the output Z for the eight possible combinations of the inputs values A, B, and C.

A	B	C	Z	
0	0	0	1	✔
0	0	1	0	✔
0	1	0	1	✔
0	1	1	1	✔
1	0	0	0	✔
1	0	1	1	✔
1	1	0	0	✔
1	1	1	0	✔

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💬 **Can't understand the diagram or how to solve**   3
So a Mux is a data selector given the selector inputs. The selector inputs correspond to the 4 inputs, 0, 1, C and not C. And I'm assu...

☑ **Can't understand diagram.**   2
I can't understand the information conveyed by the diagram. What is the meaning of the curved arrow? I have no idea about anything...

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