MITx 6.004.1x
Computation Structures 1: Digital Circuits

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🏠 **Course** / **3. CMOS** / **Lecture Videos (44:58)**

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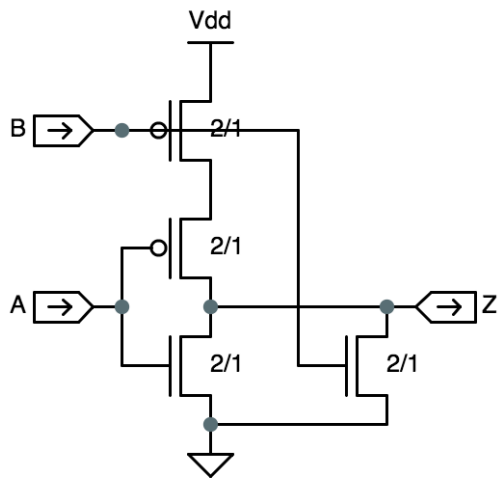
LE3.3

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LE3.3.1: CMOS Truth Table

4/4 points (ungraded)

Here's another simple CMOS circuit with series PFETs for the pullup circuit and parallel NFETs for the pulldown circuit.

Please fill in the truth table below, giving the value of the output Z for the four possible combinations of the inputs values A and B.

A	B	Z
0	0	1 ✔
0	1	0 ✔
1	0	0 ✔
1	1	0 ✔

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💬 **STAFF line over the CMOS?**
On the **top-left CMOS**, would not it be better(for clarity) to have the **line** from input B to the bottom-right CMOS go around t... 　5

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