

<u>Help</u>

selfpoised ~

<u>Course Progress Dates Course Notes Discussion</u>

★ Course / 4. Combinational Logic / Worked Examples

C

**WE4.3** 

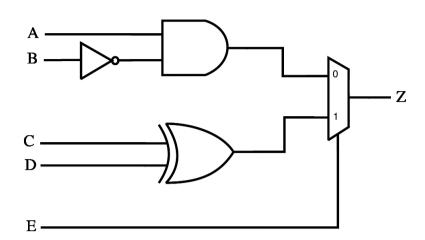
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**■** Calculator

#### Video explanation of solution is provided below the problem.

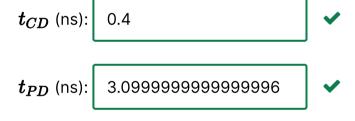
### **Combinational Logic Timing**

2/2 points (ungraded)



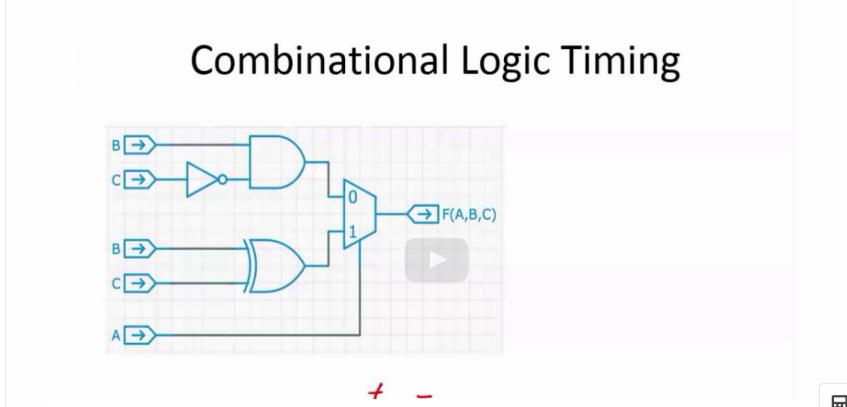
Here's a table showing the  $t_{CD}$  and  $t_{PD}$  for each of the components in the circuit above. Please compute  $t_{CD}$  and  $t_{PD}$  for the circuit as a whole.

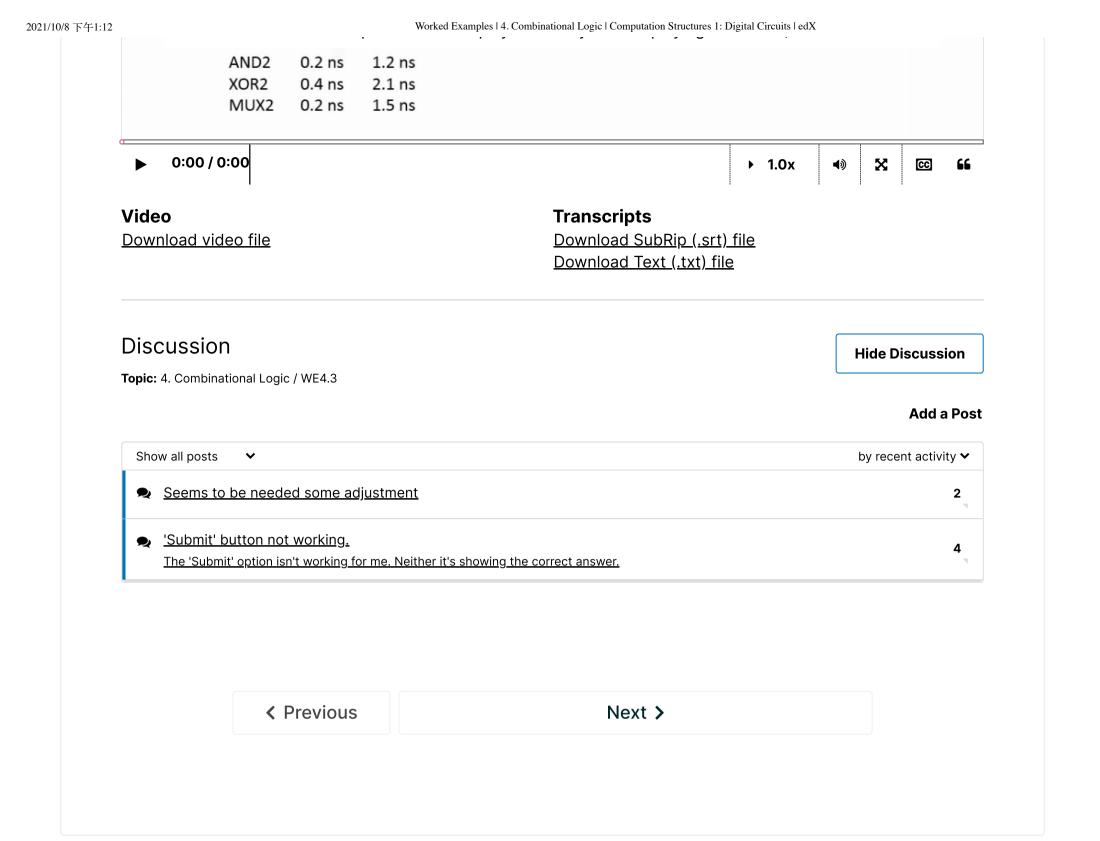
	$t_{CD}$	$t_{PD}$
Inverter	0.2 ns	0.6 ns
AND2	0.3 ns	1.0 ns
XOR2	0.7 ns	1.7 ns
MUX2	0.4 ns	1.4 ns



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#### **Combinational Logic Timing**





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