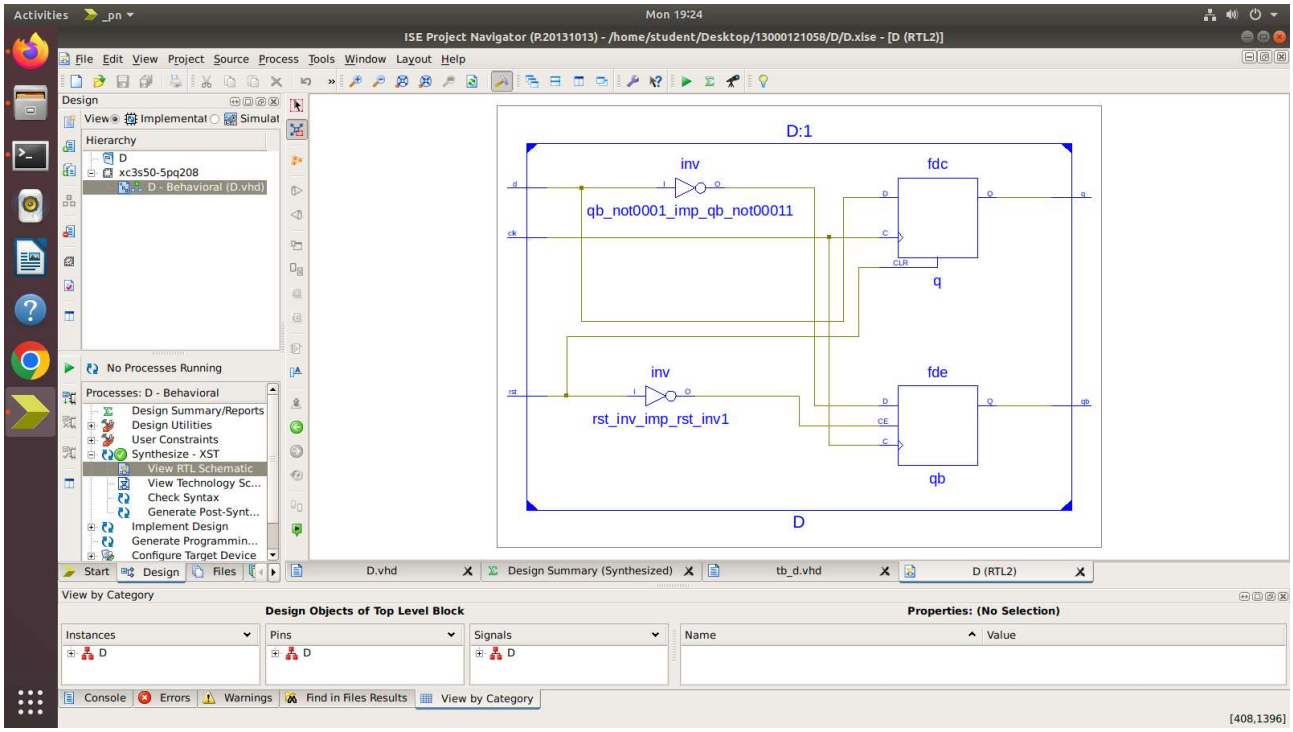
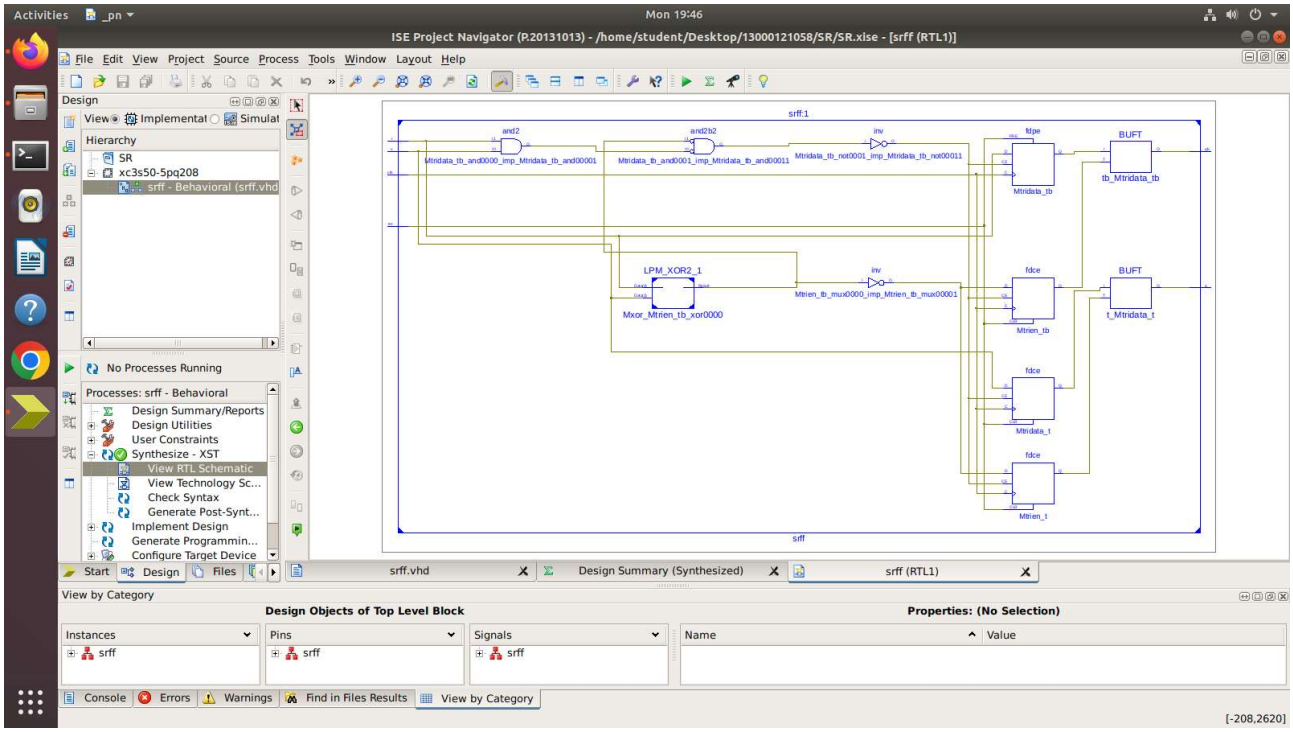
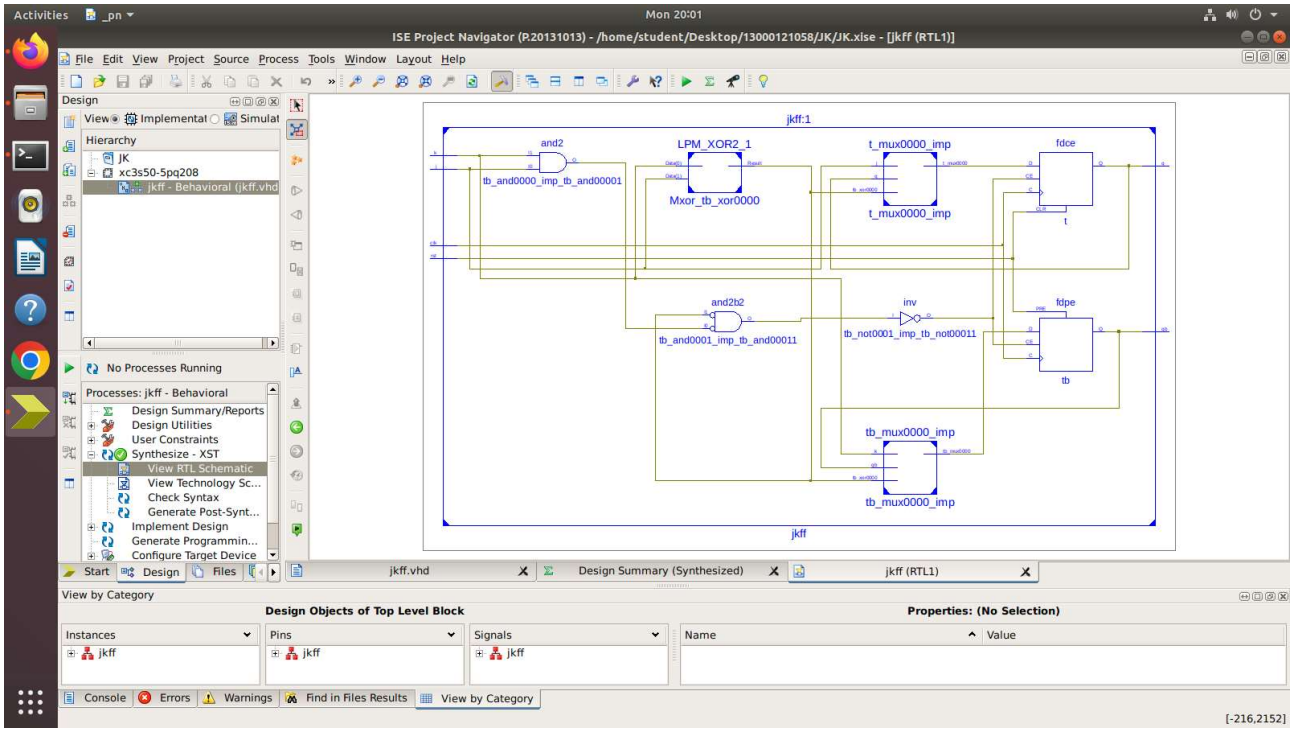
SCHEMATIC OUTPUT



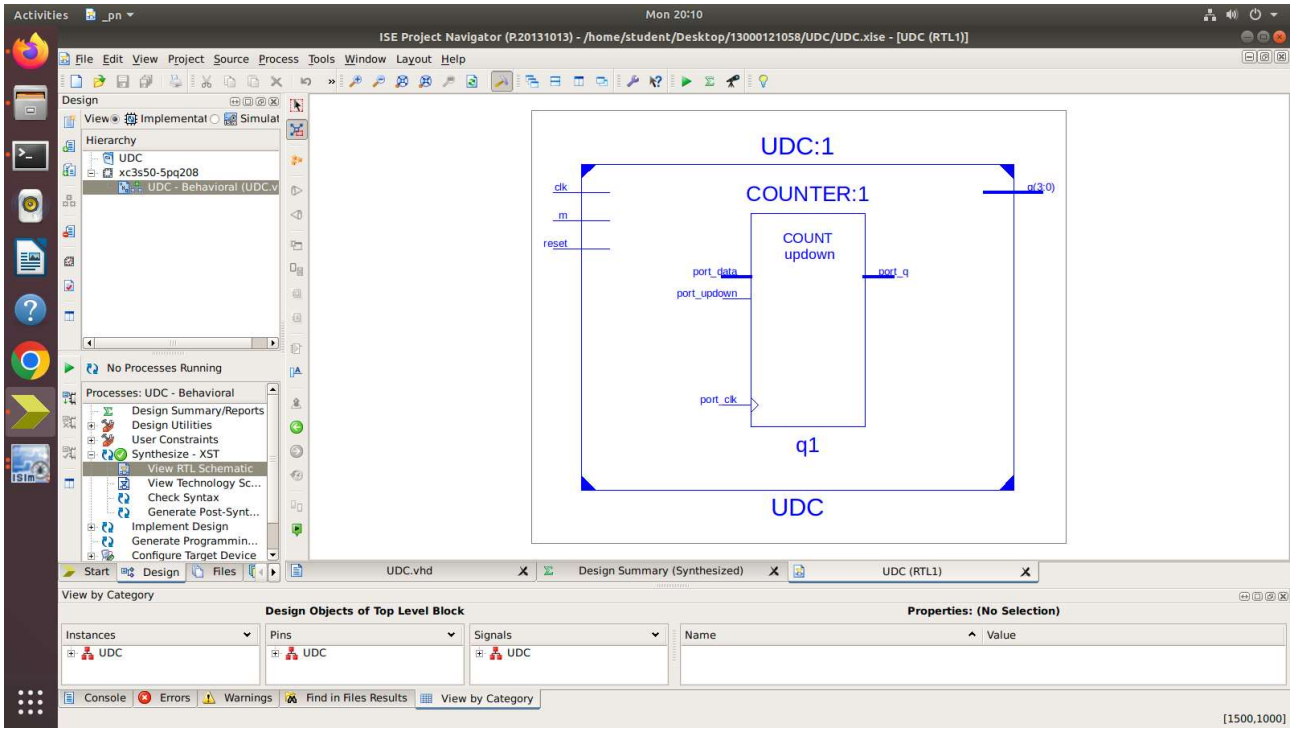
SCHEMATIC OUTPUT



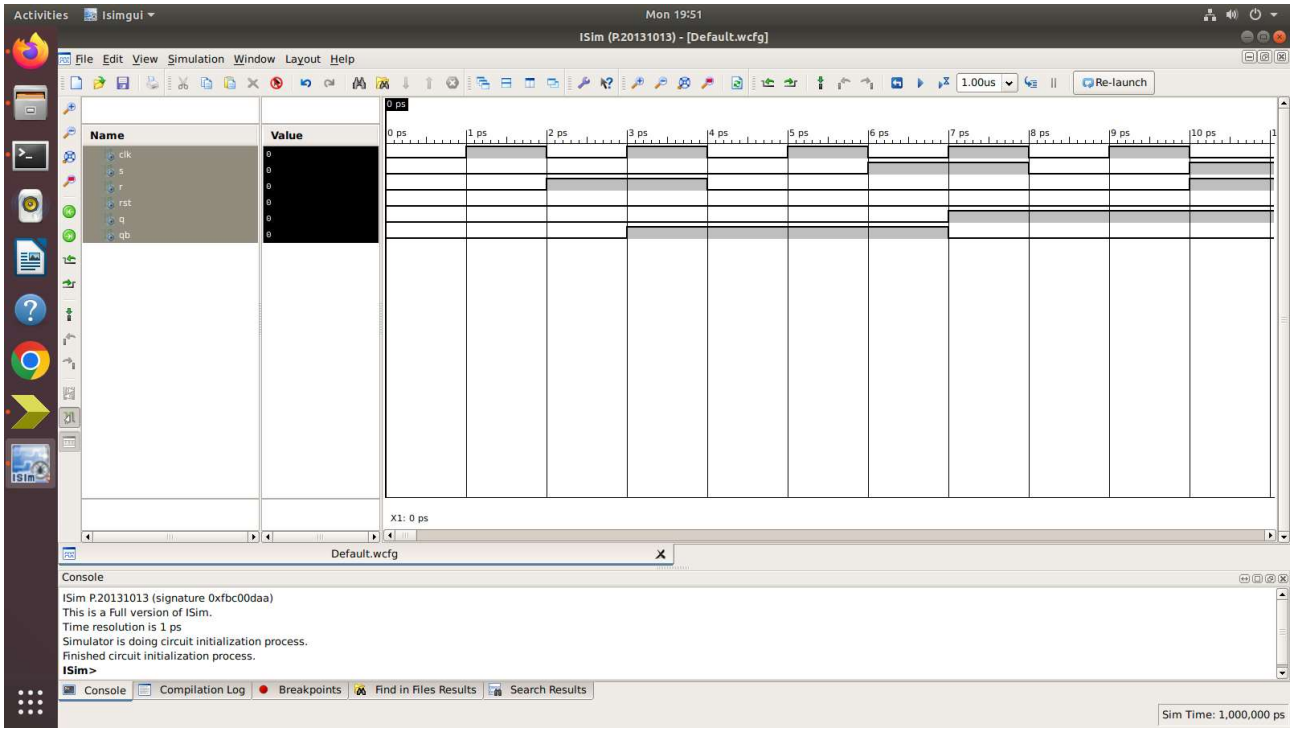
SCHEMATIC OUTPUT



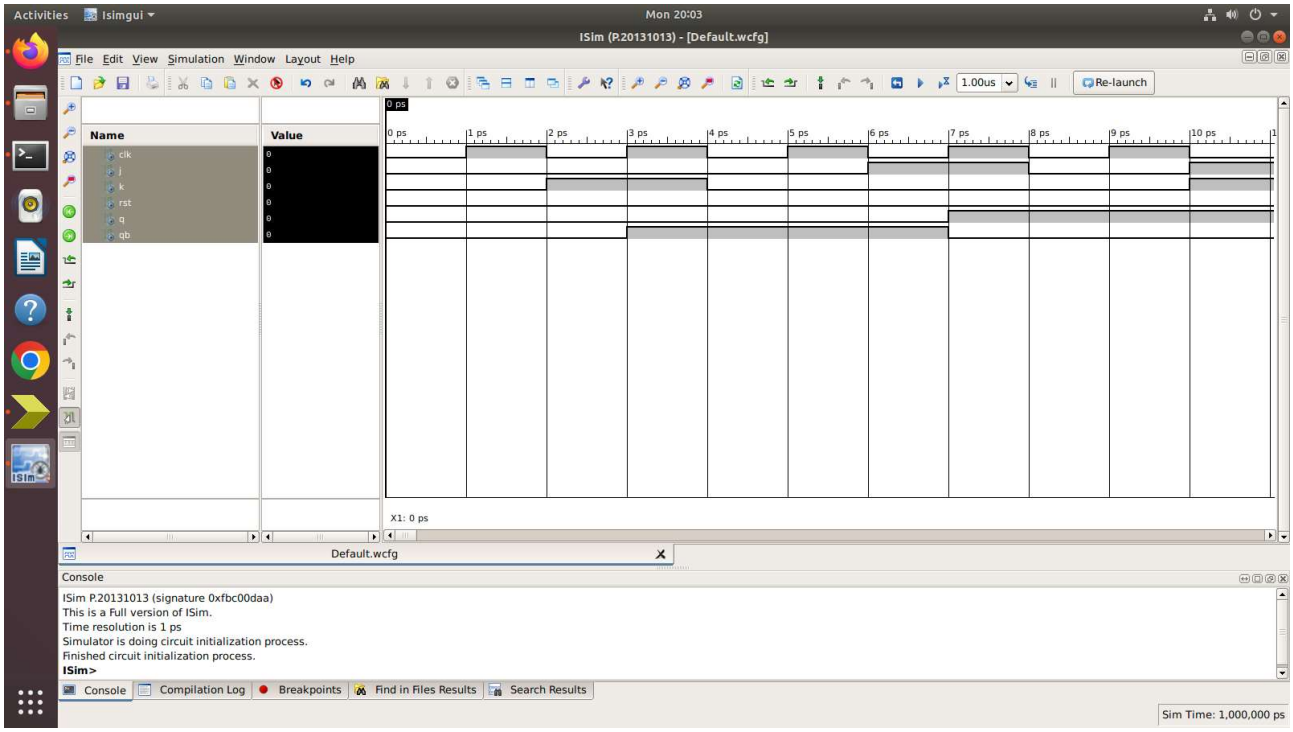
SCHEMATIC OUTPUT



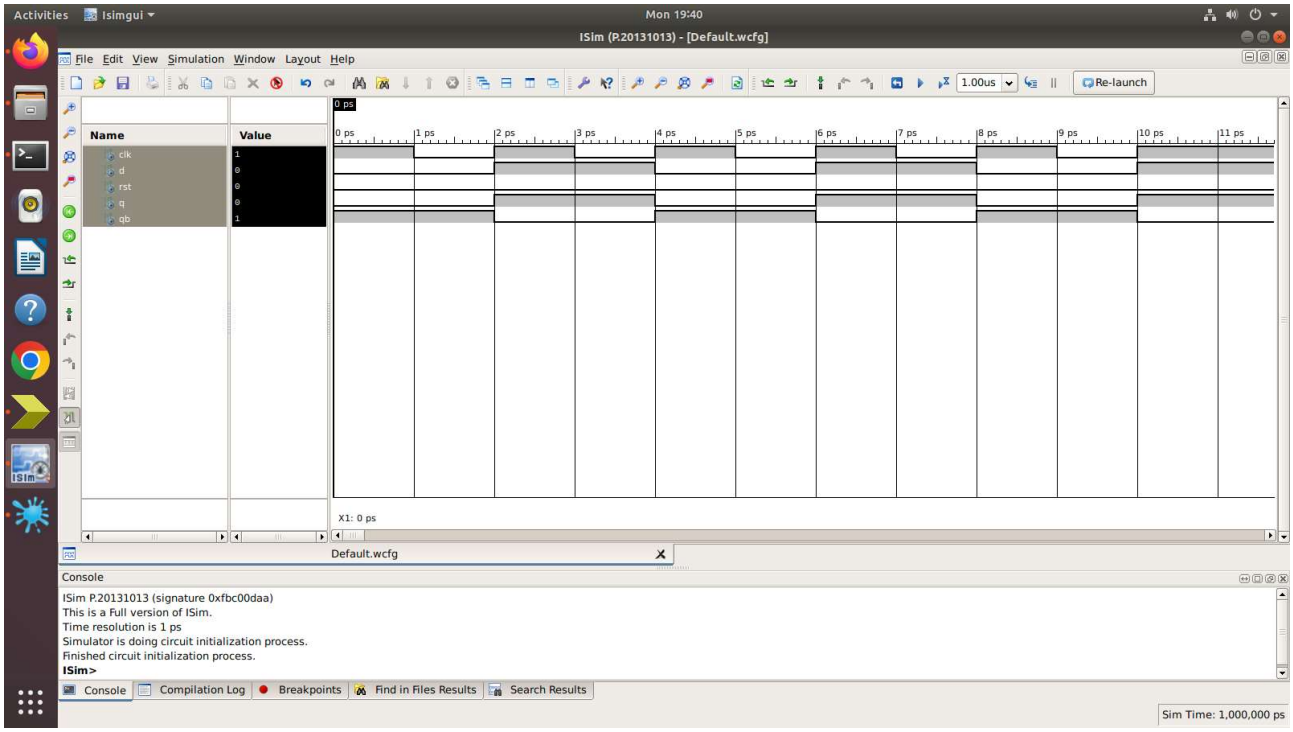
TEST OUTPUT


Activities
Isimgui
Mon 19:51
ISim (P.20131013) - [Default.wcfg]
File Edit View Simulation Window Layout Help
1.00us
Re-launch
Name
Value
clk
s
r
rst
q
qb
0 ps
1 ps
2 ps
3 ps
4 ps
5 ps
6 ps
7 ps
8 ps
9 ps
10 ps
X1: 0 ps
Default.wcfg
Console
ISim P.20131013 (signature 0xfbc00daa)
This is a Full version of ISim.
Time resolution is 1 ps
Simulator is doing circuit initialization process.
Finished circuit initialization process.
ISim>
Console
Compilation Log
Breakpoints
Find in Files Results
Search Results
Sim Time: 1,000,000 ps

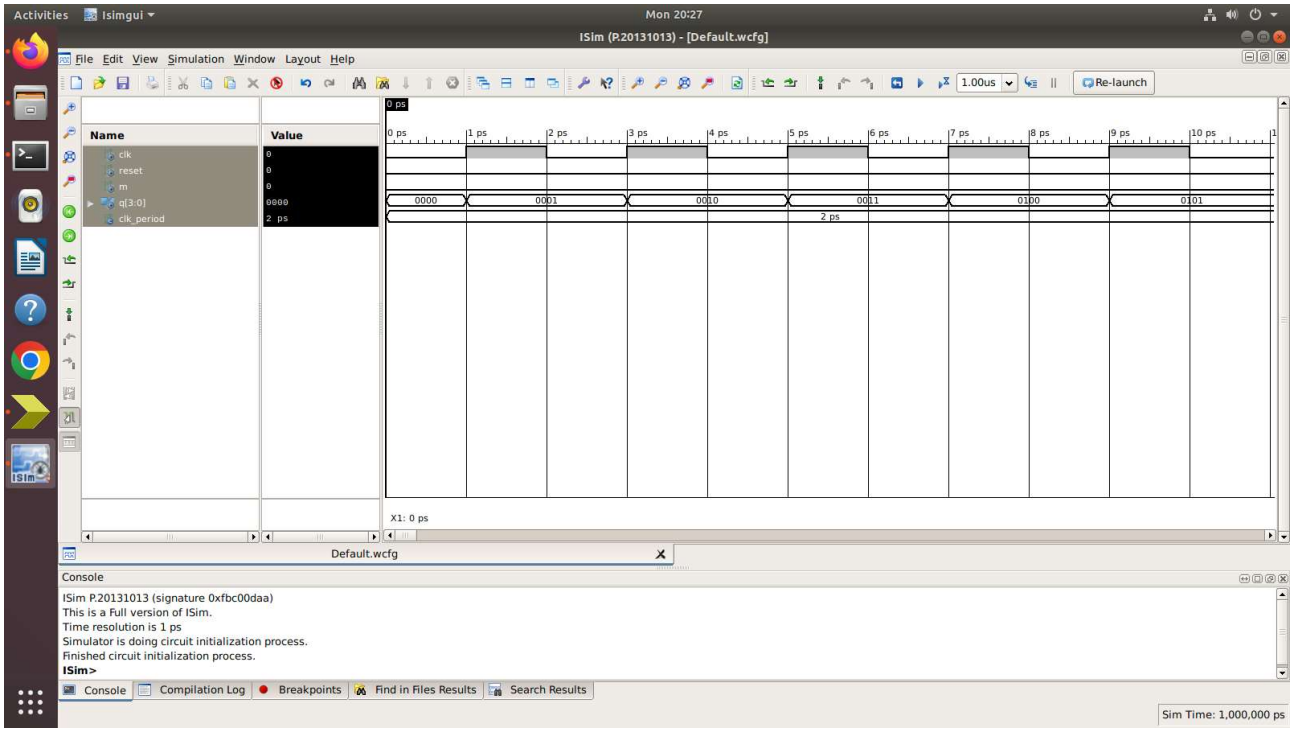
TEST OUTPUT


Activities
Isimgui
Mon 20:03
ISim (P.20131013) - [Default.wcfg]
File Edit View Simulation Window Layout Help
1.00us
Re-launch
Name
Value
clk
j
k
rst
q
qb
0 ps
1 ps
2 ps
3 ps
4 ps
5 ps
6 ps
7 ps
8 ps
9 ps
10 ps
X1: 0 ps
Default.wcfg
Console
ISim P.20131013 (signature 0xfbc00daa)
This is a Full version of ISim.
Time resolution is 1 ps
Simulator is doing circuit initialization process.
Finished circuit initialization process.
ISim>
Console
Compilation Log
Breakpoints
Find in Files Results
Search Results
Sim Time: 1,000,000 ps

TEST OUTPUT

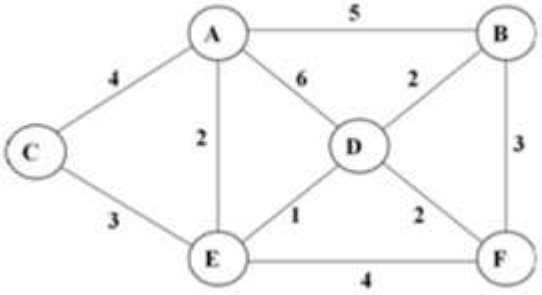

Activities
Isimgui
Mon 19:40
ISim (P.20131013) - [Default.wcfg]
File Edit View Simulation Window Layout Help
1.00us
Re-launch
Name
Value
clk
d
rst
q
qb
0 ps
1 ps
2 ps
3 ps
4 ps
5 ps
6 ps
7 ps
8 ps
9 ps
10 ps
11 ps
X1: 0 ps
Default.wcfg
Console
ISim P.20131013 (signature 0xfbc00daa)
This is a Full version of ISim.
Time resolution is 1 ps
Simulator is doing circuit initialization process.
Finished circuit initialization process.
ISim>
Console
Compilation Log
Breakpoints
Find in Files Results
Search Results
Sim Time: 1,000,000 ps

TEST OUTPUT

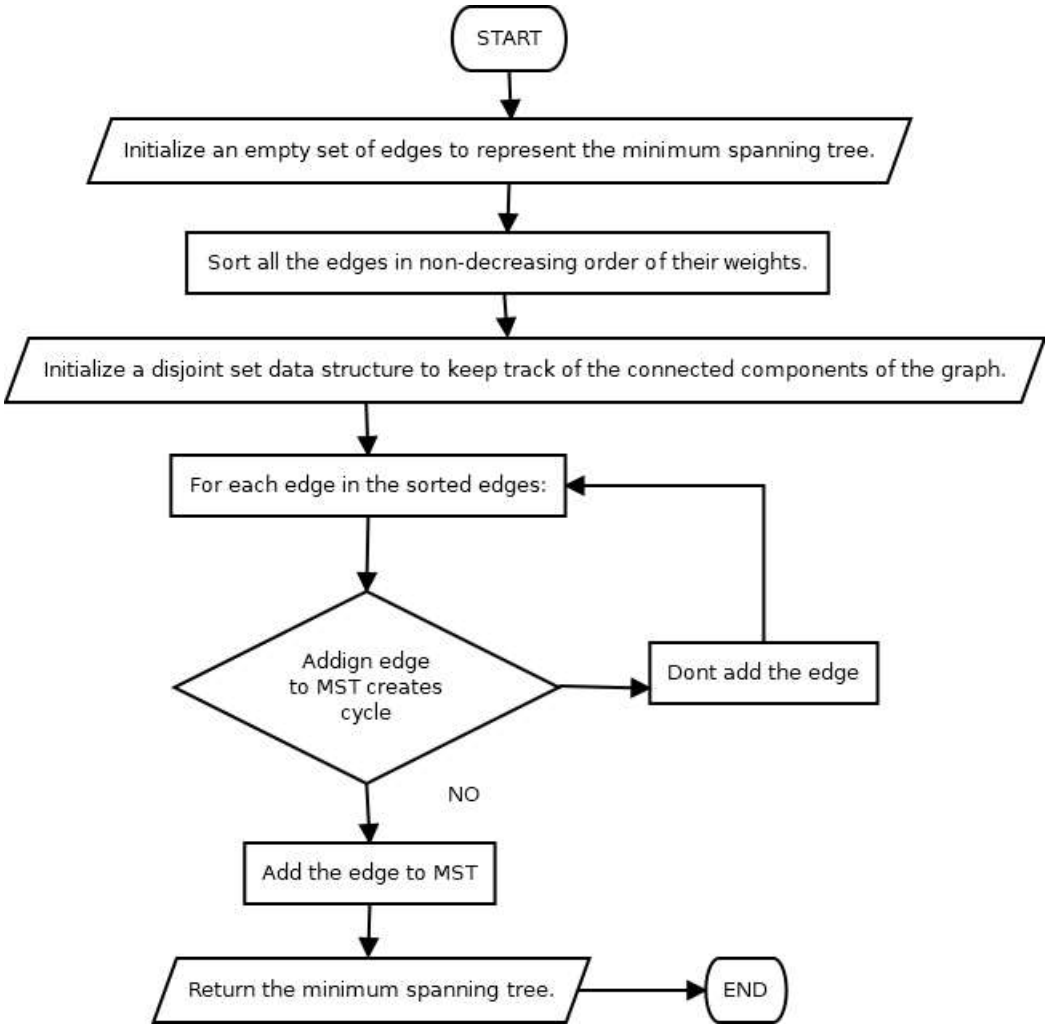

Activities
Isimgui
Mon 20:27
ISim (P.20131013) - [Default.wcfg]
File Edit View Simulation Window Layout Help
1.00us
Re-launch
Name
Value
clk
reset
m
q[3:0]
clk_period
0
0
0
0000
2 ps
0 ps
1 ps
2 ps
3 ps
4 ps
5 ps
6 ps
7 ps
8 ps
9 ps
10 ps
0000
0001
0010
0011
0100
0101
2 ps
X1: 0 ps
Default.wcfg
Console
ISim P.20131013 (signature 0xfbc00daa)
This is a Full version of ISim.
Time resolution is 1 ps
Simulator is doing circuit initialization process.
Finished circuit initialization process.
ISim>
Console
Compilation Log
Breakpoints
Find in Files Results
Search Results
Sim Time: 1,000,000 ps

ASSIGNMENT 7.2

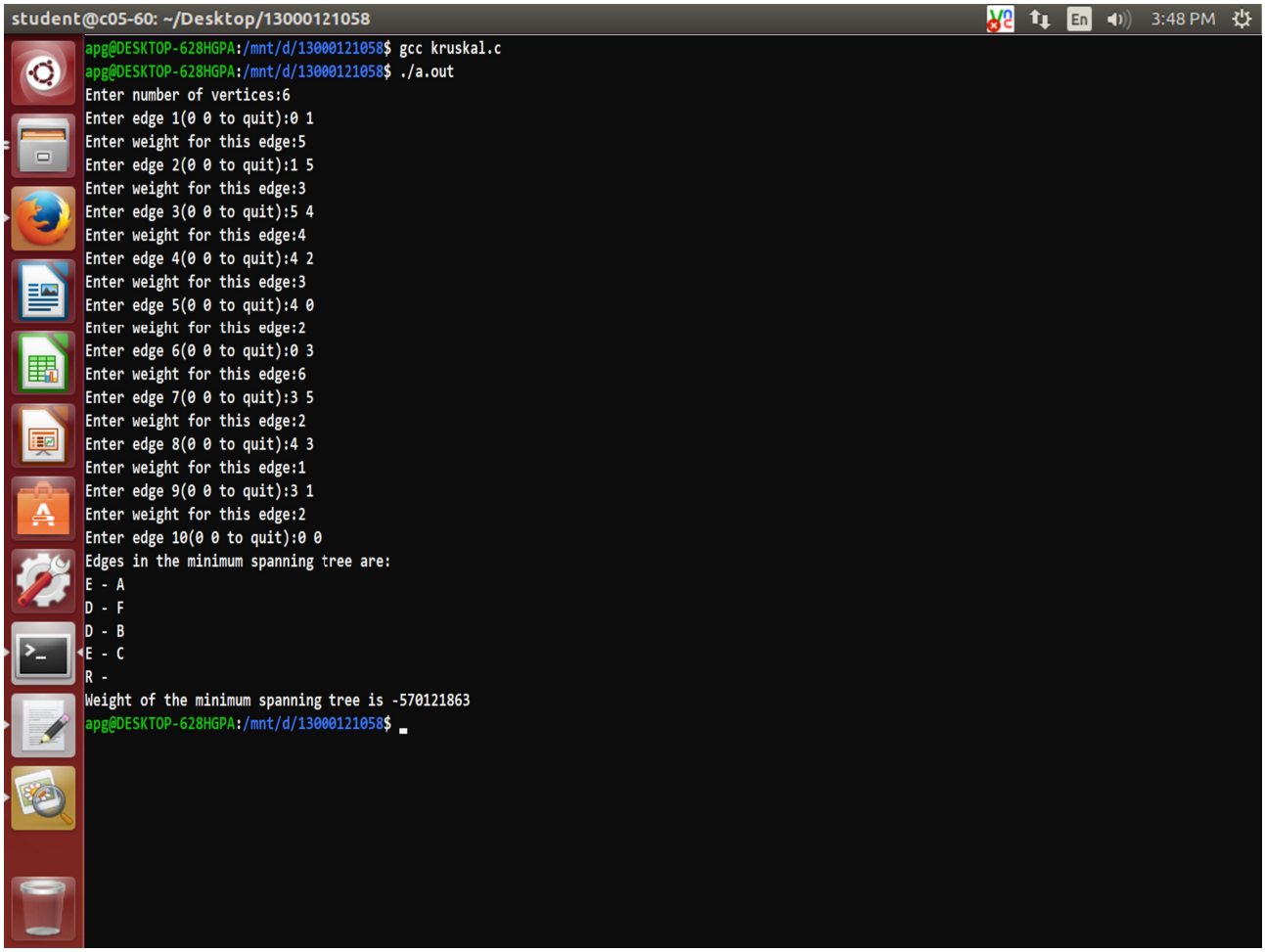
WAP using greedy method to find the MST for the given graph using the Kruskal's Algorithm.



FLOWCHART



OUTPUT


student@c05-60: ~/Desktop/13000121058
En
3:48 PM
apg@DESKTOP-628HGPA:/mnt/d/13000121058$ gcc kruskal.c
apg@DESKTOP-628HGPA:/mnt/d/13000121058$ ./a.out
Enter number of vertices:6
Enter edge 1(0 0 to quit):0 1
Enter weight for this edge:5
Enter edge 2(0 0 to quit):1 5
Enter weight for this edge:3
Enter edge 3(0 0 to quit):5 4
Enter weight for this edge:4
Enter edge 4(0 0 to quit):4 2
Enter weight for this edge:3
Enter edge 5(0 0 to quit):4 0
Enter weight for this edge:2
Enter edge 6(0 0 to quit):0 3
Enter weight for this edge:6
Enter edge 7(0 0 to quit):3 5
Enter weight for this edge:2
Enter edge 8(0 0 to quit):4 3
Enter weight for this edge:1
Enter edge 9(0 0 to quit):3 1
Enter weight for this edge:2
Enter edge 10(0 0 to quit):0 0
Edges in the minimum spanning tree are:
E - A
D - F
D - B
E - C
R -
Weight of the minimum spanning tree is -570121863
apg@DESKTOP-628HGPA:/mnt/d/13000121058$