

# Course Outcome

After this course student will be able to

CO1	Construct suitable combinational or sequential module's solution circuit schematic in Xilinx platform using VHDL
CO2	Develop a formal test bench using informal requirements in VHDL and simulate the test cases to check the productivity of the circuit.
CO3	Construct integrated circuit designs like memory and processing units and further integrate them together to observe their behavioral and functional execution using Xilinx and ISim
CO4	Inspect various schematic presentation and simulation features of Xilinx as an environment through hardware description language and gather expertise as an individual
CO5	Compose structured and informative report of the solution to the corresponding problem along with verifies test cases.