

The Terramaster F2 motherboard has a PCIe x4 connector with non-standard pinout. It provides 12V, 5V and 3.3V power rails, 2xSATA and 2xPCIe (gen 2) 1-lane. The purpose of pins A1, A11, A32, B10, B11, B30 and B31 are unknown to me but leaving them unconnected result in a working backplane. $\sqrt{(y)}$ +12V +5V +3.3V This little circuit was on my existing F2-221's B6 SATA1_A-SATA1_Abackplane PCB. I added it here just in case but SATA1_B+ A5 —⊲SATA1_B+ leaving it unpopulated seems to be fine. B31 SATA1_B- A6 SATA2_A-B15 USATA2_A-SATA2_B+ 16 SATA2_B+ SATA2_B-PE1_TX+ \$19 OPE_TX+ PE1_TX-B20 QPE_TX-PE1_RX+ PE1_RX+
PE1_RXPE1_REFCLK+
PE1_REFCLK+
PE1_REFCLK+
PE1_REFCLK+
PE1_REFCLK+
PE1_REFCLK+
PE1_RX+
PE1_RX PE1_REFCLK-PERST 17 OPERST J1 PCIEx4_HOST GND Sheet: /Connector/ File: connector.kicad_sch Title: F3 Backplane Connector Size: A4 Date: Rev: 1.0 KiCad E.D.A. kicad 7.0.11 ld: 2/5





