```
Stack
                                     a: edx
                                                           p: esp + 8
foo(int a, int b) {
                   allocations
                                    a: esp + 20
                                                           b: esp + 24
  int *p, *a;
                  foo:
                      subl $16, %esp
                                              // Allocate 16-byte stack frame
  p = &a;
                      lea 20(\%esp), 8(\%esp) // Put &a(esp+20) into p(esp+8)
                      store ..., (%edx)
                                              // Store to MEM[q]
  *a = ...;
                      load 8(%esp),%ecx
                                              // Temp ecx \leftarrow p (same as &a)
  ... = b;
                      load 4(%ecx)
                                               // Load "b" by using the fact that
                                                  \&b = \&a + 4 = ecx + 4
Source Code
                     Pseudo Assembly Code
```