

Arithmetic and Logic Unit

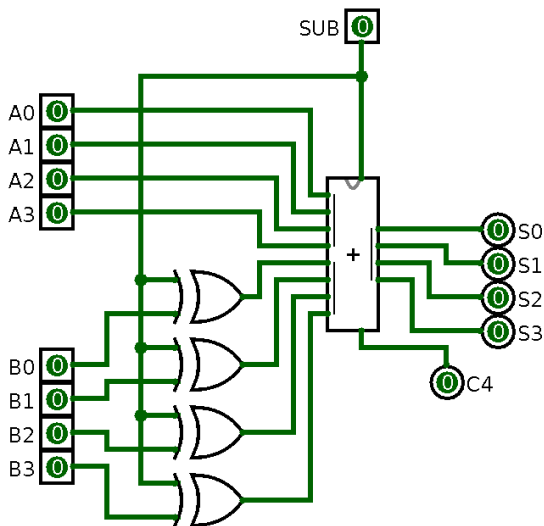
Praktikum Rangkaian Digital

Ilmu Komputer IPB

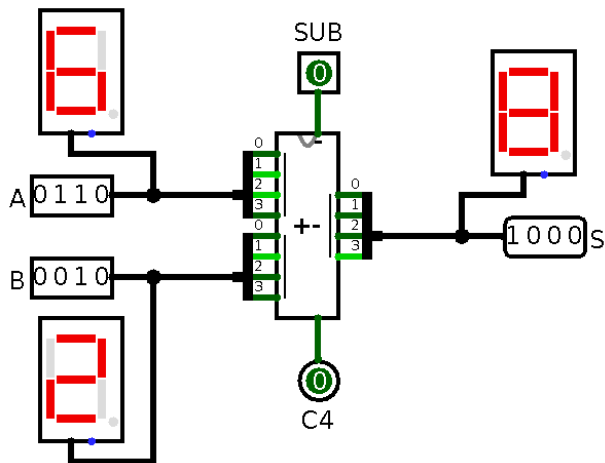
2020

Adder-Subtractor 4-Bit

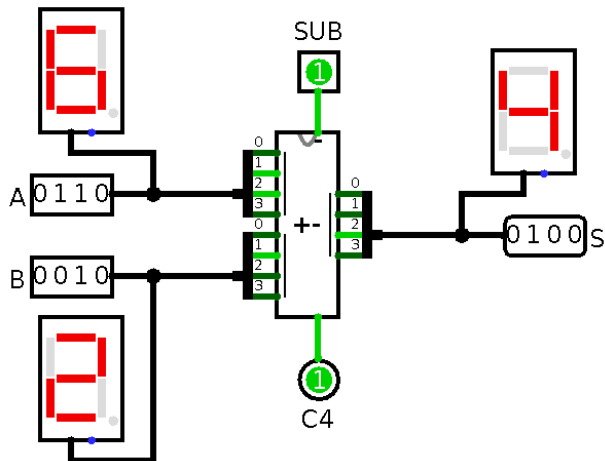
Adder-Subtractor 4-Bit



Adder-Subtractor 4-Bit Test

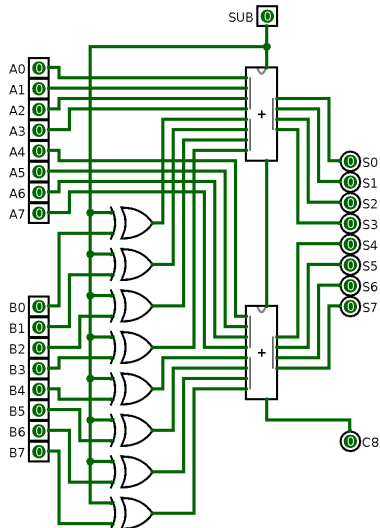


Adder-Subtractor 4-Bit Test

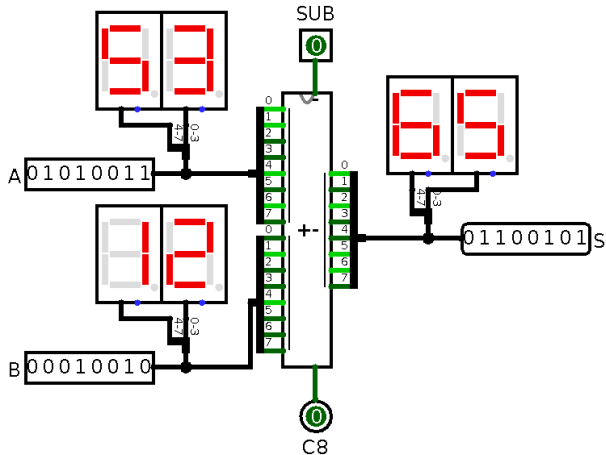


Adder-Subtractor 8-Bit

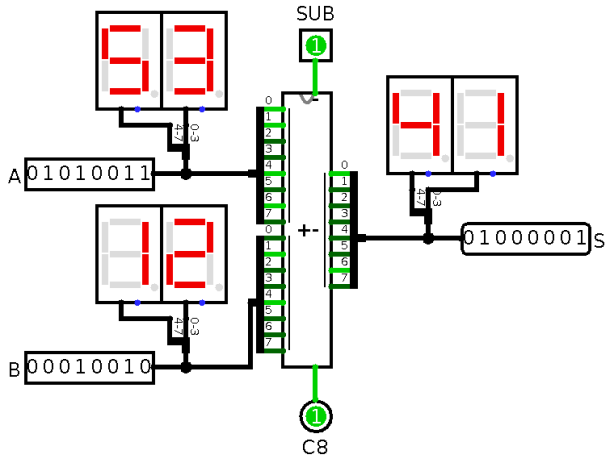
Adder-Subtractor 8-Bit



Adder-Subtractor 8-Bit Test

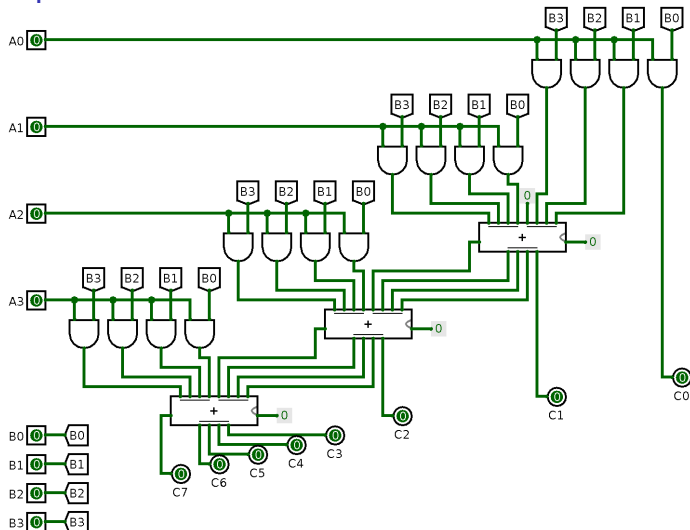


Adder-Subtractor 8-Bit Test

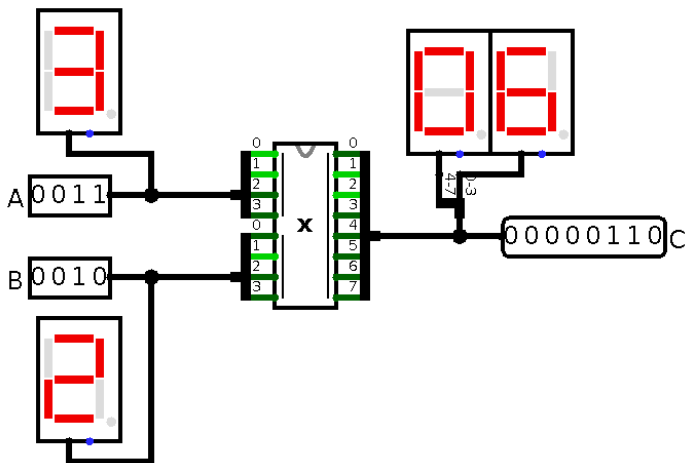


Multiplier 4-Bit

Multiplier 4-Bit



Multiplier 4-Bit Test



Tugas

Simulasi

- ▶ Buat simulasi pada Logisim:
 - ▶ adder-subtractor 4-bit
 - ▶ multiplier 4-bit
- ▶ Gunakan templat rangkaian adder di LMS
- ▶ Penilaian langsung pada saat praktikum oleh asprak