Ian Hsieh

TESDA

S U M M A R Y : Electrical and Software Engineer with 2+ years

Experience in Analog and Mix-Signal IC Design, Digital IC Design and Design Verification, Development of EDA Tools

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Others:

EDUCATION

PhD of Electrical and Computer Engineering

National Yang Ming Chiao Tung University 2022-Present

Bachelor of Electrical and Computer Engineering

National Yang Ming Chiao Tung University 2019-2022

SKILL & EXPERIENCE

R&D Engineer

Taiwan Electronic System Design Automation (TESDA), 2023.06-Present

SoC Verification and Development of EDA Tools

Computer Vision Development Intern

Industrial Technology Research Institute (ITRI) 2023.05-2023.06

Deep Learning Algorithm Development and Model Training

Research Assistant

Novatek Technology, 2022.09-Present

High Resolution and Low Noise All Digital Phased-Locked Loops with ADC

Analog IC Intern

Richtek Technology, 2022.07-2022.08

IP Function Validity and IC Measurement