USB-C Clock Delay MCU (RP2040) 12-bit 3.3 volts / 4096 = ~805 uV GND GND V6 AD5241BRZ1M U7 © IS25LP128F-JBLE D6 2040_L0 Power ECS-120-18-33-JEM-TR **Timestamp Counter** GND GND GND Added hidden Pin 4 to MCP1826S Symbol GPI027_ADC1 GPI028_ADC2 R6 4k7 D2 3v3 R5 4k7 D1 5V R7 4k7 D3 1v1 U1 RP2040 74HC4520D **Extension Connectors Probe Inputs** A15 2 A14 3 A13 4 A12 5 A11 6 A10 7 A9 8 A8 9 A7 | 15 BUF04 | TP105 | TP104 | TP105 | T baldengineer.com Sheet: / File: state mode logic analyzer.kicad_sch Title: State Mode Logic Analyzer