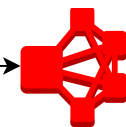
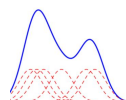
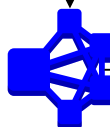
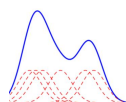
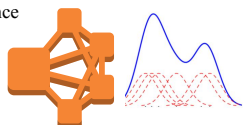


Inefficient rejection
sampling (RS) loops
replaced with
amortized inference
procedures



Using amortized inference,
combined with our *hijacking* scheme
we can utilize **more inference
engines** and **increase the
computational efficiency** of the
simulator.