

# Bruno Magalhaes

## Research Engineer for High Performance Computing, Simulation & Machine Learning

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📍 Lausanne, Switzerland    [in](#) linkedin.com/in/brunomaga    [G](#) github.com/brunomaga  
🇵🇹 Native in Portuguese, fluent in English and French, proficient in Spanish and fair in Slovenian  
🎮 Hobbies : waterpolo, skiing, reading, travelling, cooking, guitar



## 📁 Work Experience

ongoing Sep 2019	AI Resident, Microsoft Research, Cambridge, UK
Aug 2019 Mar 2015	<b>Doctoral Assistant ▸ Postdoctoral Researcher, École Polytechnique Fédérale de Lausanne, Switzerland</b> <ul style="list-style-type: none"><li>➢ Research, conceptualization and implementation of new methods for asynchronous execution of the simulation of detailed neural networks on large networks of highly-heterogeneous compute nodes</li><li>➢ Contributions focus on distributed micro-parallelism of individual neurons via branch- and graph-based parallelism, distributed fully-asynchronous execution models, and high-accuracy solutions via variable timestep interpolations</li><li>➢ Performed 400 hours of teaching assistant duties for Unsupervised and reinforcement learning in neural networks, Projects in neuroinformatics and <i>In silico</i> neuroscience</li></ul> <div>C C++ Python HPX-5 MPI <math>\LaTeX</math> tensorflow google test TCLAP Sundials CVODE</div>
Feb 2015 Mar 2011	<b>Research Engineer for High Performance Computing, Blue Brain Project, EPFL, Lausanne, Switzerland</b> <ul style="list-style-type: none"><li>➢ Parallel algorithms for spatial decomposition of neural networks</li><li>➢ Parallel algorithms for distributed task-stealing programming models on neural networks</li><li>➢ Parallel algorithms for synaptic map reconstruction via efficient distributed sparse matrix transposition</li><li>➢ Algorithms for the distributed spatial indexing of detailed neuron morphologies</li></ul> <div>C C++ Message Passing Interface (MPI) OpenMP CMake IBM BlueGene/P and /Q parallel IO (MPI, HDF5)</div>
Feb 2011 Sep 2009	<b>Junior Architect for IT infrastructures, Noble Group, Hong Kong, New York, São Paulo &amp; London</b> <ul style="list-style-type: none"><li>➢ Network design of a contingency data centre for all EU Power &amp; Gas trading infrastructure, London, UK</li><li>➢ Network and infrastructure design of a port and warehouse for coffee and soy beans, Santos, Brazil</li><li>➢ Implementation of a web-based software for metals and coffee trading, New York, USA</li></ul> <div>Cisco and 3Com network devices ASP.NET</div>
Oct 2008 Mar 2007	<b>Analyst programmer, MSCI (former IPD - Investment Property Databank), London, UK</b> <ul style="list-style-type: none"><li>➢ Development of a web-based geographical system for real estate data search and analytics</li><li>➢ Development of algorithms for data warehousing and efficient search queries on heterogeneous data structures</li></ul> <div>C# Visual Basic F# ASP .NET MS SQL Server SSIS google maps API javascript</div>

## 🎓 Education

Jun 2019 Mar 2015	<b>PhD Neuroscience, École Polytechnique Fédérale de Lausanne (EPFL), Switzerland</b> <ul style="list-style-type: none"><li>➢ Thesis <i>Asynchronous Simulation of Neuronal Activity</i> nominated for the Neuroscience Doctoral School excellency award and for the IBM research prize in computational sciences</li><li>➢ Visiting scholar at the Center for Research in Extreme Scale Technologies at Indiana University (US), Summers 2015-17</li></ul>
Sep 2009 Oct 2008	<b>MSc Advanced Computing, Imperial College London, UK</b> <ul style="list-style-type: none"><li>➢ Thesis work researching distributed, multi-core CPU and GPU computation of large Markov models awarded distinction and published at NSMC'10. Finished degree with Merit.</li></ul>
Jul 2007 Oct 2002	<b>Licenciatura (5-year BSc/BEng) Systems Engineering and Computer Science, Univ. of Minho, Portugal</b> <ul style="list-style-type: none"><li>➢ Exchange student at the University of Maribor, Slovenia, 2005/2006. Finished degree with A (Top 10%)</li></ul>

## 📄 Publications    peer reviewed ; main author unless mentioned otherwise

ongoing	Efficient Distributed Transposition of Large-Scale Multigraphs And High-Cardinality Sparse Matrices
ongoing	Distributed Asynchronous Execution Model Speeds and Scales Up Over Hundredfold The Detection Of Contacts Between Detailed Neuron Morphologies
submitted	Fully-Asynchronous Fully-Implicit Variable-Order Variable-Timestep Simulation of Neural Networks
2019	Asynchronous SIMD-Enabled Branch-Parallelism of Morphologically-Detailed Neuron Models, Frontiers in Neuroinformatics (PhD thesis) Asynchronous Simulation of Neuronal Activity, EPFL Scientific publications
2019	Fully-Asynchronous Cache-Efficient Simulation of Detailed Neural Networks, Proc. International Conference on Computational Science (ICCS 2019), Faro, Portugal
2019	Exploiting Implicit Flow Graph of System of ODEs to Accelerate the Simulation of Neural Networks, Proc. International Parallel & Distributed Processing Symposium (IPDPS 2019), Rio de Janeiro, Brazil
2016	An efficient parallel load-balancing strategy for orthogonal decomposition of geometrical data, Proc. International Super Computing (ISC 2016), Frankfurt, Germany
2015	(co-author) Reconstruction and Simulation of Neocortical Microcircuitry, Cell 163, 456–492.
2010	(MSc final project) GPU-enabled steady-state solution of large Markov models, Proc. International Workshop on the Numerical Solution of Markov Chains (NSMC 2010), Williamsburg, Virginia