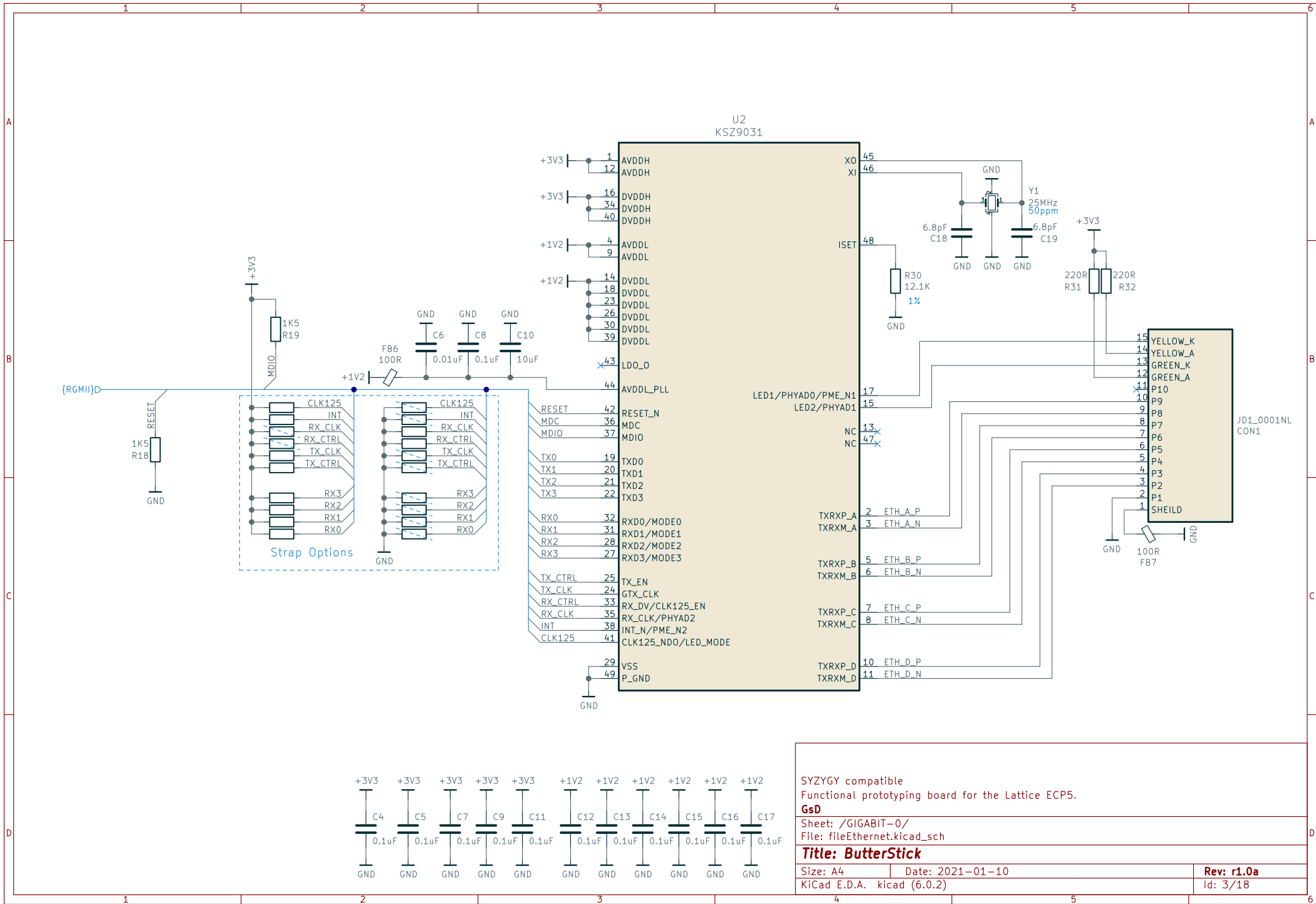
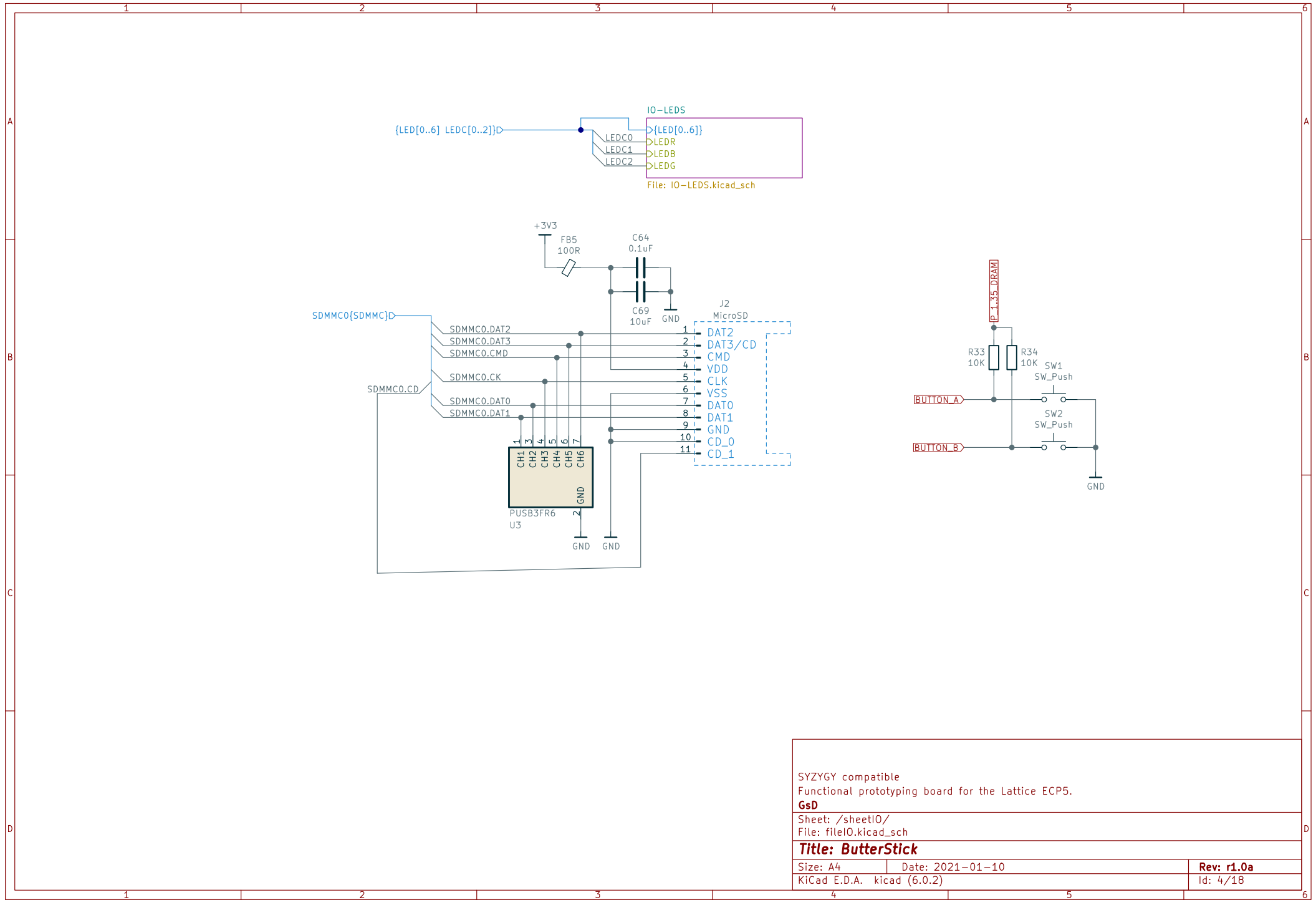


Id: 2/18





SYZGY compatible
Functional prototyping board for the Lattice ECP5.

GsD

Sheet: /sheetIO/
File: fileIO.kicad_sch

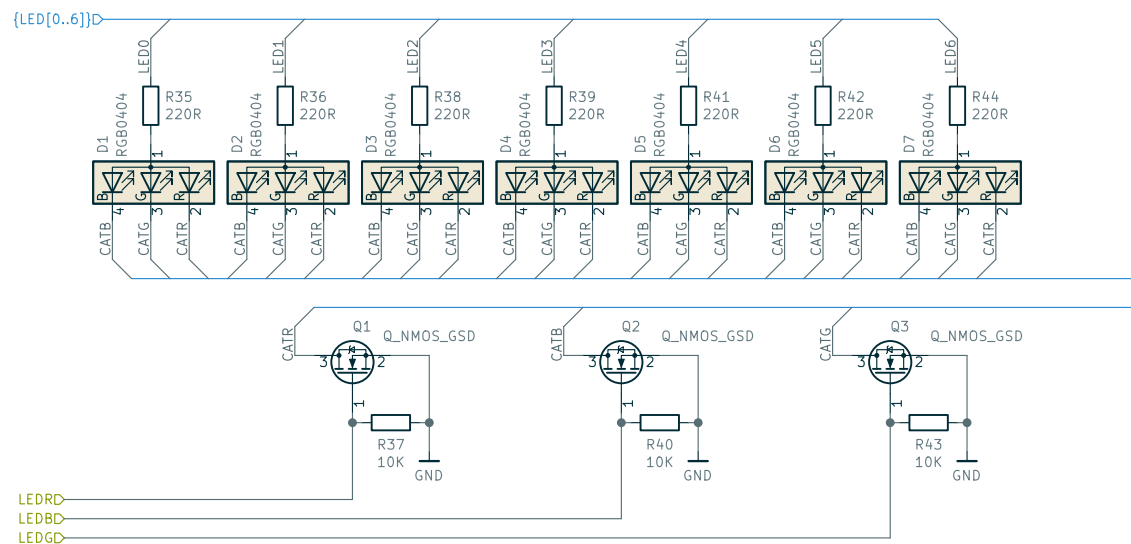
Title: ButterStick

Size: A4 Date: 2021-01-10

KiCad E.D.A. kicad (6.0.2)

Rev: r1.0a

Id: 4/18



SYZGY compatible
Functional prototyping board for the Lattice ECP5.

GsD

Sheet: /sheetIO/IO-LEDS/
File: IO-LEDS.kicad_sch

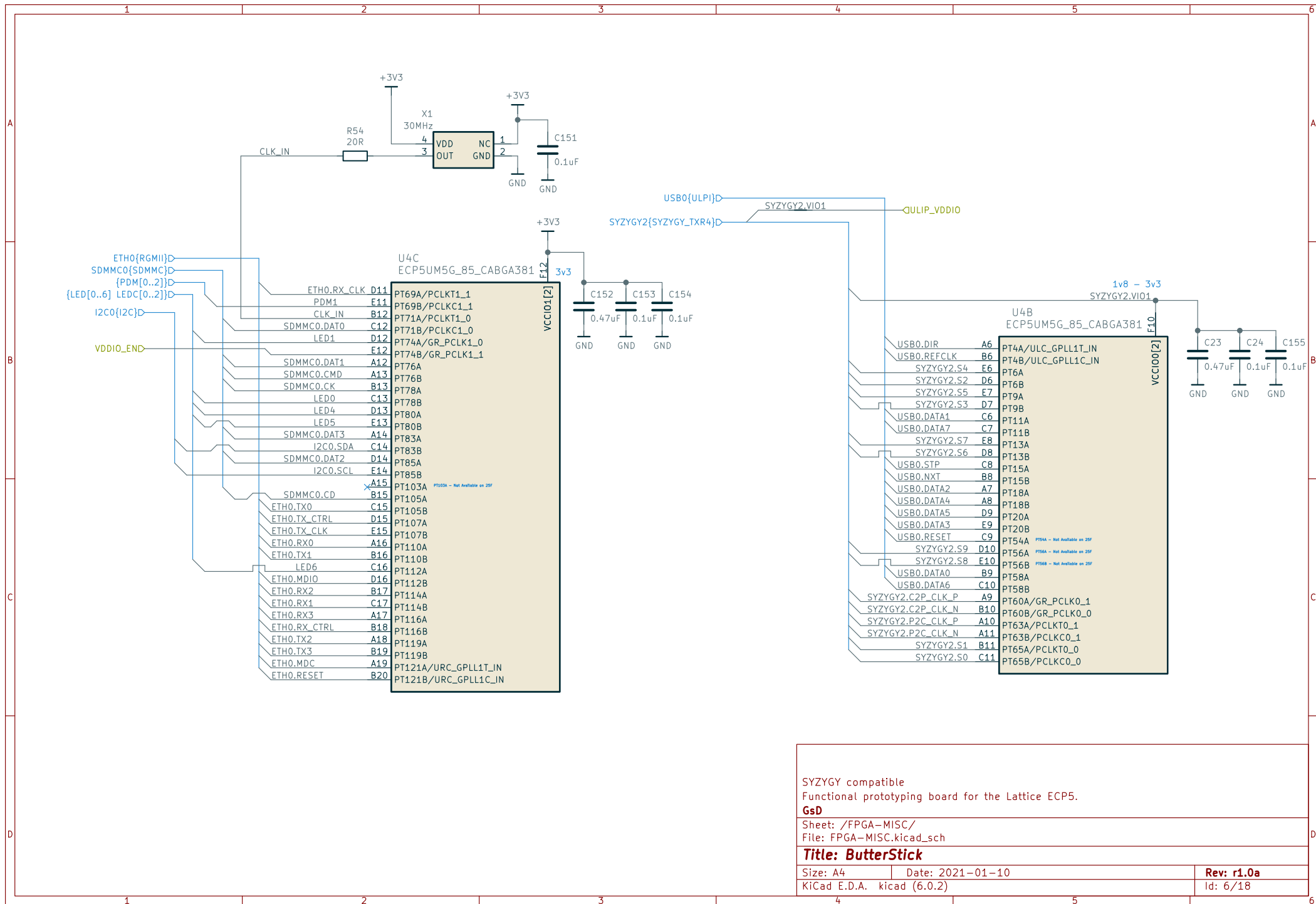
Title: ButterStick

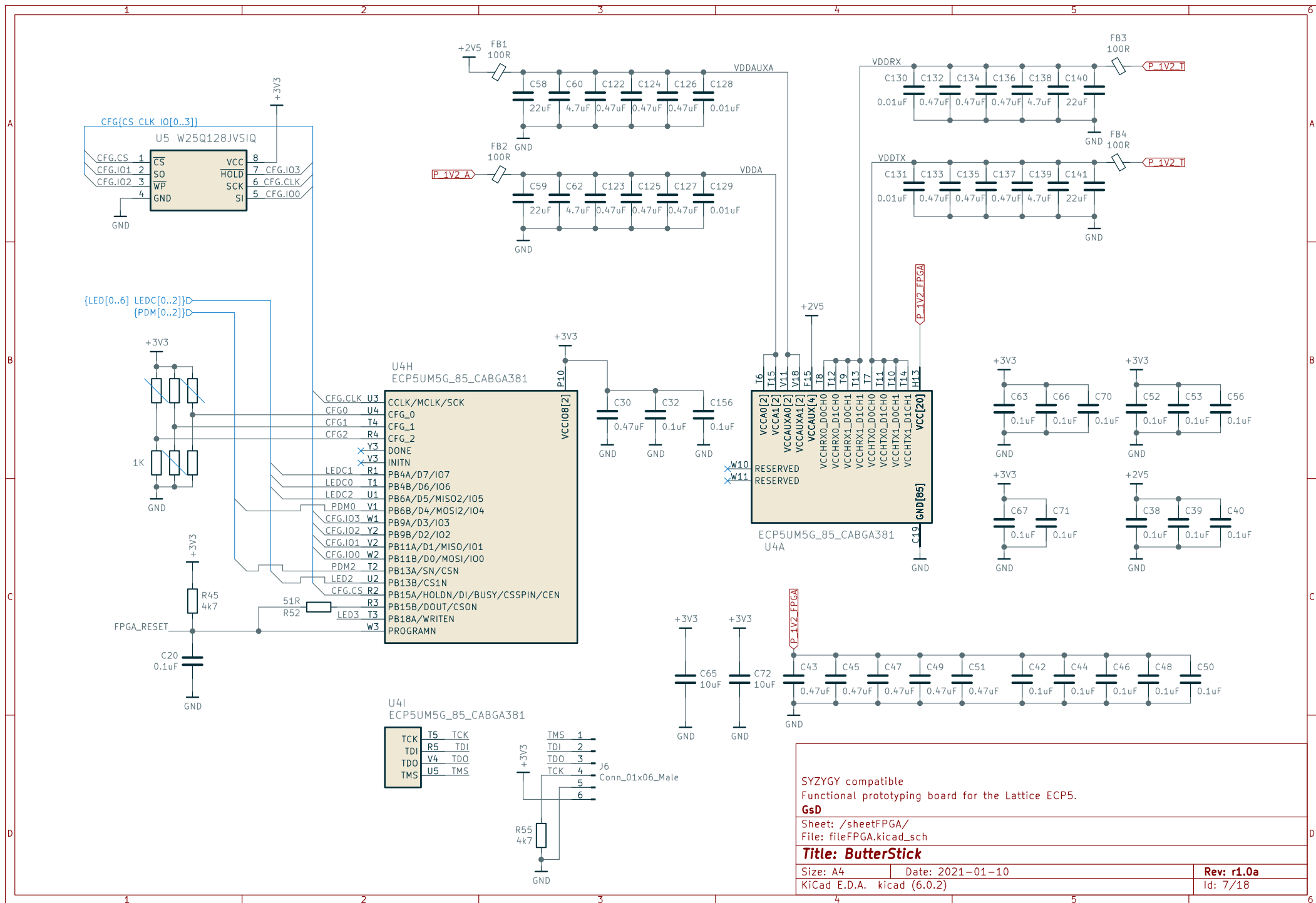
Size: A4 Date: 2021-01-10

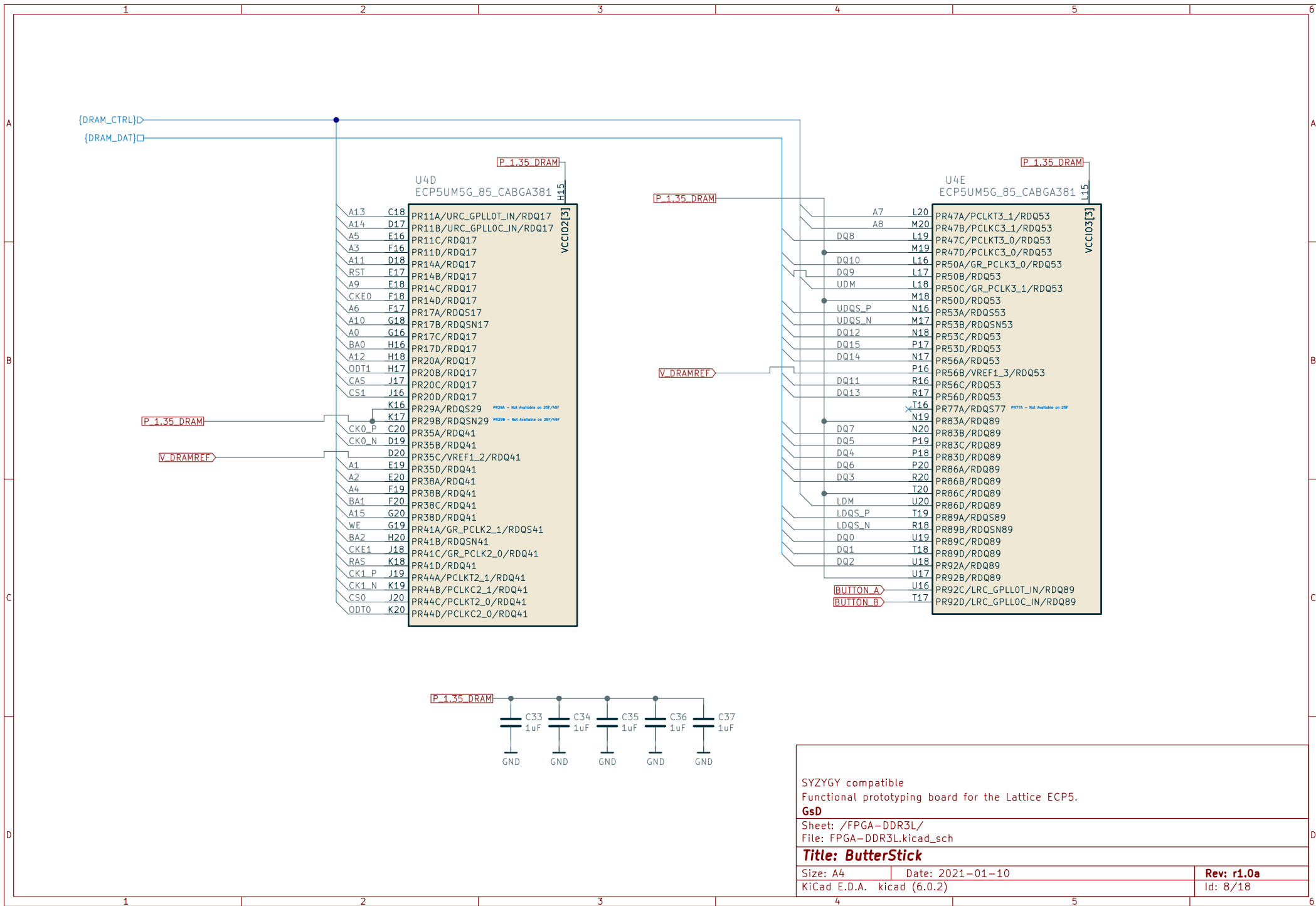
KiCad E.D.A. kicad (6.0.2)

Rev: r1.0a

Id: 5/18







SYZYGY compatible
Functional prototyping board for the Lattice ECP5.

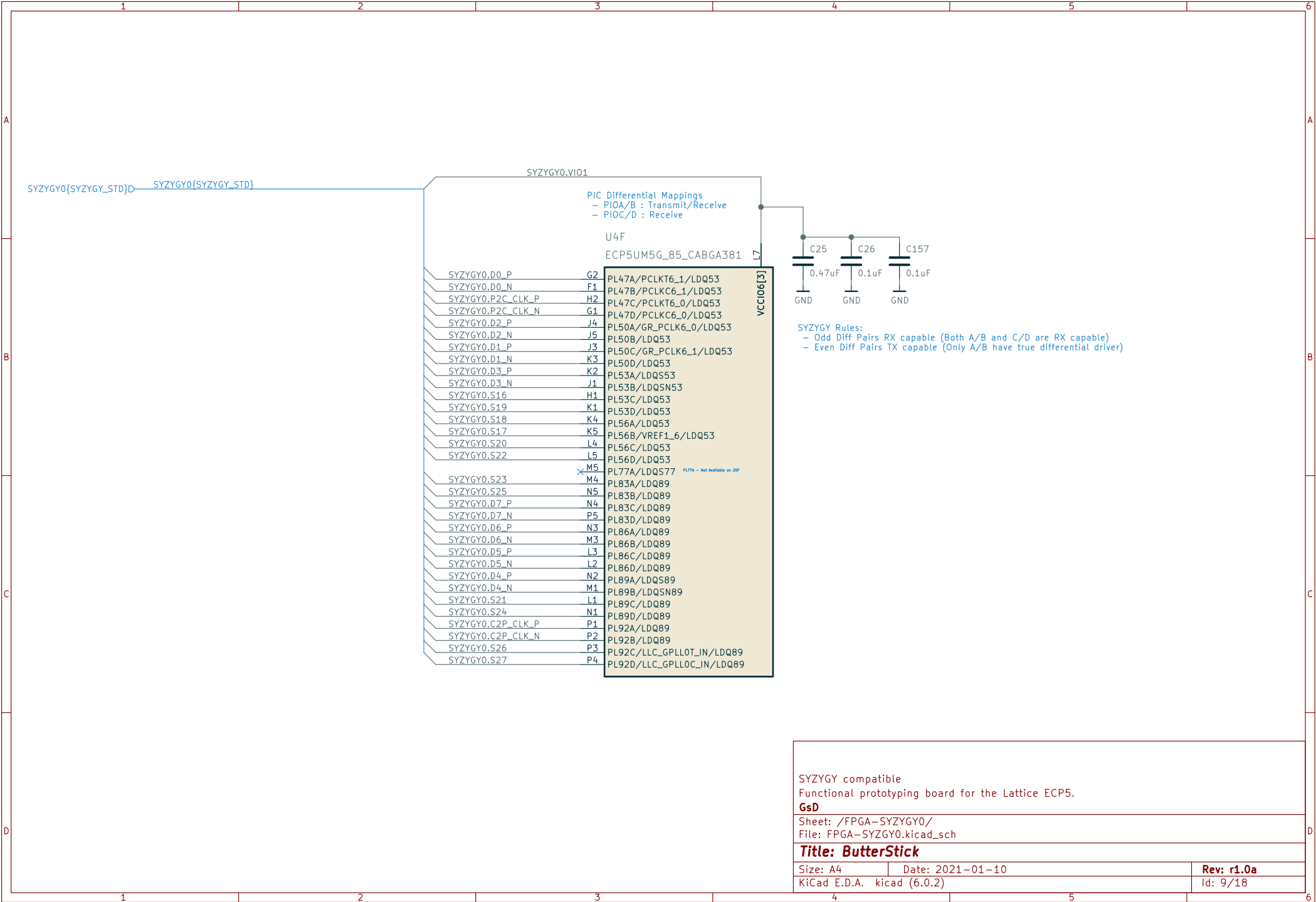
GsD

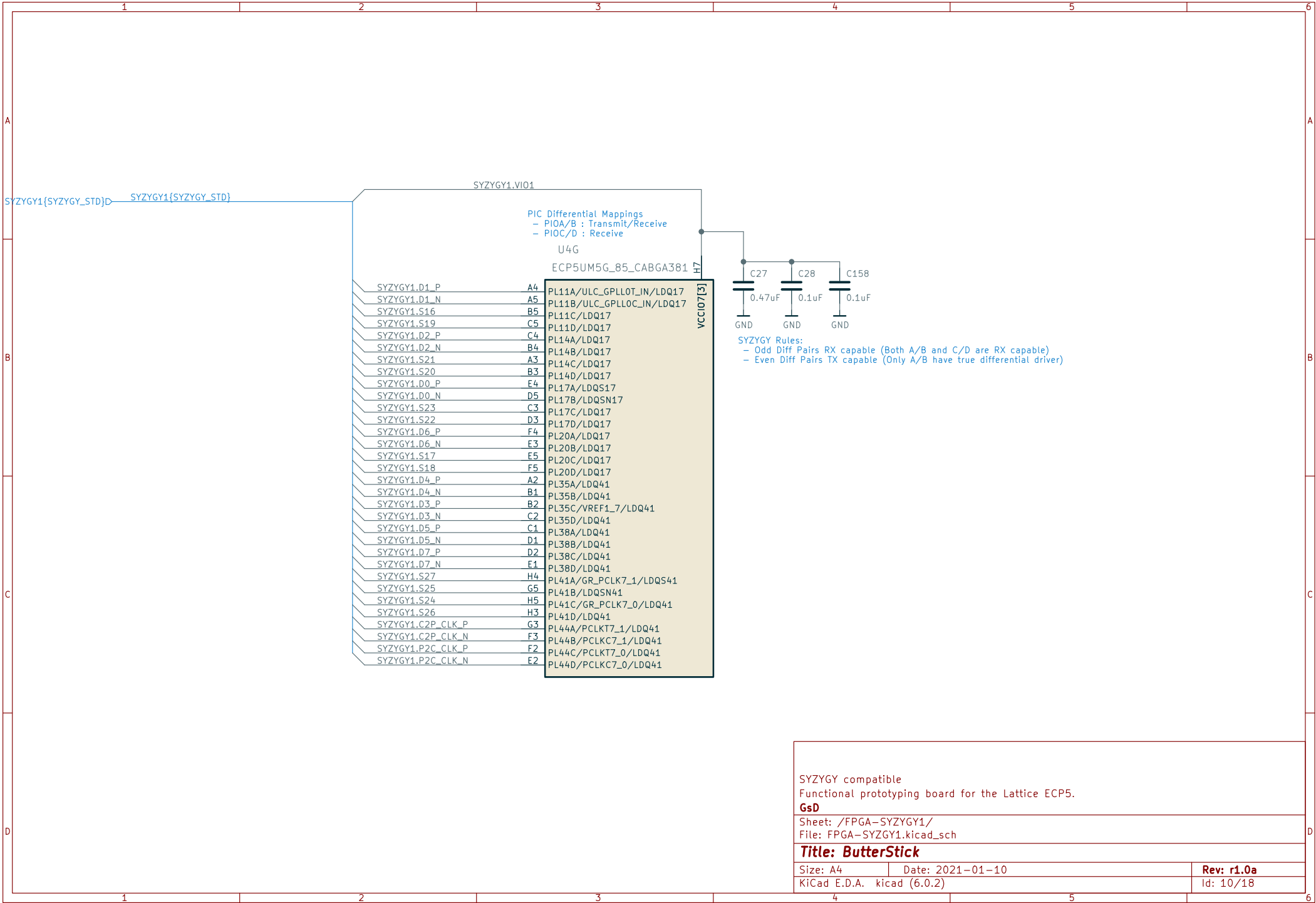
Sheet: /FPGA-DDR3L/
File: FPGA-DDR3L.kicad_sch

Title: ButterStick

Size: A4
KiCad E.D.A. kicad (6.0.2)

Date: 2021-01-10
Rev: r1.0a
Id: 8/18





SYZGY compatible
Functional prototyping board for the Lattice ECP5.

GsD

Sheet: /FPGA-SYZGY1/
File: FPGA-SYZGY1.kicad_sch

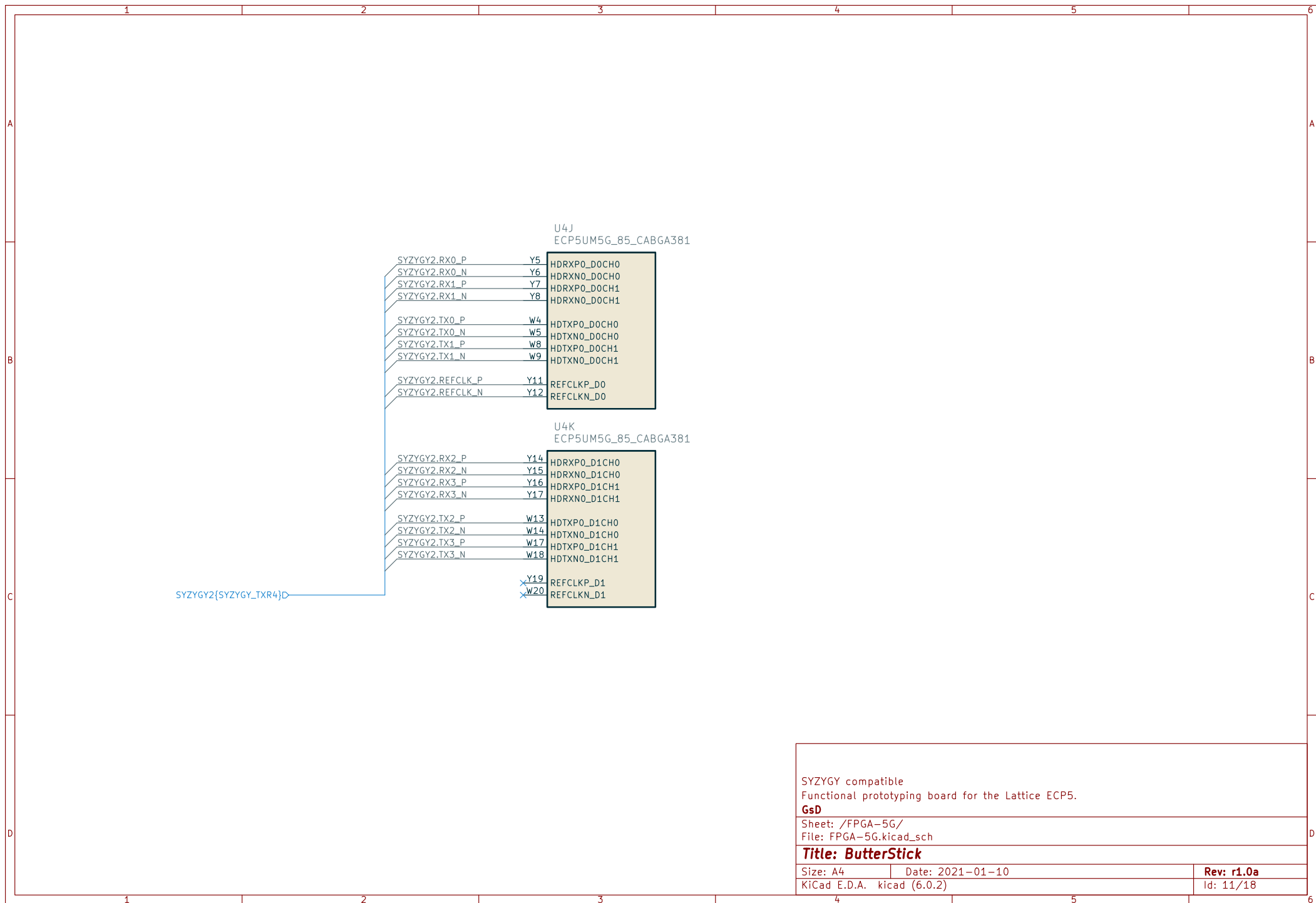
Title: ButterStick

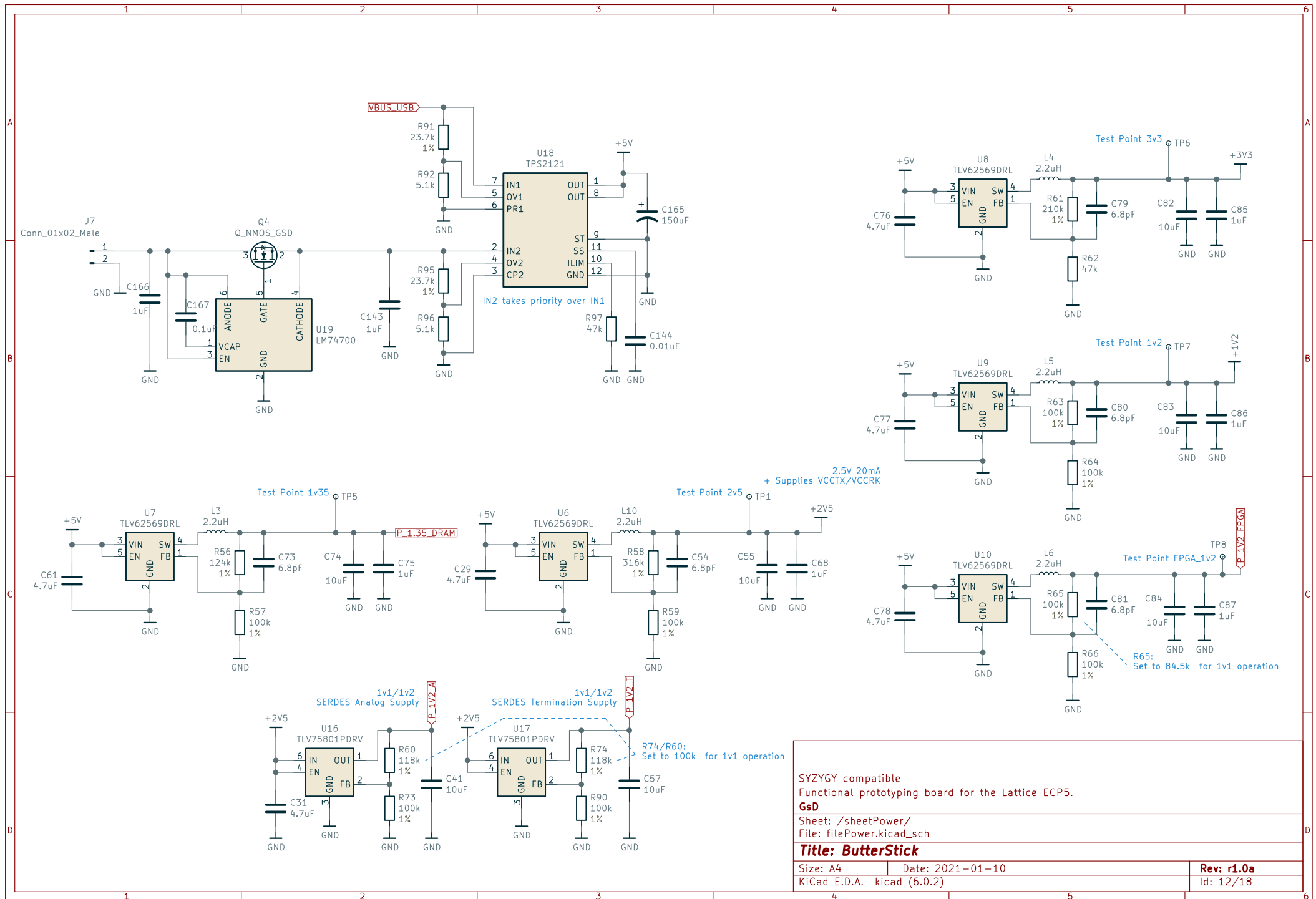
Size: A4 Date: 2021-01-10

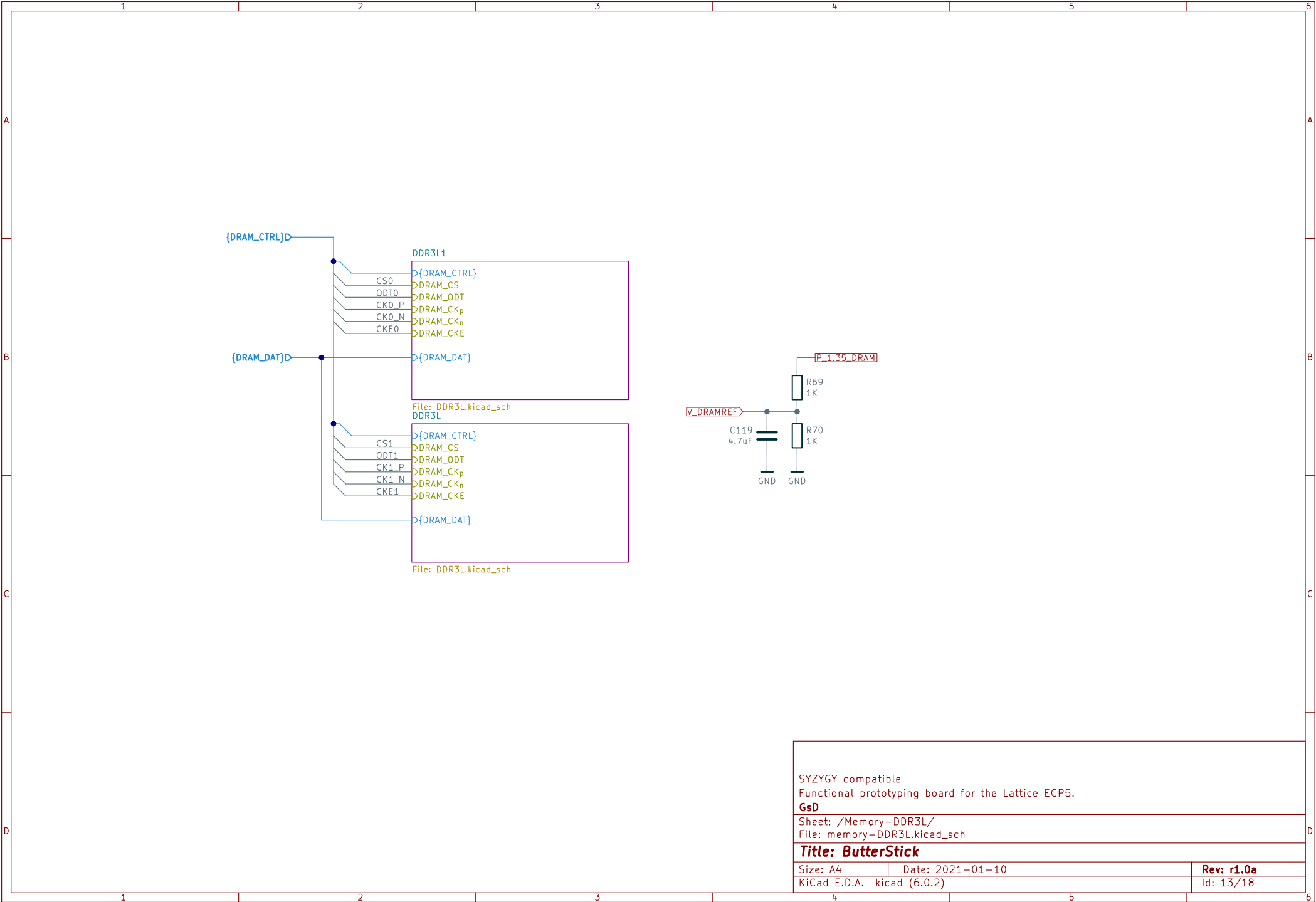
KiCad E.D.A. kicad (6.0.2)

Rev: r1.0a

Id: 10/18







SYZYGY compatible
Functional prototyping board for the Lattice ECP5.

GsD

Sheet: /Memory-DDR3L/
File: memory-DDR3L.kicad_sch

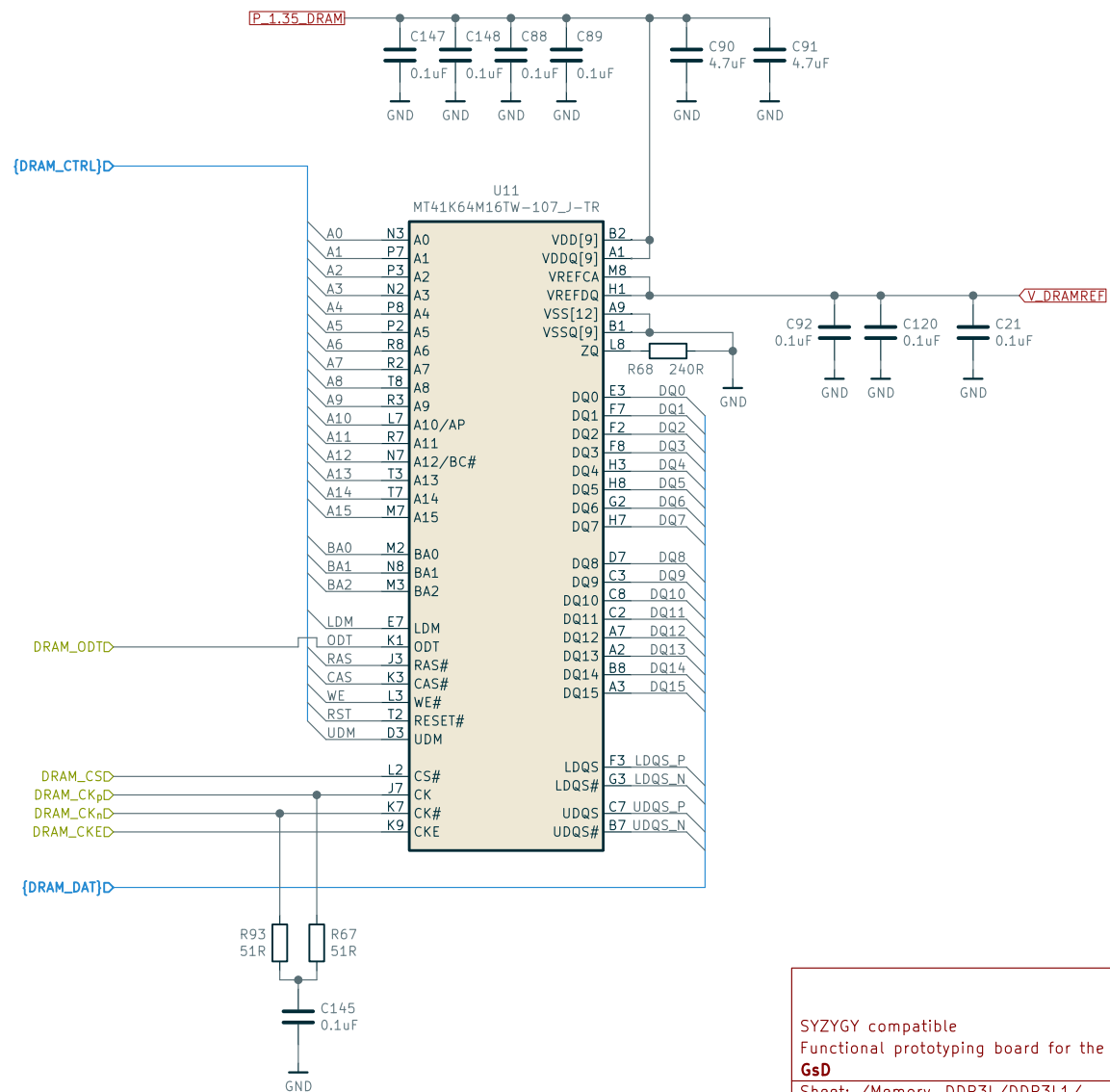
Title: ButterStick

Size: A4 Date: 2021-01-10

KiCad E.D.A. kicad (6.0.2)

Rev: r1.0a

Id: 13/18



SYZYGY compatible
Functional prototyping board for the Lattice ECP5.

GsD

Sheet: /Memory-DDR3L/DDR3L1/
File: DDR3L.kicad_sch

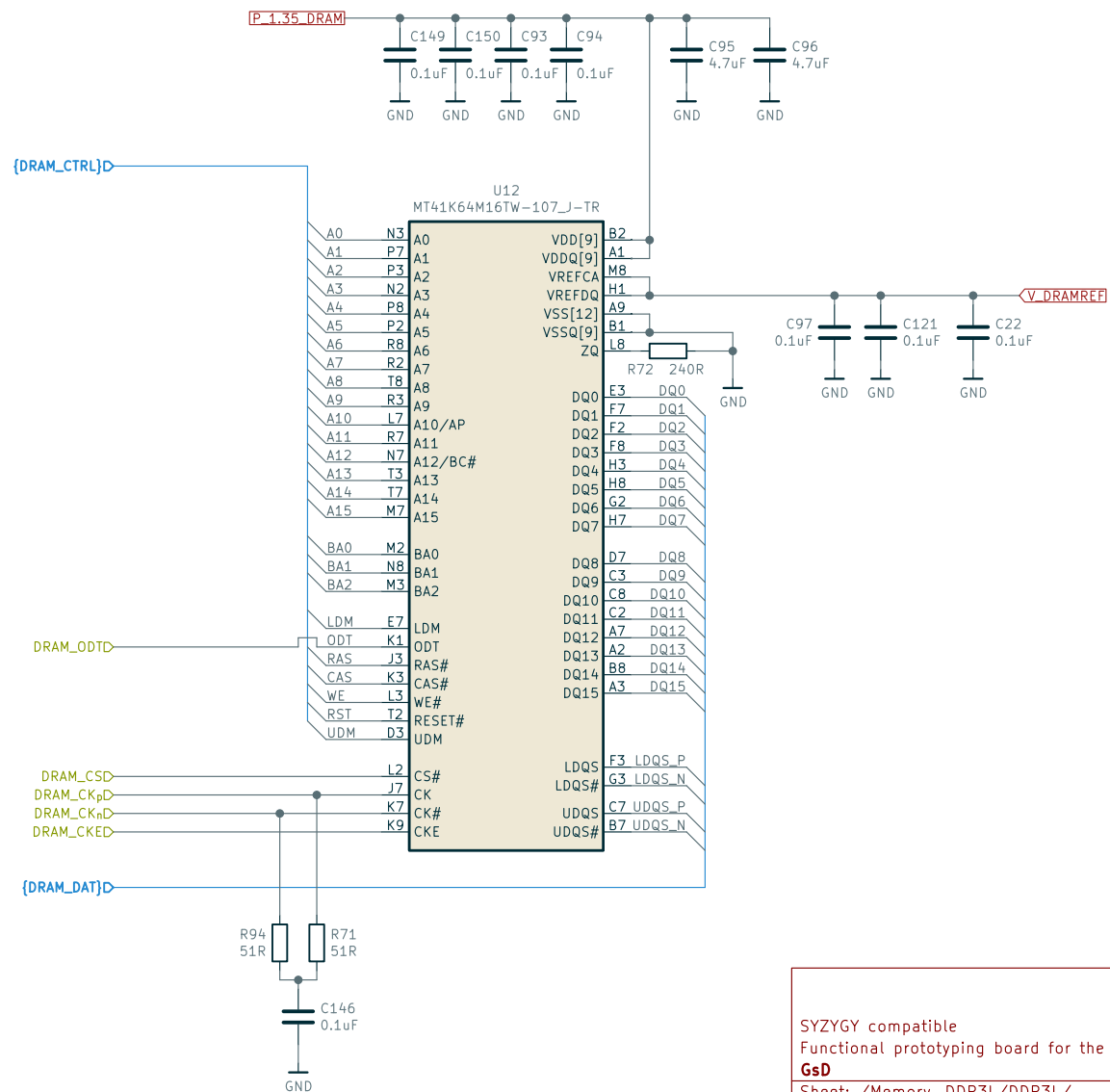
Title: ButterStick

Size: A4 Date: 2021-01-10

KiCad E.D.A. kicad (6.0.2)

Rev: r1.0a

Id: 14/18



SYZYGY compatible
Functional prototyping board for the Lattice ECP5.

GsD

Sheet: /Memory-DDR3L/DDR3L/
File: DDR3L.kicad_sch

Title: ButterStick

Size: A4 Date: 2021-01-10

KiCad E.D.A. kicad (6.0.2)

Rev: r1.0a

Id: 15/18

