

MP-1 Write-Up

Blake VERMEER

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1 Objective

In this lab we were introduced to the Xilinx tools and the lab environment setup. Also, we did some basic VHDL coding to manipulate a UART link.

2 Software Overview

3 Setting up Bluetooth

After generating the bitfile, I moved onto the software configuration side of UART0. To do this I just had to lookup the UART0

4 Talking to the Quad

5 PPM Passthrough

6 Data Analysis

7 Pseudo-GPS

8 PID Controller

9 Conclusion