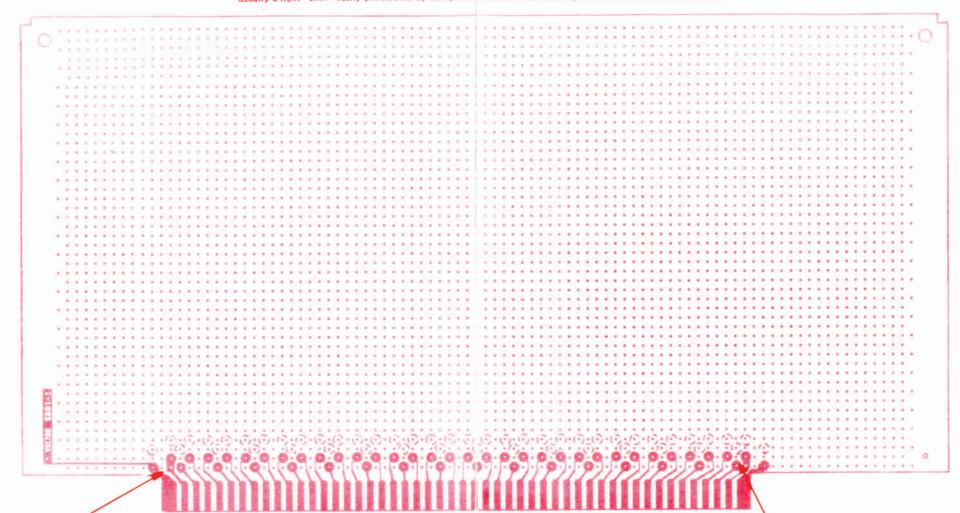
## 8801-1 PLUGBORD COMPONENT SIDE LAYOUT PAPER

NOTICE: Where tin coated circuitry exists a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases, a soldering iron may be required.



CAUTION:

Do not use lower hole in pad type both ends (insufficient clearance)

Before pressing terminals into board, position (rotate) terminals to maximize the clearance

NOTES:

- BROKEN CIRCLE ABOVE EDGE CONTACTS INDICATES LOCATION OF ACTUAL CONNECTOR CONTACT PADS ON OPPOSITE SIDE OF BOARD.
- 2. INTENDED FOR USE IN NON-HOSTILE ENVIRONMENTS UP TO 200 VOLTS RMS OR 300 VOLTS D.C.

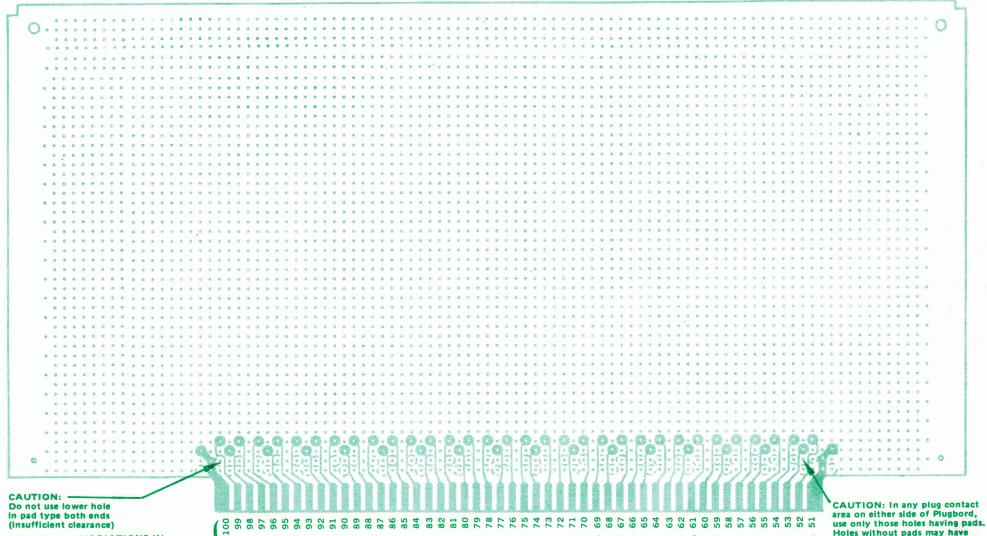
between the widest part of the terminal and the nearest adjacent conductor.

CAUTION: In any plug contact area on either side of Plugbord, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.

## 8801-1 PLUGBORD

## WIRING SIDE LAYOUT PAPER

NOTICE: Where tin coated circuitry exists a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases, a soldering iron may be required.



**VARIATIONS IN** RECEPTACLE CONTACT DESIGNATIONS

 $\begin{array}{c} \mathsf{A}\,\mathsf{A}\,\mathsf{A}\,\mathsf{A}\,\mathsf{A}\,\mathsf{A}\\ \mathsf{C}\,\mathsf{C}\,\mathsf{C}\,\mathsf{C}\\ \mathsf{C}\,\mathsf{C}\,\mathsf{C}\\ \mathsf{C}\,\mathsf{C}\\ \mathsf{C}\,\mathsf{C}\\ \mathsf{C}\,\mathsf{C}\\ \mathsf{C}\,\mathsf{C}\\ \mathsf{C}\\ \mathsf{C}\,\mathsf{C}\\ \mathsf{C}\\ \mathsf$ 

Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.

## NOTES:

- 1. BROKEN CIRCLE ABOVE EDGE CONTACTS INDICATES LOCATION OF ACTUAL CONNECTOR CONTACT
- . PADS ON OPPOSITE SIDE OF BOARD.
- 2. INTENDED FOR USE IN NON-HOSTILE ENVIRONMENTS UP TO 200 VOLTS RMS OR 300 VOLTS D.C.

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insufficient clearance to adjacent

circuitry and using them could

cause shorting.

VECTOR D.I.P. PLUGBORD PATTERN .042" x 0.1" SPACED HOLES LA17-P2 LAYOUT PAPER