```
3.3 V 1 O O 2 5 V
SDA1 2 3 @ @ 4 5 V
SCL1 3 5 @ @ 6 GND
  GPCLK 4 7 ⊚ ⊚ 8 14 TXD <sup>—</sup>
     22 15 0 016 23
     3.3 V 17 @ @18 24
 ™MOSI 10 19 ⊚ 20 GND
   MISO 9 21 @ @22 25
 SCLK 11 23 © ©24 8 CE0
     GND 25 @ 026 7 CE1 __
 SDA0 (I<sup>2</sup>C) 0 27 @ @28 1 SCL0 (I<sup>2</sup>C)
        6 31  32 12 PWM0
  MISO 19 35 © 36 16
       GND 39 @ 40 21 SCLK
```