

1

2

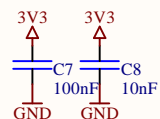
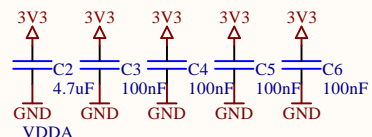
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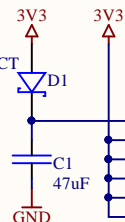
A

A

VDD, VDDUSB



B0530WSRRGCT



U1

1 VBAT
48 VDDUSB
13 VDDA
19 VDD
32 VDD
64 VDD

PA0
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PA8
PA9
PA10
PA11
PA12
PA13
PA14
PA15

14 SERVO1
15 SERVO2
16 USB_DET
17 BUZZER
20 LED1
21 SPI1_SCK
22 QSPI_IO3
23 QSPI_IO2
41 PYRO3
42 UART1_TX
43 UART1_RX
44 USB_N
45 USB_P
46 SWDIO
49 SWCLK
50 IO2

26 QSPI_IO1
27 QSPI_IO0
28 LED2
55 CS_IMU1
56 SPI1_MISO
57 SPI1_MOSI
58 INT_ACCEL
59 IO1
61 CAN_RX
62 CAN_TX
59 QSPI_CLK
60 QSPI_NCS
33 CS_BARO3
34 SPI2_SCK
35 SPI2_MISO
36 SPI2_MOSI

PC0
PC1
PC2
PC3
PC4
PC5
PC6
PC7
PC8
PC9
PC10
PC11
PC12
PC13
PD2

8 V_PYRO1
9 V_PYRO2
10 V_PYRO3
11 V_BAT
24
25
37 CS_BARO1
38 CS_BARO2
39 PYRO1
40 PYRO2
51 IO3
52 CS_MAG
53 CS_ACCEL
54 CS_IMU2

3 PC14-OSC32_IN
4 PC15-OSC32_OUT

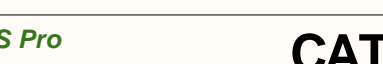
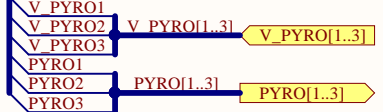
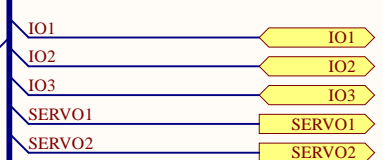
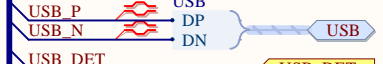
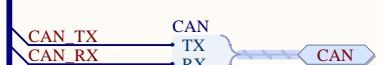
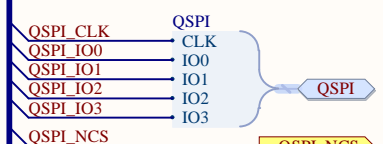
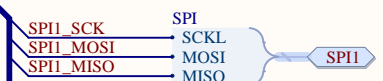
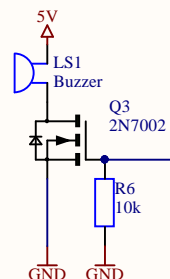
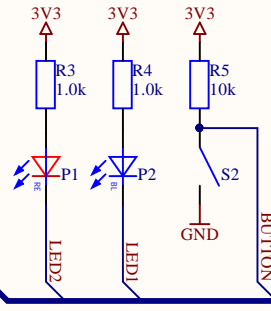
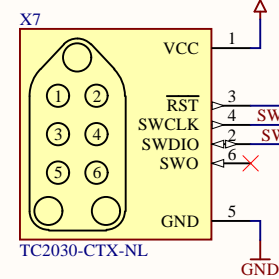
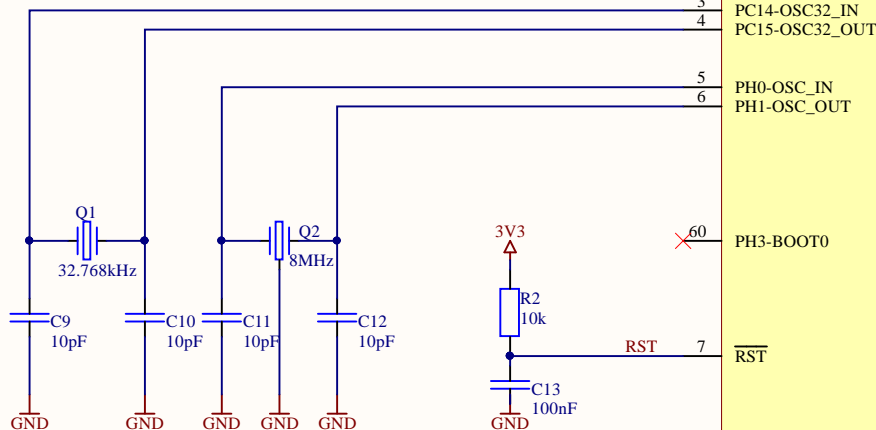
5 PH0-OSC_IN
6 PH1-OSC_OUT

PH3-BOOT0

RST

12 VSSA
18 VSS
31 VSS
47 VSS
63 VSS

STM32L433RCT



B

C

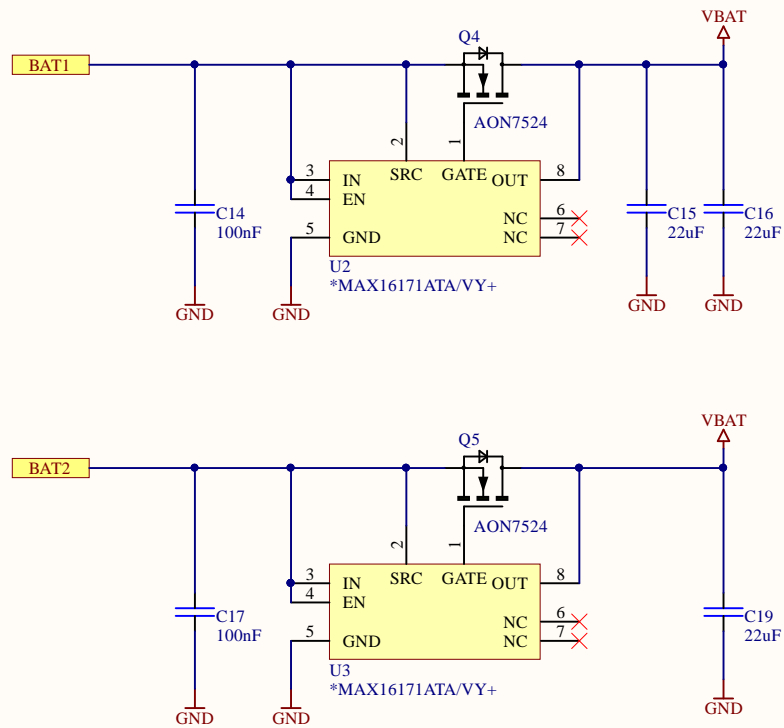
D

1

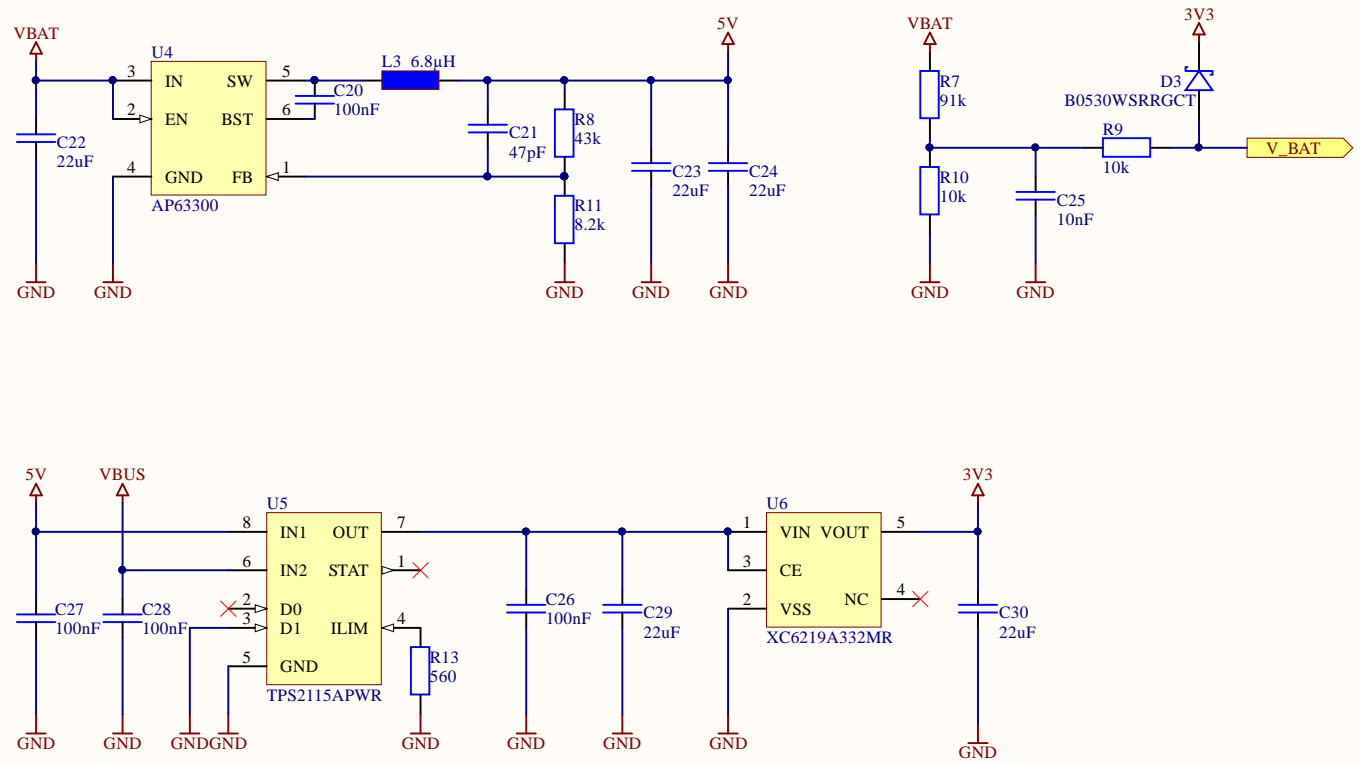
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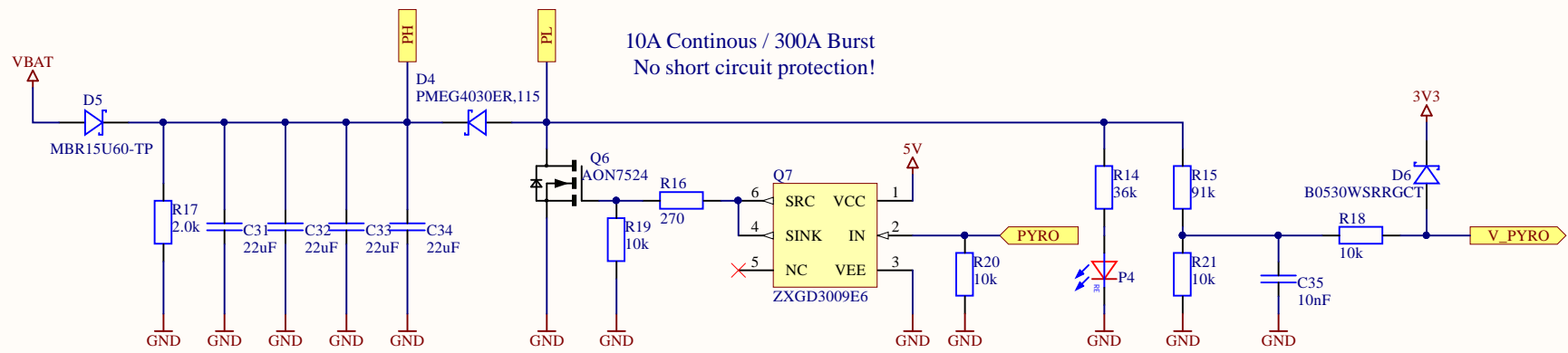
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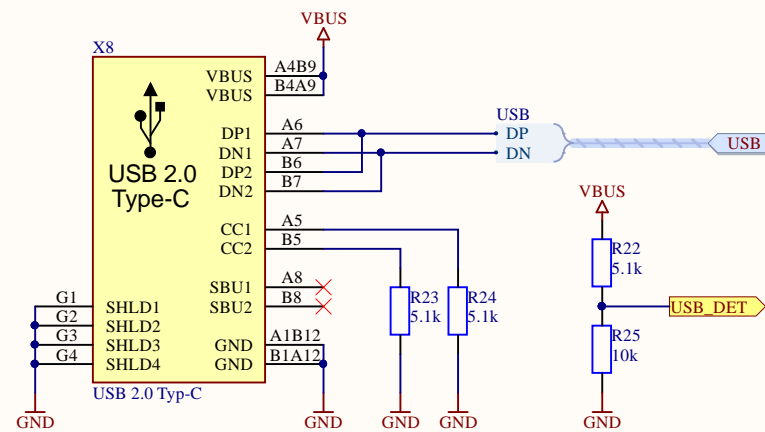
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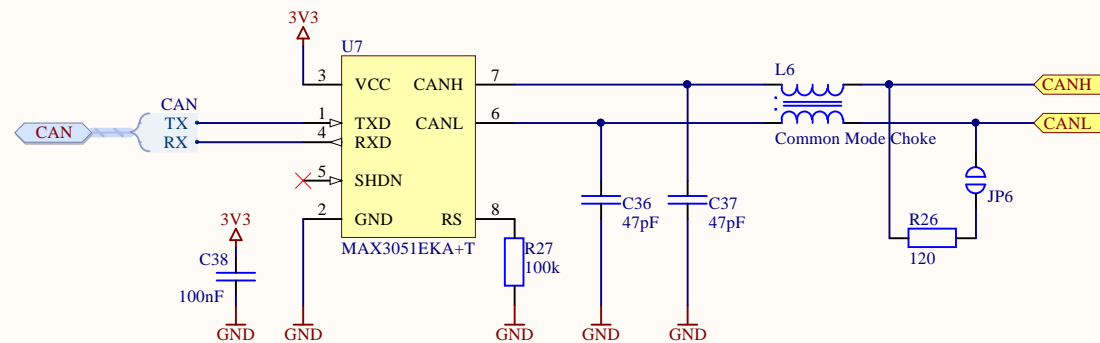


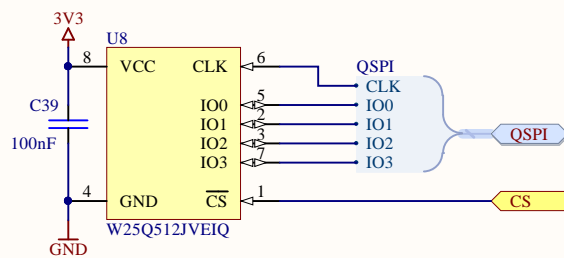
Redundant Battery ORing with ideal Diodes



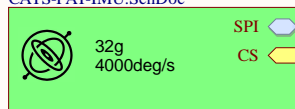








U_CATS-FAT-IMU
CATS-FAT-IMU.SchDoc

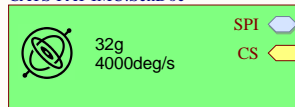


SPI
CS

SPI1

CS_IMU1

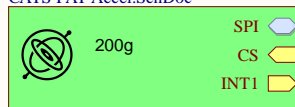
U_CATS-FAT-IMU
CATS-FAT-IMU.SchDoc



SPI
CS

CS_IMU2

U_CATS-FAT-Accel
CATS-FAT-Accel.SchDoc

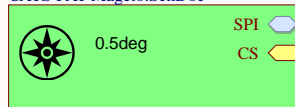


SPI
CS
INT1

CS_ACCEL

INT_ACCEL

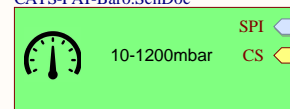
U_CATS-FAT-Mageto
CATS-FAT-Mageto.SchDoc



SPI
CS

CS_MAG

U_CATS-FAT-Baro
CATS-FAT-Baro.SchDoc

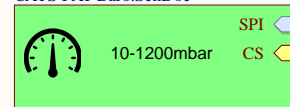


SPI
CS

SPI2

CS_BARO1

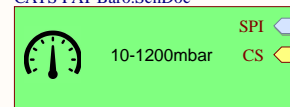
U_CATS-FAT-Baro
CATS-FAT-Baro.SchDoc



SPI
CS

CS_BARO2

U_CATS-FAT-Baro
CATS-FAT-Baro.SchDoc



SPI
CS

CS_BARO3

