

1

2

3

4

A

A

B

B

C

C

D

D

SPI

CS

BUSY

DIO1

DIO2

FE_EN

TX_EN

TEMP

UART1

UART2

IO1

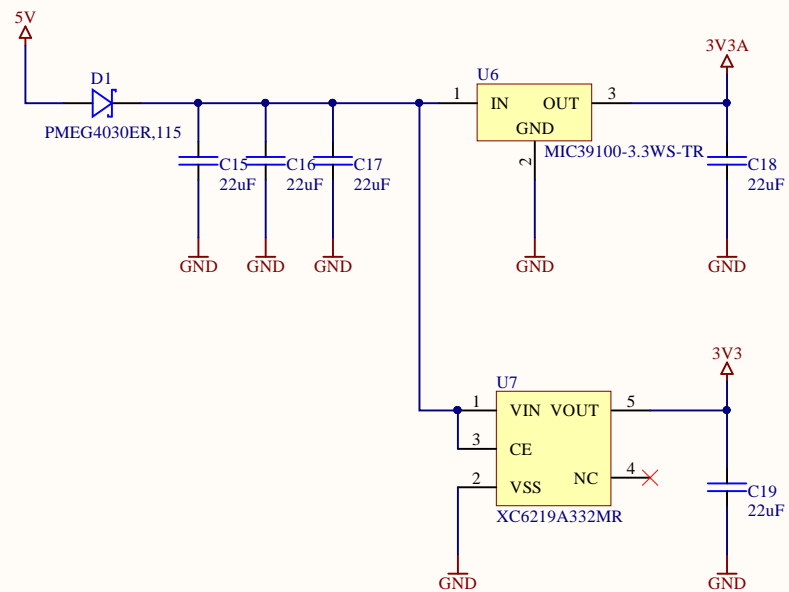
IO2

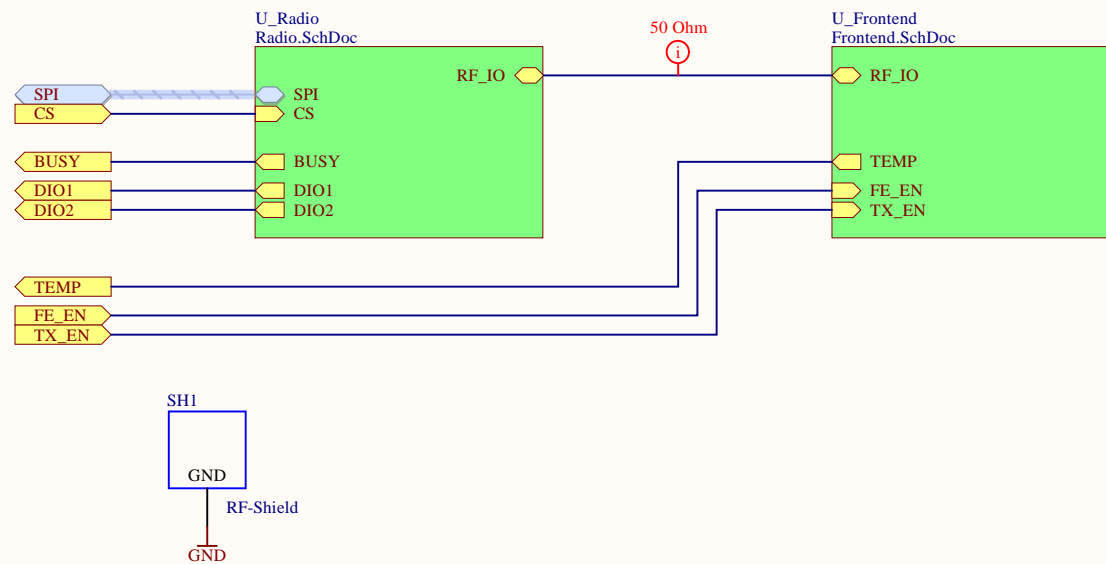
1

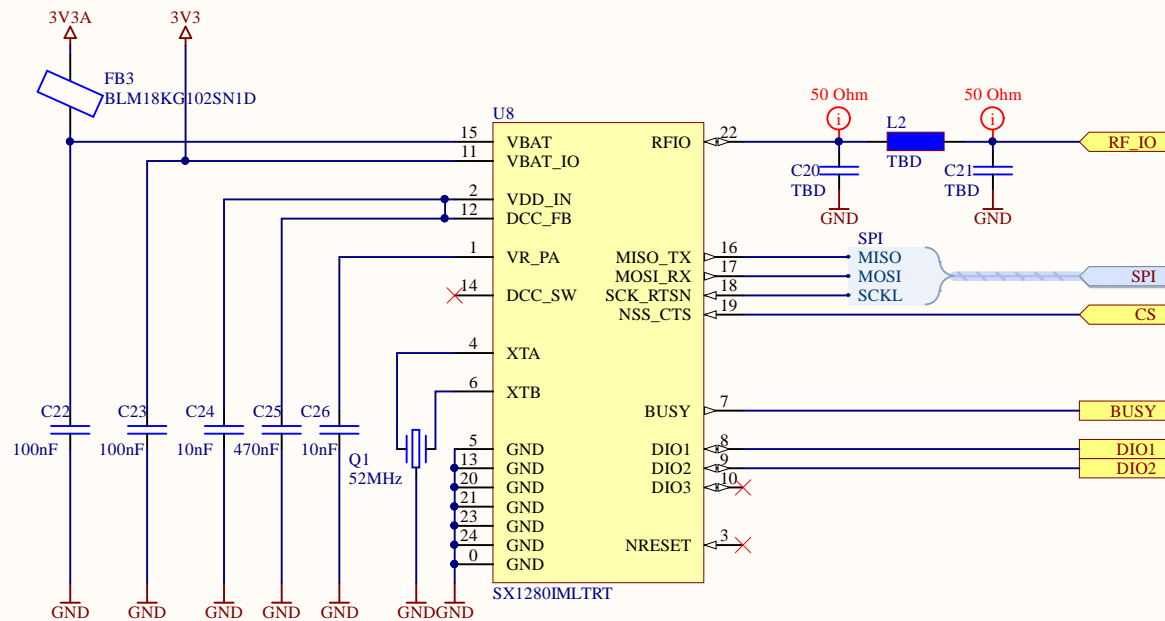
2

3

4





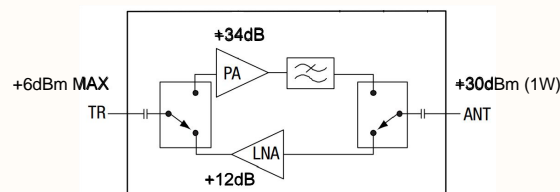
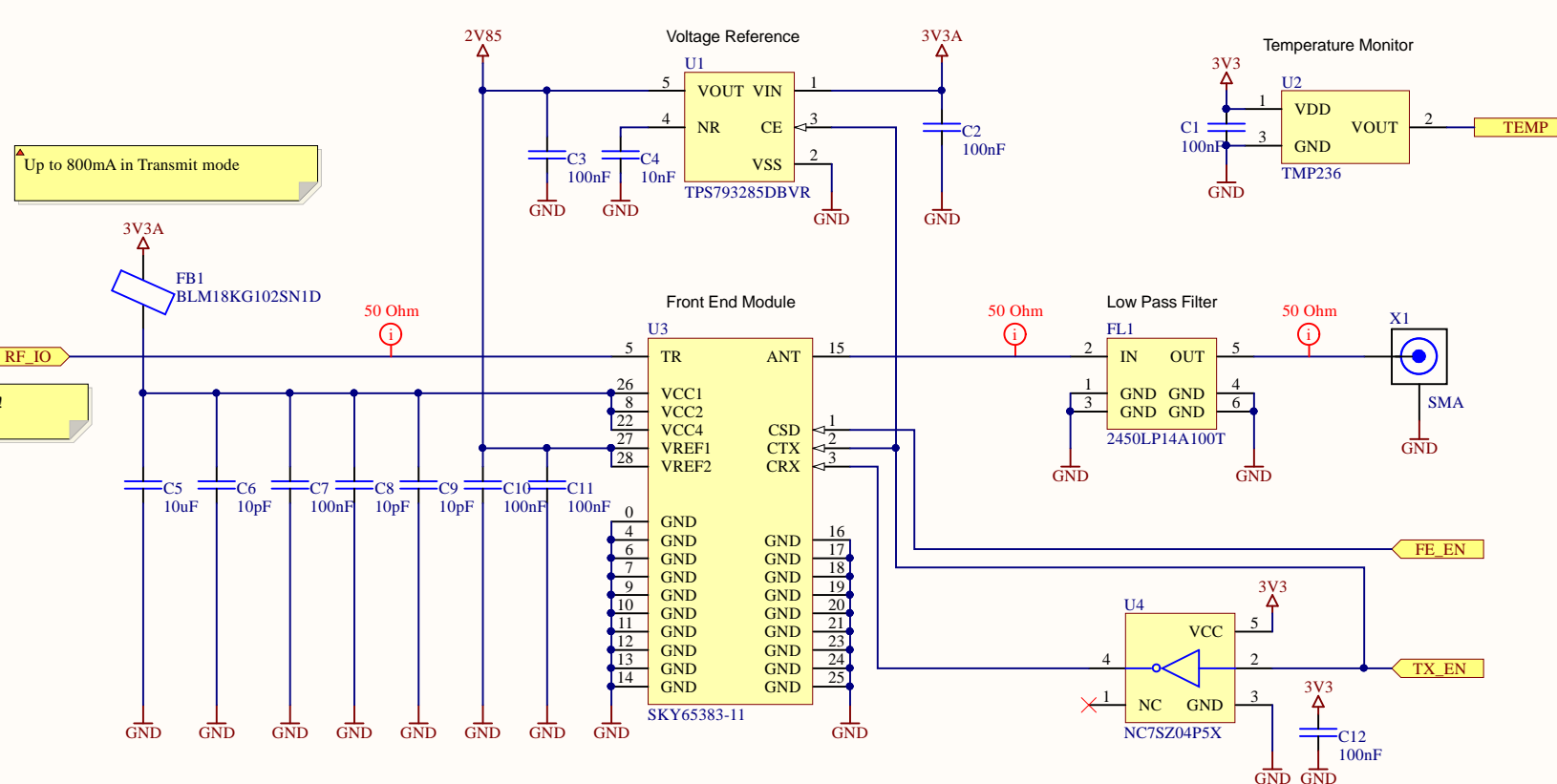


1

2

3

4



Operating Mode	CSD (Pin 1)	CTX (Pin 2)	CRX (Pin 3)
All off (sleep mode) ²	0	0	0
Receive ²	1	0	1
Transmit	1	1	0

