Andrey Tenne

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University graduate with interest and experience in working with digital circuits seeking a challenging full–time position in electrical engineering to expand hands–on experience in design, production, and testing of electronics devices.

Skills

Hardware: working experience with Verilog and VHDL, skilled with FPGAs (Altera Cyclone II and Xilinx Spartan 3A), hands–on experience with circuit testing using various lab equipment (multimeters, DC power supplies, oscilloscopes), and through–hole soldering.

Applications: skilled with ModelSim, Quartus II, Xilinx ISE, OrCAD Pspice, and AutoCAD, proficient with Microsoft Word, Excel, and PowerPoint, familiar with Microsoft Visio.

Software: skilled with C/C++ and MATLAB, familiar with Java.

Experience

A/D Precision, **Newton, MA**   October 2012 – December 2012
FPGA Design Intern
- Created time–of–day clock with numerical output displayed on a VGA monitor and with options to reset clock and set time.
- Designed and built a circuit that counts number of pulses from an external signal source and displays results on a VGA screen.
- Gained skills in using an FPGA to output data to a VGA port and synchronizing a VGA monitor to prevent flickering.

LTX – Credence, Norwood, MA   January 2011 – May 2011
EE Hardware Intern
- Created block diagrams of a test circuit using Microsoft Visio.
- Measured noise figure and gain of amplifiers with a spectrum analyzer.
- Was responsible for testing circuit boards using a software–controlled test system to identify defective boards.
- Analyzed signal lines and pin connections for parasitic capacitances in a multilayer circuit board.

Zoran, Burlington, MA   January 2010 – June 2010
Software Engineer
- Performed isolations of reproducible printer code bugs using a Linux – based debugger to pinpoint the root cause of a reported problem.
- Designed, developed, and implemented test cases to cause erroneous printout.
- Was responsible for keeping online bug log up to date.

Other Projects

FPGA – Interfaced Temperature Monitor
- Designed an FPGA interface to read data from a high – precision temperature sensor with output periodically displayed on several seven – segment indicators.

Education
Northeastern University, Boston, MA

Bachelor of Science in Electrical Engineering, *Magna cum Laude*   May 2013

GPA: 3.58

Honors / Awards: Dean's List, Dean's Scholarship, member of Eta Kappa Nu (HKN) Honors Society.

Capstone Project: PARABed (Partially Articulated Rest Assistant Bed) – created and tested an FPGA – based subsystem

- Configured an Altera Cyclone II FPGA to control flashing of LEDs that functioned as visual triggers for an EEG.
- Implemented SPI communication to receive LED flash pattern parameters from CPU.

Personal
Foreign Languages: fluent Russian, level 3 Spanish.