

# Joel Reindel

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## Address:

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## OBJECTIVE

Work an entry level electrical engineering position in the fields of digital systems design, embedded systems design, or hardware design, where I can utilize my technical skills and personal strengths to solve problems for the consumer.

## WORK EXPERIENCE

### **EchoStar, Aurora, CO**

Intern to VP of Information Technology Summer 2012

- Conceptualized hardware configuration for a new kind of high density, low cost storage server.
- Utilized various computer hardware solutions to develop a layout optimized for storage capacity, cooling and cost.
- Fabricated Custom prototype/proof of concept server, and aided in the software and network level implementation.

### **National Cinemedia, Centennial, CO**

IT Intern, Summer 2010 Summer 2011

- Aided IT technical analysts in troubleshooting computer issues through collaboration, research and personal knowledge.
- Communicated with users in person and on the phone, to guide base level diagnostics and coordinate user down time.
- Combined broken, like model, computers into operational units for redeployment.

## ACADEMIC PROJECT EXPERIENCE

### **Senior Design Project** August 2013 – May 2014

- Worked on real world design problem for Rockline Industries; automated wet wipe tub unloader.
- Responsibilities included: overall design team work, electrical portions of design, part of final design paper.

### **Junior Design Project** January 2013 – May 2013

- Took a product through the design lifecycle, including: building prototype, manual and other documentation.
- Responsibilities included: part of proposal, design and synthesis of electrical components, lead programmer, design testing.

### **FORTTRAN Computation Program Conversion** October 2012 – May 2013

- Worked for physics professor to convert theoretical particle physics computation program from FORTRAN into C.
- Helped professor to understand how the C code synthesized FORTRAN code, as he was not familiar with C.

## SKILLS

- VHDL, C, C++, C#, Python, Bash, Quartus II, LTSPICE, MATLAB, Mathcad, SolidWorks and MS Office Suite.
- Lab Work: Altera DE2 FPGA, TI MSP430 Launchpad, Oscilloscope, Waveform Generator, PSU, Digital Multi-meter.
- Circuit Building Skills: digital and simple analog circuit design, soldering and component testing.

## CLUB ACTIVITY

- Co-Leader of SASS (Students Against Sex Slavery) – Focuses on raising awareness of, and ending, sex slavery in the US.
- Was part of IEEE Chapter on Campus – Discusses IEEE activities and general tech news, also host IEEE competitions.
- Was active member in JBU Automotive Club – Auto enthusiast, auto care events, auto maintenance training.

## EDUCATION

John Brown University (ABET accredited), Siloam Springs, AR  
BS in Engineering, Electrical/Computer Concentration

Graduated: May 2014  
Overall/Major GPA: 3.2 / 3.3 (4.0 scale)

## NOTEABLE COURSEWORK

Embedded Systems, Advanced Computer Programming, Engineering Economics, International Problem Solving