

# John Rainier Batasin

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## Objective

Seeking for a full time or part time to utilize my skills and knowledge I learned in school to provide additional competitive person into your company.

## Education

University of California San Diego

Expected Graduation

Date: June 2013

Bachelor of Science in Electrical Engineer

- Provost Honor Spring 2012

## Related Coursework

- Microwave Systems and Circuits
- Digital Signal Processing I
- Digital & Analog Integrated Circuits Design
- Linear Electronic Systems | Electromagnetism
- Electronic Circuits and Systems
- Engineering Probability and Statistics
- Verilog, C Programming, MIPS, CADENCE, PSPICE, Arduino Programming, Objective C (In Progress)

## Related Projects

- Collaborated with Scanadu Company to develop and a test a device to detect diseases with liquid sample for a senior project
- Design 1kb SRAM with the use of CADENCE
- Design, test, and optimize a ring Oscillator and to find PDP, EDP, and minimum delay
- Design and simulated low-pass filter active filter with the use of Sallen and Key topology
- Extensive use of Cadence, PSPICE , bread boarding, and electronic measurement tools

## Internships

Scanadu, summer 2012

*Position: Electrical Engineer Intern*

- ♦ Designed and prototyped a device that will be sold in the market in the near future.
- ♦ I used Eagle CAD to design the board layout, and Arduino pro mini microcontroller to program and to debug the system. I also worked with other engineers to assist them with their ongoing project to solder the PCB board and test their program.