

A

B

C

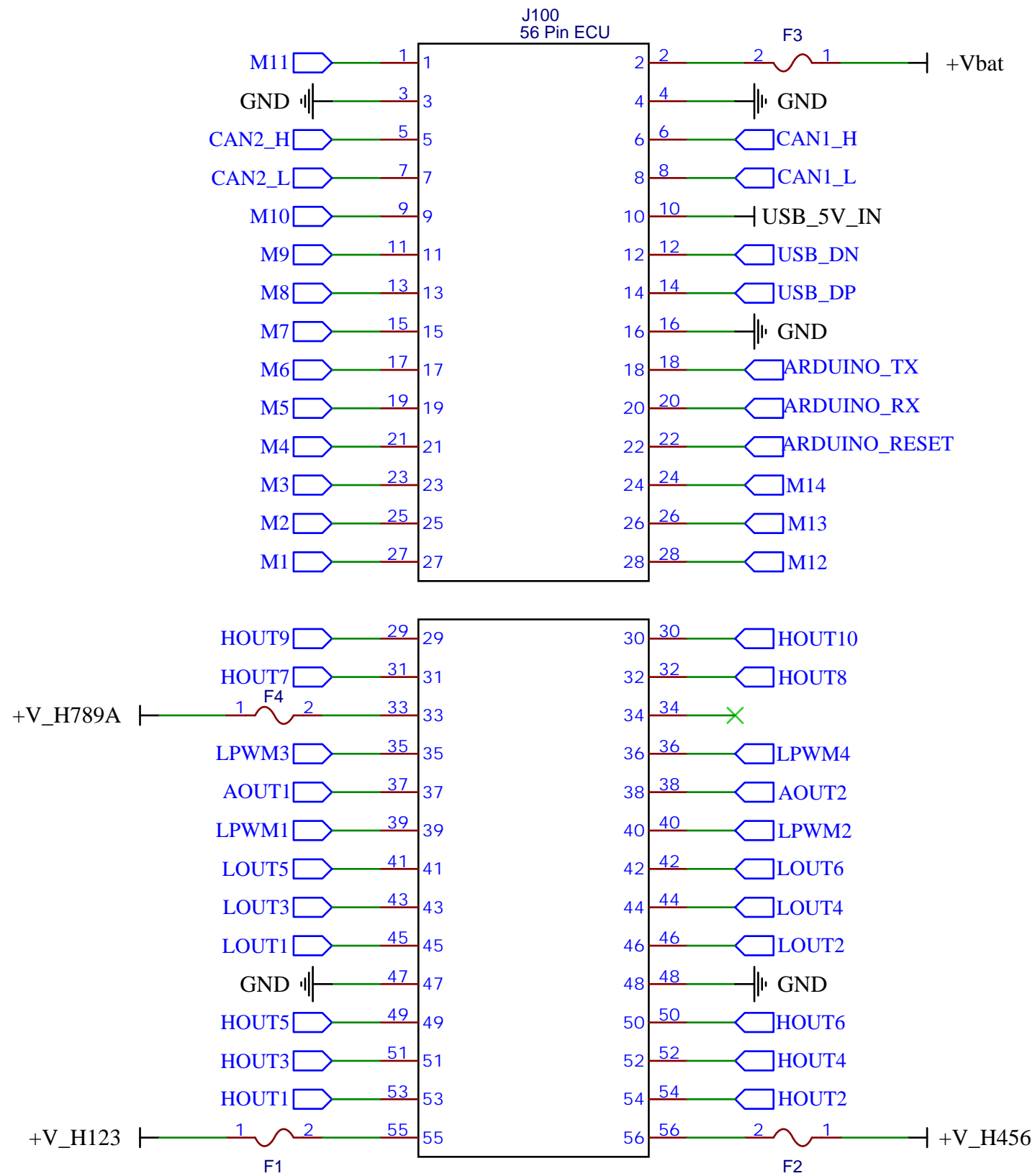
D


A

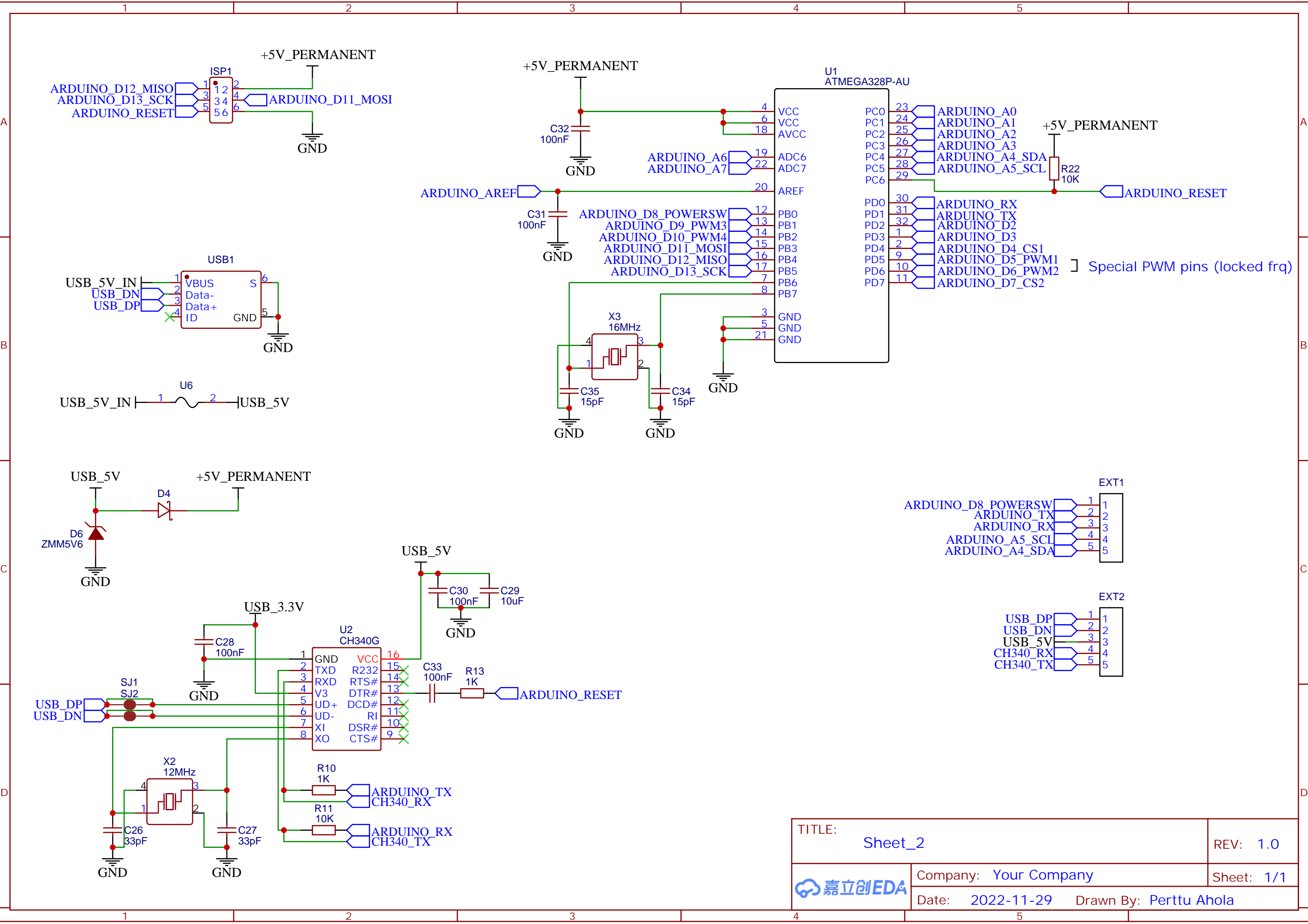
B


C

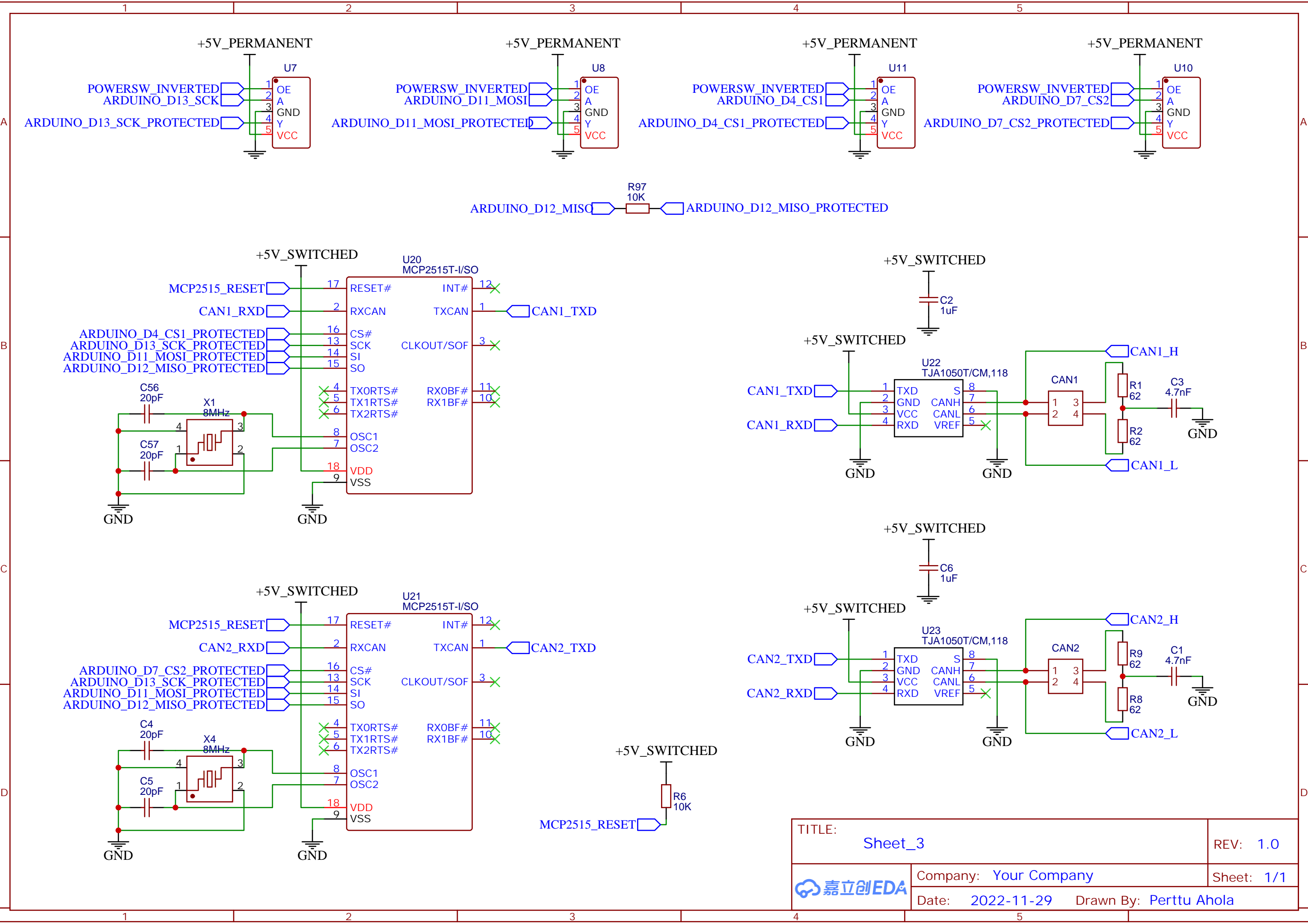
D



TITLE: Sheet_1		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2022-11-29 Drawn By: Perttu Ahola	



TITLE: Sheet_2		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2022-11-29	Drawn By: Perttu Ahola



TITLE: Sheet_3		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2022-11-29	Drawn By: Perttu Ahola

A

A

B

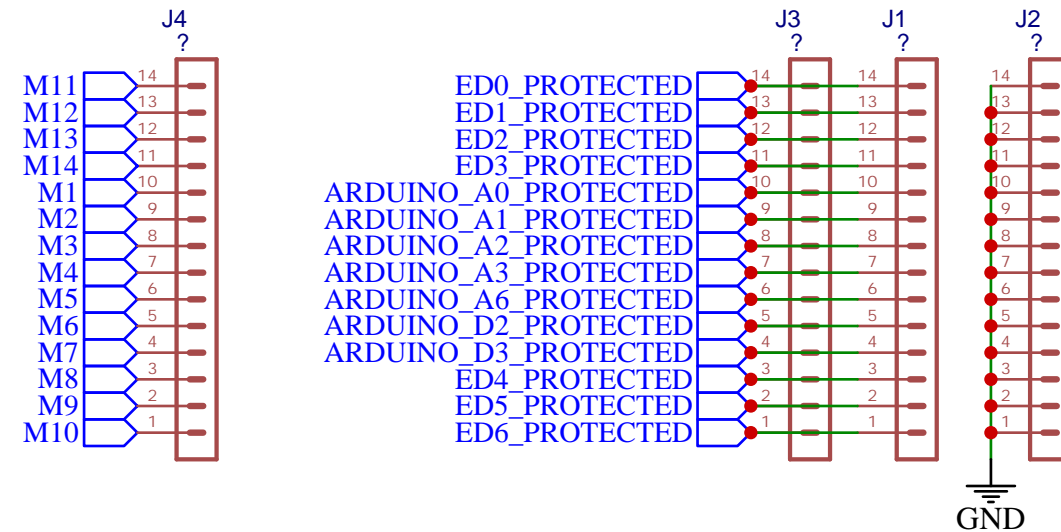
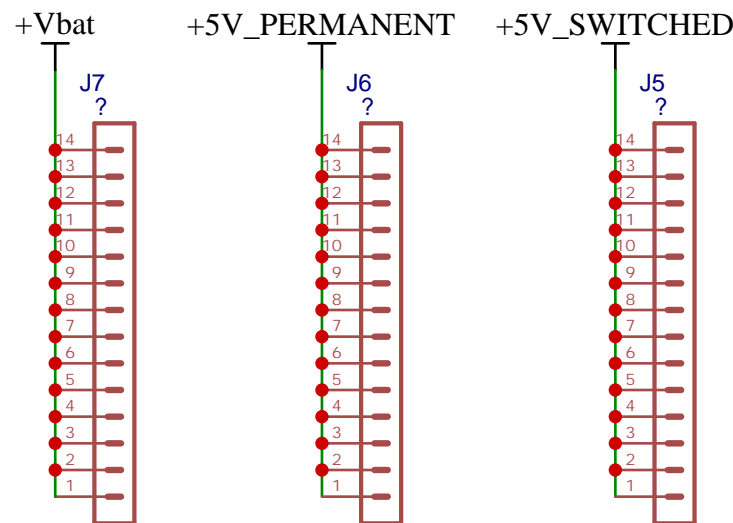
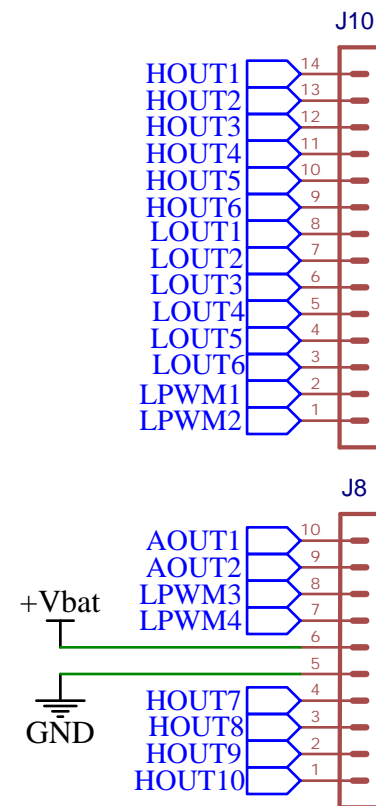
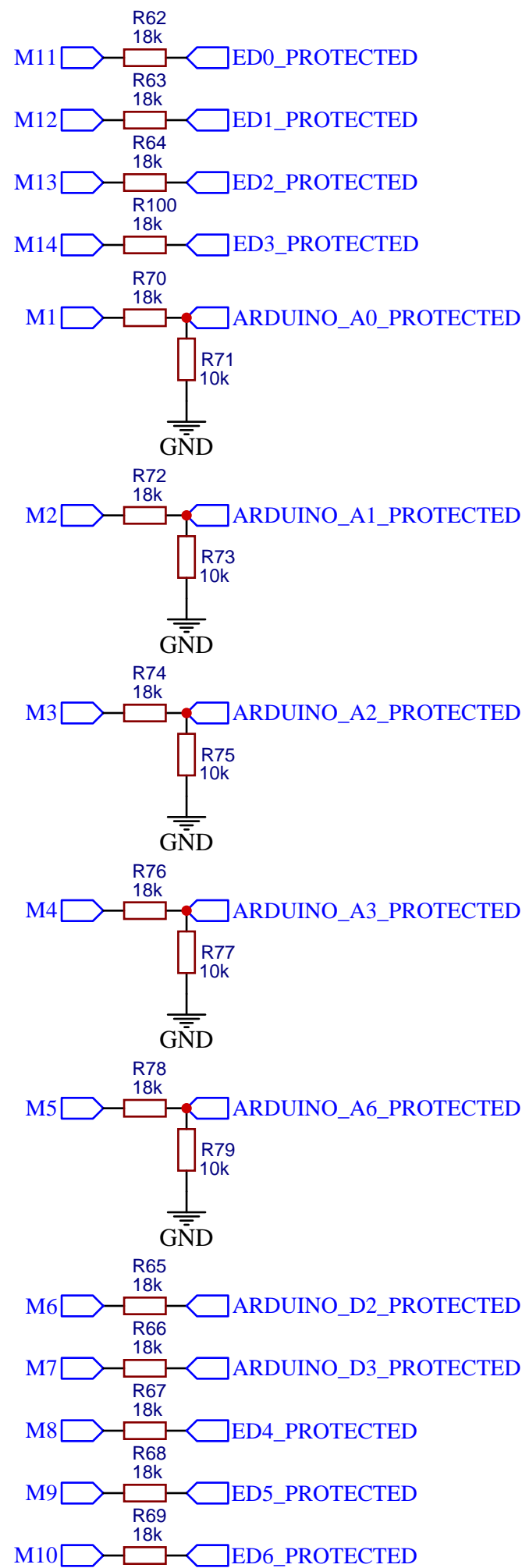
B


C

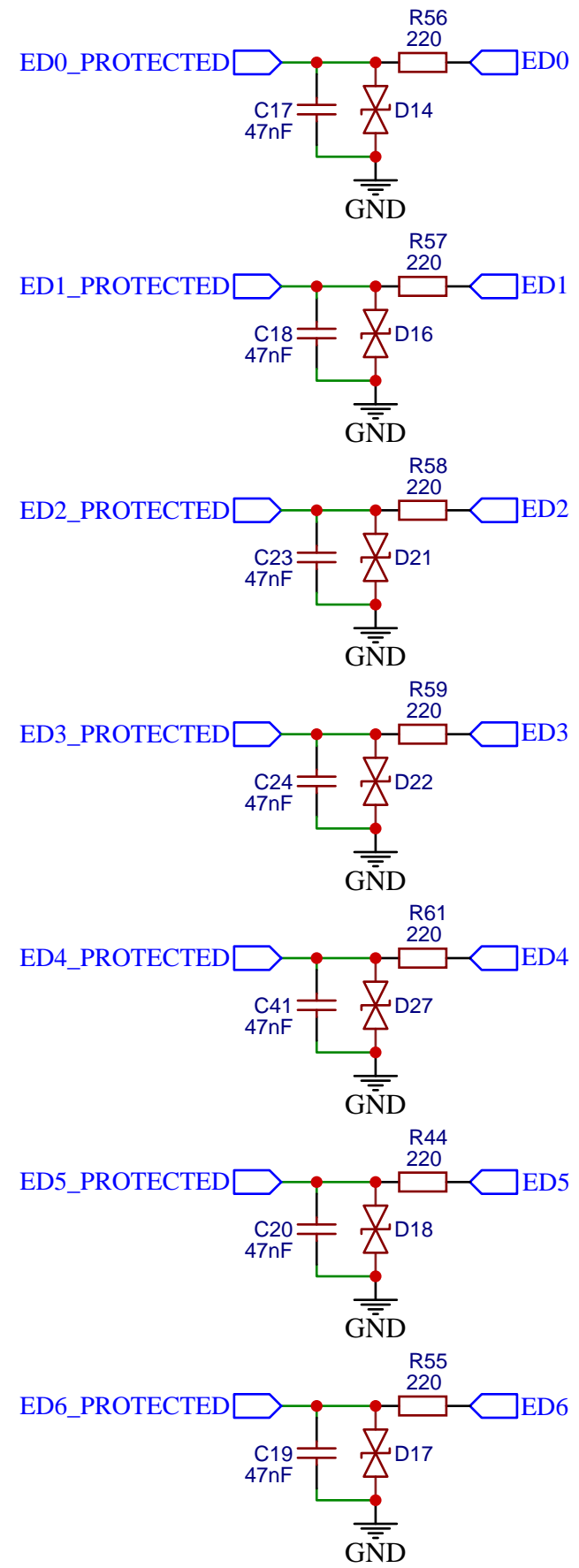
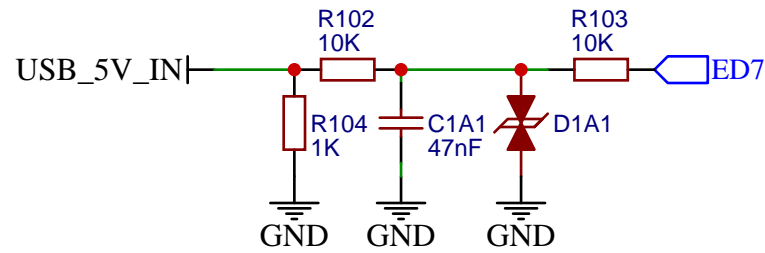
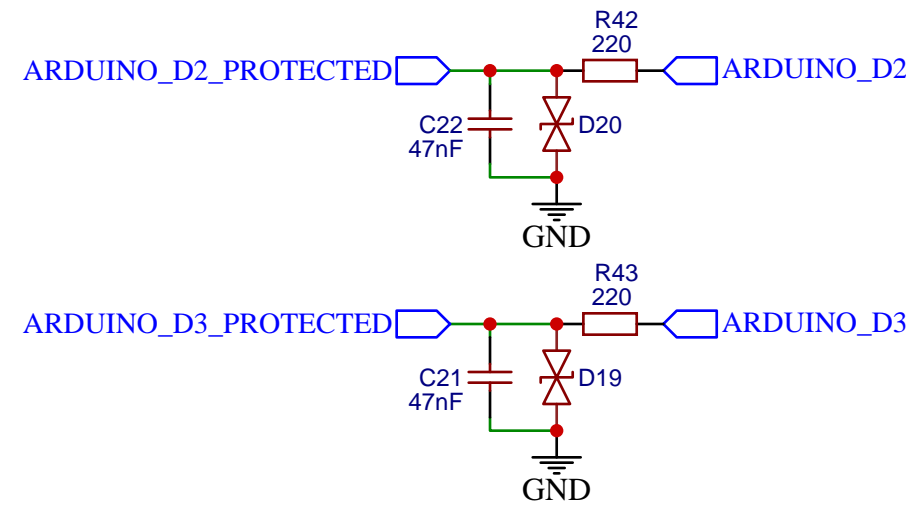
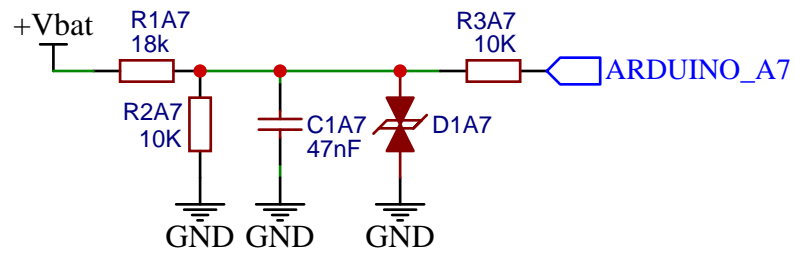
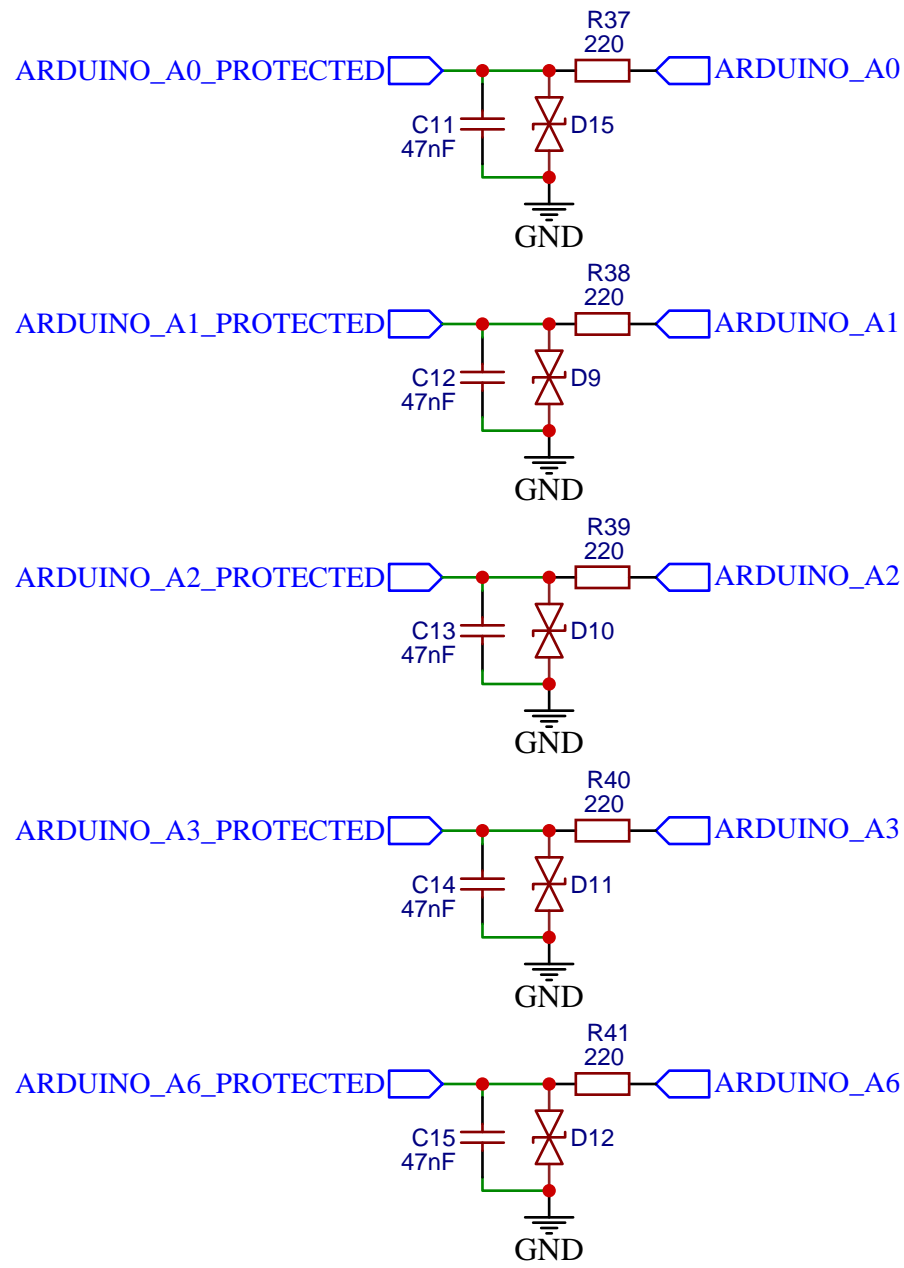
C

D

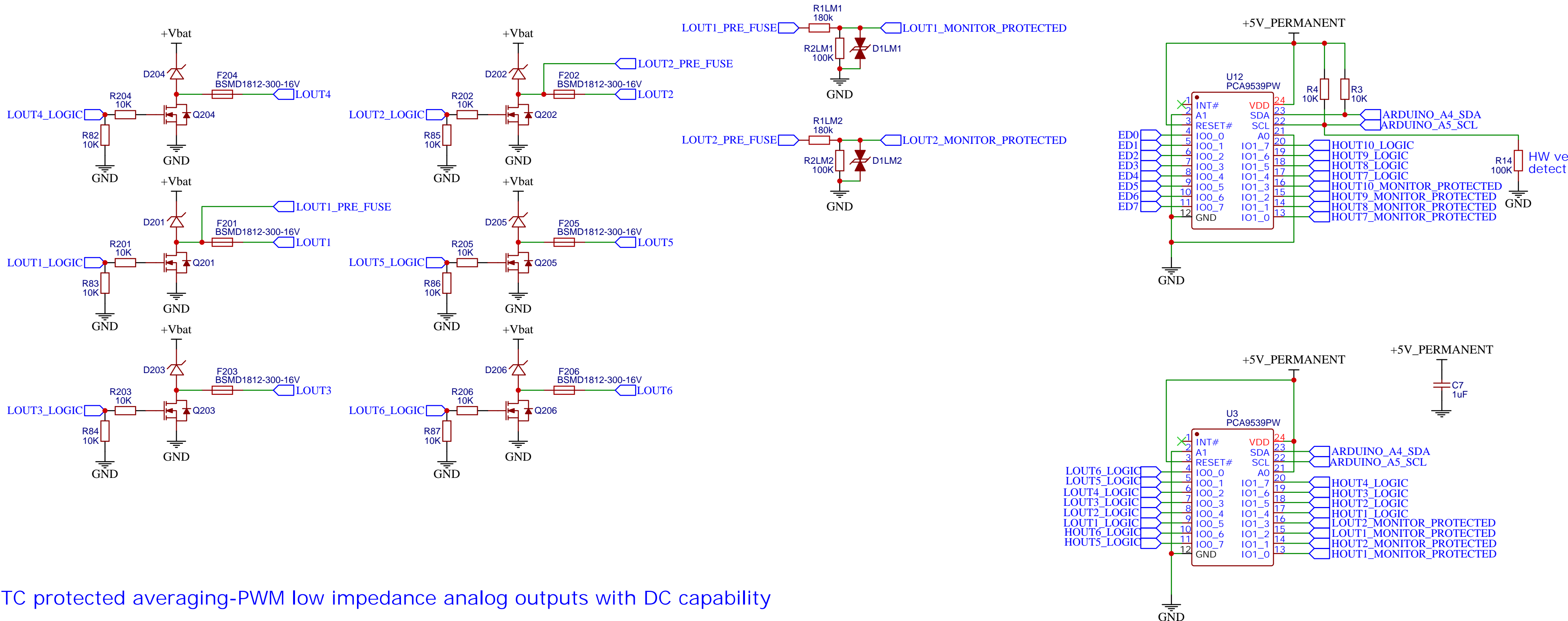
D



TITLE: Sheet_1		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2022-11-29 Drawn By: Perttu Ahola	



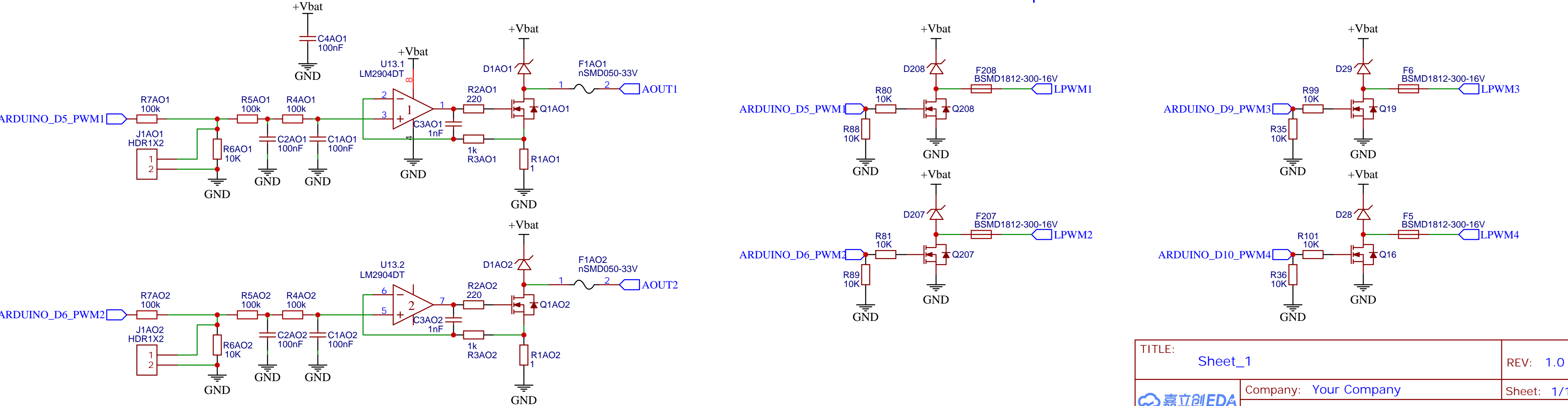
TITLE: Sheet_1		REV: 1.0
嘉立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2022-11-29 Drawn By: Perttu Ahola	

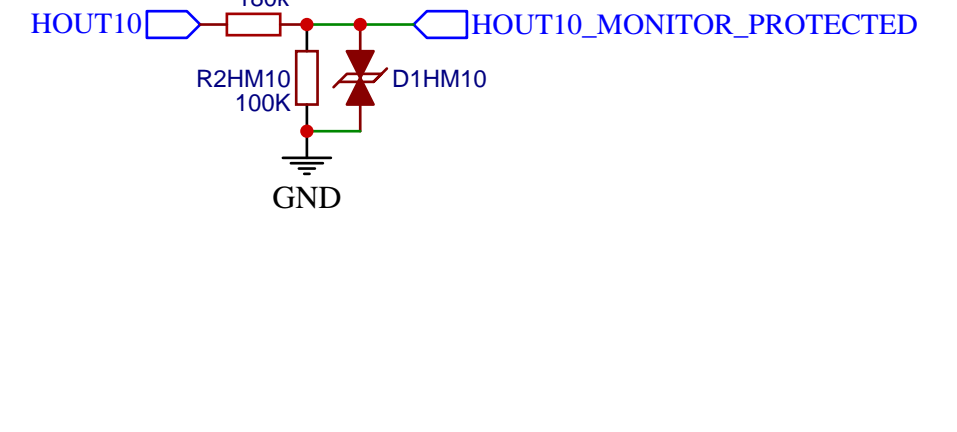
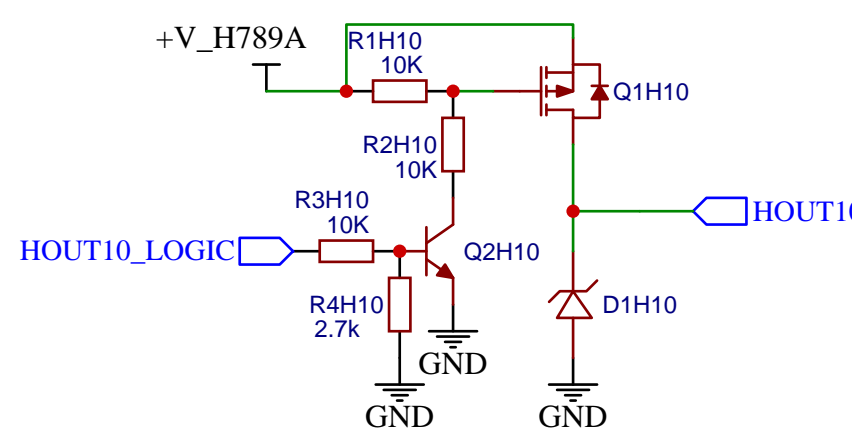
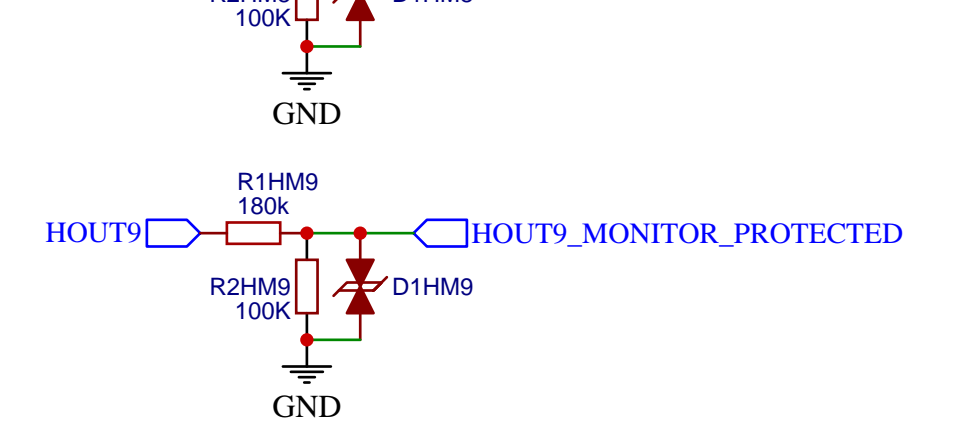
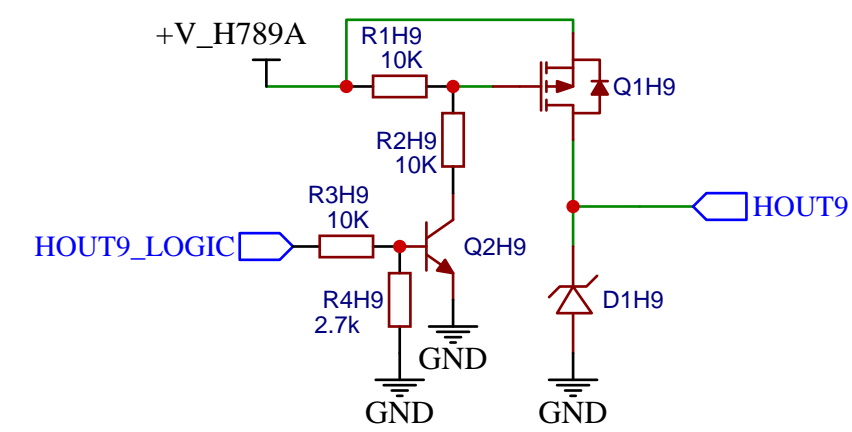
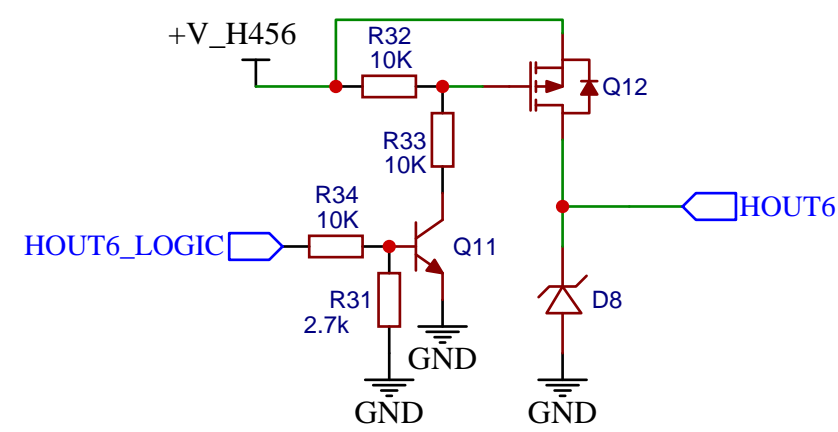
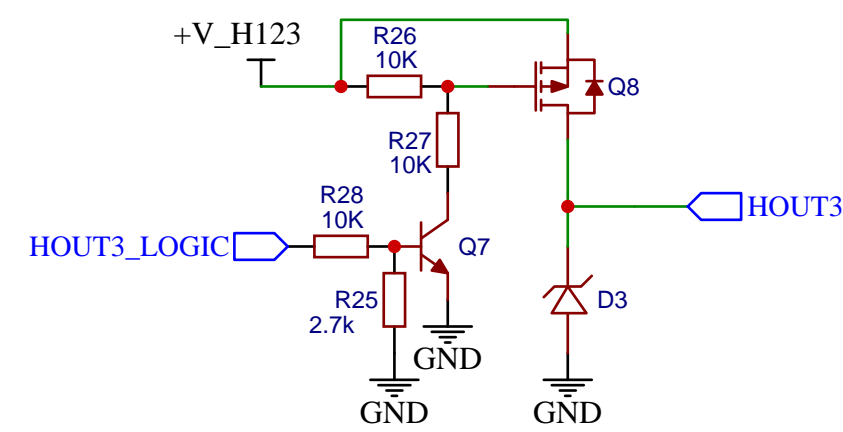
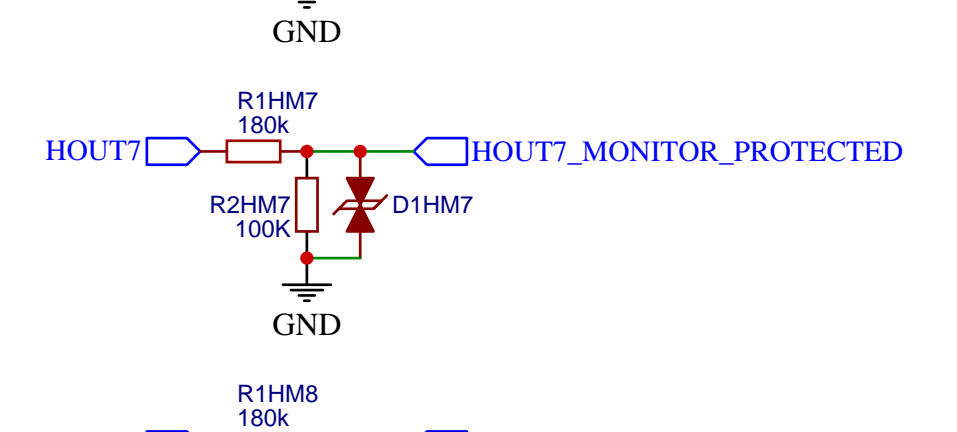
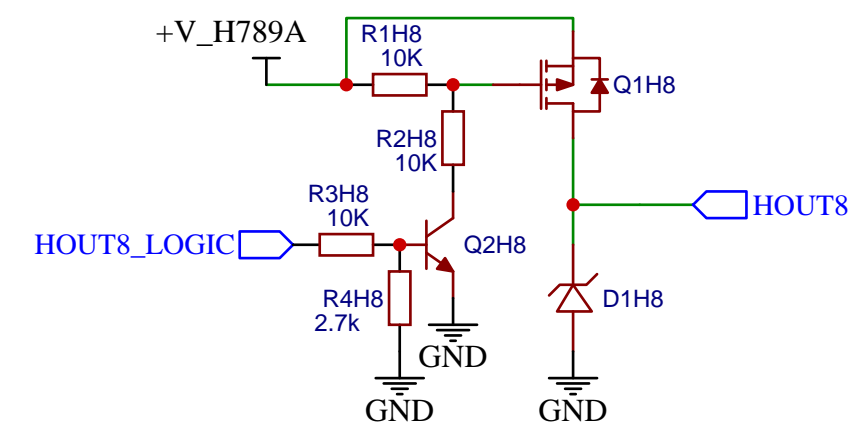
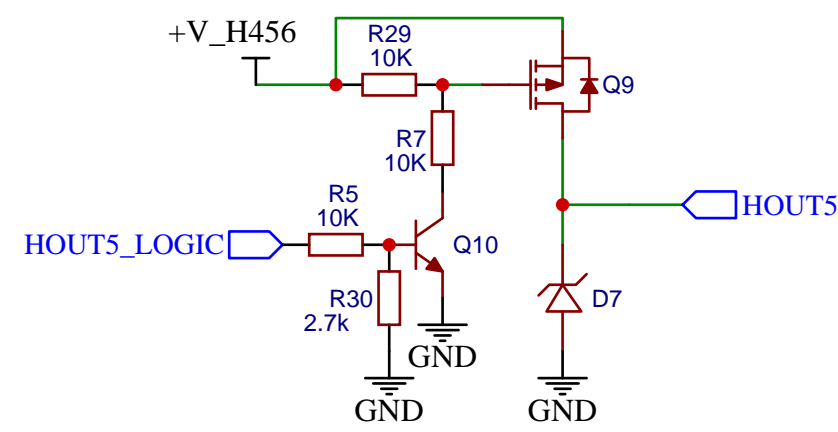
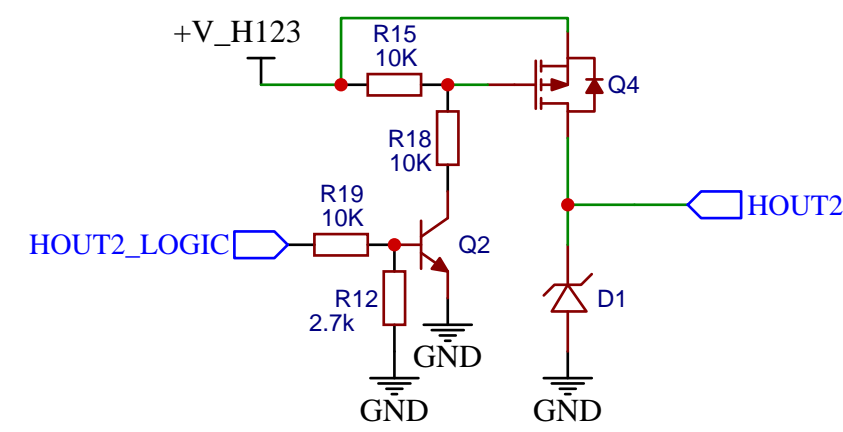
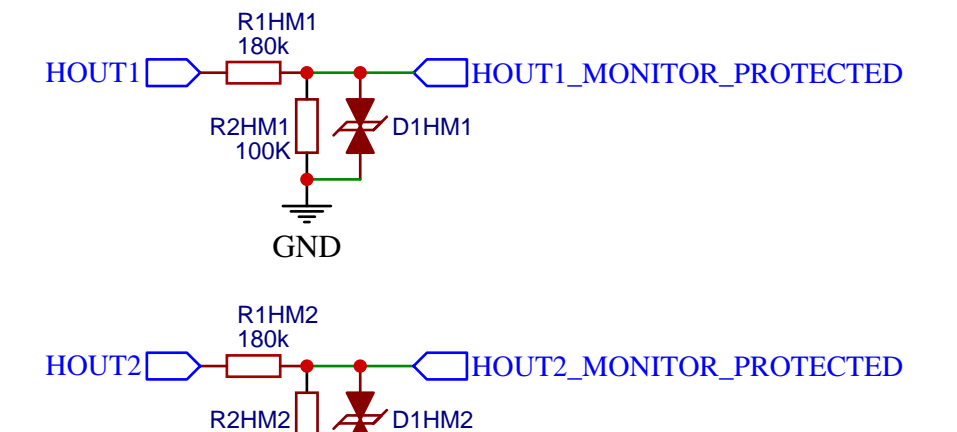
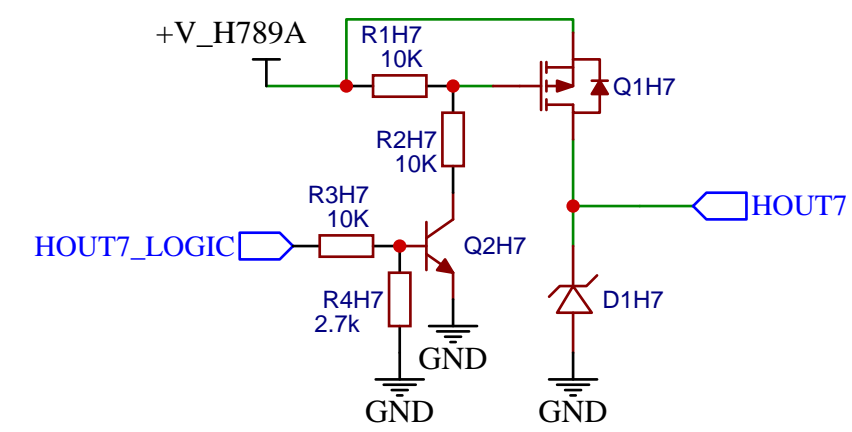
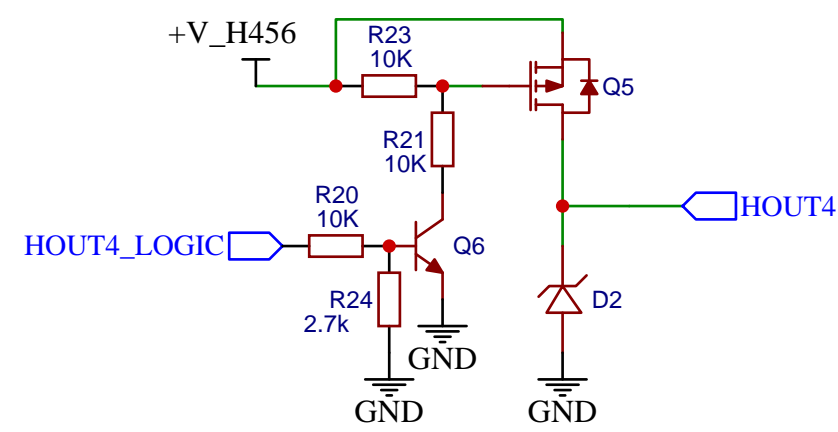
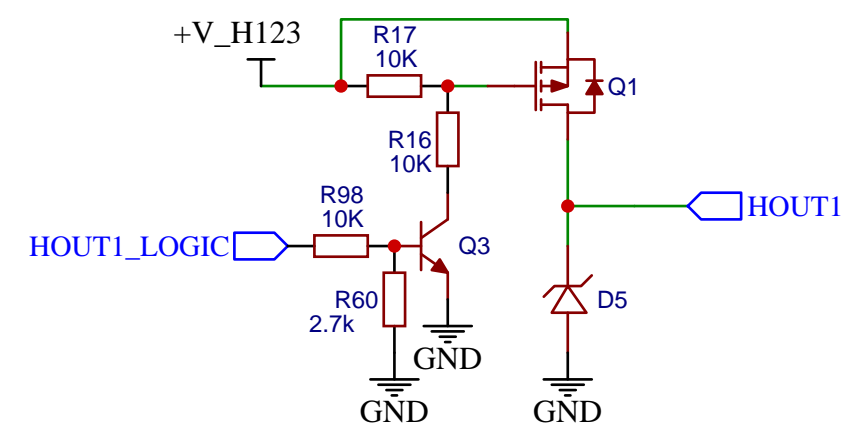


PPTC protected averaging-PWM low impedance analog outputs with DC capability

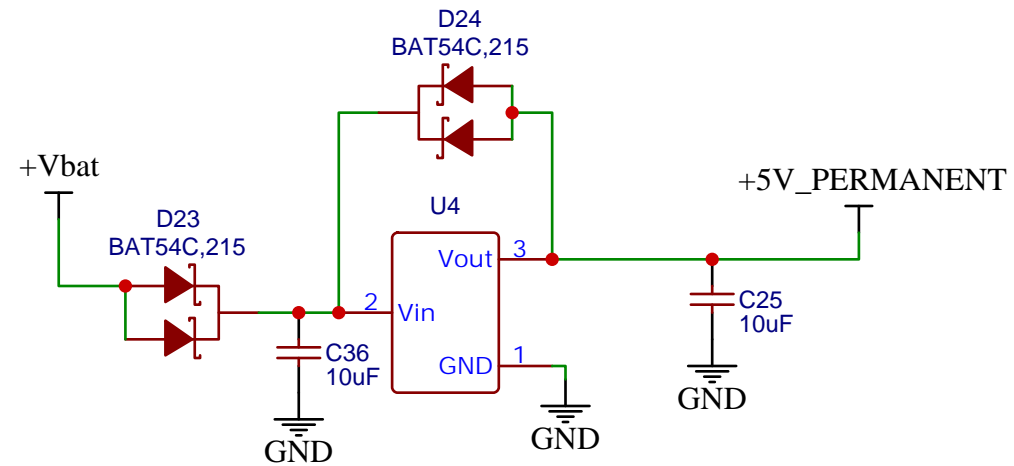
NOTE: At 500mA @ 12V, each of the MOSFETs will generate 6W of heat

Low-side PWM outputs



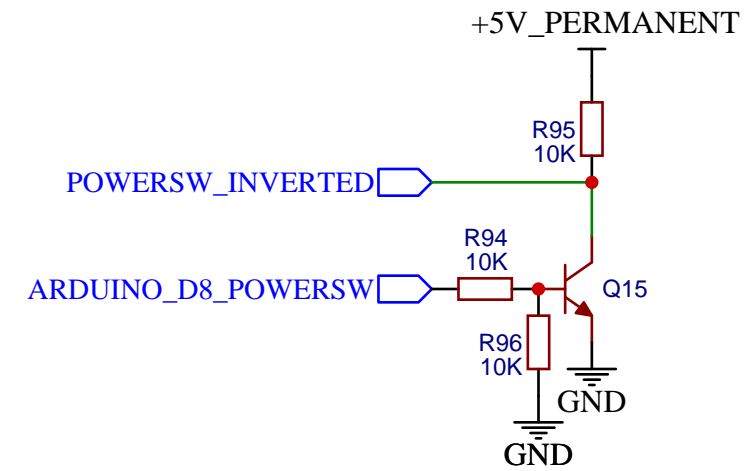


Permanent 5V regulator (100mA)



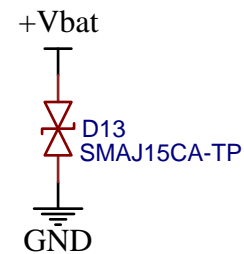
TODO: Capacitors

Power switch inversion logic

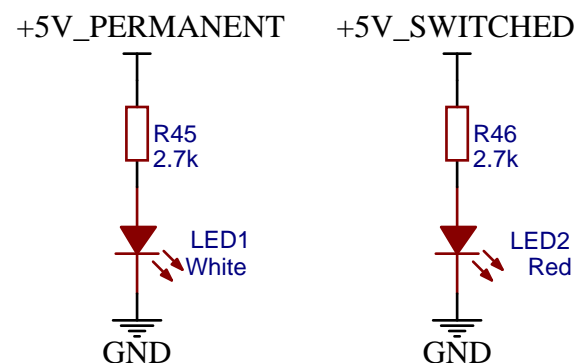


Vbat transient voltage suppression

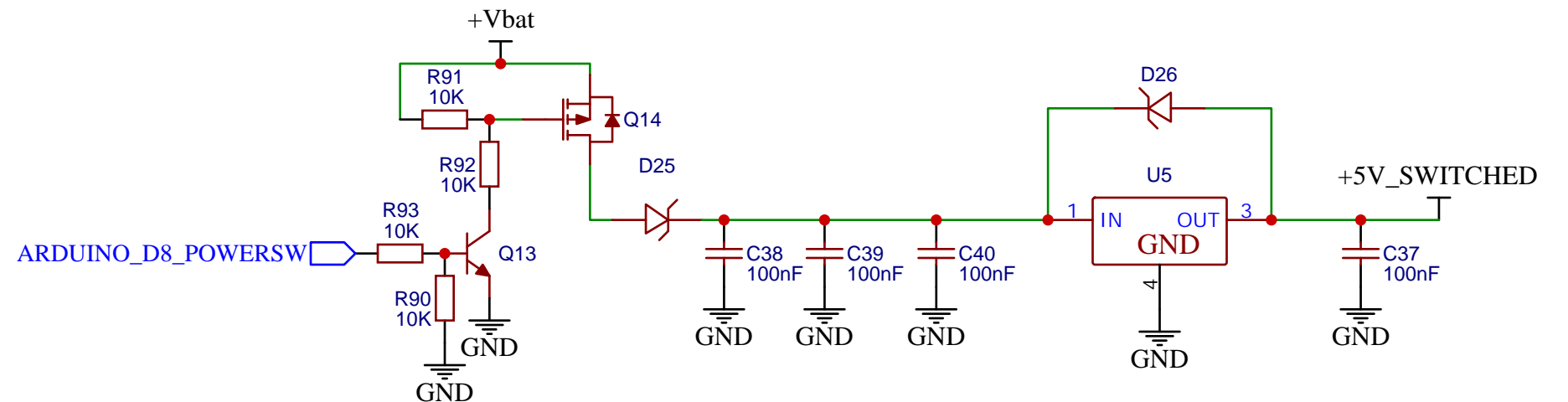
NOTE: There is a 5A PPTC in series with the Vbat input



Power indicator LEDs



Switched 5V regulator (500mA)



TITLE: Sheet_1		REV: 1.0
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