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Cesar Zaragoza Cortes

Resources Estimation for Quantum Computing Algorithms in Multiple Physical Platforms

Cesar Zaragoza Cortes

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Reading Committee:

Jeffrey Wilkes, Chair

Boris Blinov

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Abstract

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Cesar Zaragoza Cortes

Chair of the Supervisory Committee:
Professor Emeritus Jeffrey Wilkes
Department of Physics

An important task in the development of quantum computing technologies is to determine the resources needed to execute a particular algorithm in a device with certain characteristics. Resources estimation allows not only to determine whether it is possible to execute an algorithm in a current Noisy Intermediate-Scale Quantum (NISQ) device, but also to experiment with the parameters of quantum hardware to find out how much it would have to scale to achieve quantum advantage. We use the Q# quantum programming language to implement Shor's factorization algorithm, leverage simulators to perform resources estimation for trapped-ion and superconducting quantum hardware platforms, and compare the results.

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Chapter 1

INTRODUCTION

Algorithms designed for quantum computers have the potential to solve some problems that cannot be efficiently solved by algorithms designed for classical computers. However, estimating how much resources are needed to execute a quantum algorithm that outperforms a classical one is a difficult task. There are many quantum programming languages and tools built around them such as Q#[3], Qiskit[4] and Cirq[1] that allow execution of quantum algorithms on simulators but out-of-the-box options to estimate resources are limited to the logical level or not existent.

1.1 The Purpose of This Thesis

This thesis aims to estimate the resources required at the physical level to run Shor's semiprime integer factorization algorithm for trapped-ion and superconducting quantum hardware platforms. To do this, we will extend the simulators infrastructure built around Q# to calculate the maximum number of physical qubits, the total number of physical gates, and the maximum runtime required to execute a particular quantum algorithm.

Furthermore, we will also analyze the effects of errors introduced by the physical gates, analyze the feasibility of obtaining reliable results without implementing fault-tolerance, and estimate the cost of running the algorithm using error-corrected qubits and fault-tolerant gates.

In order to make this thesis more accesible to people from different backgrounds, we dedicate the next few chapters to provide a brief overview of the basics of quantum computing, quantum error correction, and Shor's semiprime integer factorization algorithm.

Chapter 2

BASICS OF QUANTUM COMPUTING

2.1 Dirac Notation

Also known as bra-ket notation, Dirac notation provides a convenient way of expressing the vectors used in quantum mechanics.

Dirac notation defines two elements:

- The *ket* ($|\psi\rangle$), which denotes a column vector in a complex vector space that represents a quantum state.

$$|\psi\rangle = \begin{bmatrix} \psi_1 \\ \psi_2 \\ \dots \\ \psi_n \end{bmatrix}$$

- The *bra* ($\langle\psi|$), which denotes a row vector that is the conjugate transpose, or adjoint, of a corresponding *ket* ($|v\rangle$).

$$\langle\psi| = \begin{bmatrix} \psi_1^* & \psi_2^* & \dots & \psi_n^* \end{bmatrix} = \begin{bmatrix} \psi_1 \\ \psi_2 \\ \dots \\ \psi_n \end{bmatrix}^\dagger$$

An *operator* \mathbf{A} , represented by a $n \times n$ matrix, acting on a *ket* $|\psi\rangle$ produces another *ket* $|\psi'\rangle$ such that the produced *ket* can be computed by matrix multiplication:

$$|\psi'\rangle = \mathbf{A} |\psi\rangle = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} \begin{bmatrix} \psi_1 \\ \psi_2 \end{bmatrix} = \begin{bmatrix} A_{11}\psi_1 + A_{12}\psi_2 \\ A_{21}\psi_1 + A_{22}\psi_2 \end{bmatrix}$$

The *inner product* is denoted as a bra-ket pair $\langle\phi|\psi\rangle$ and represents the probability amplitude that a quantum state ψ would be subsequently found in state ϕ :

$$\langle\phi|\psi\rangle = \begin{bmatrix} \phi_1^* & \phi_2^* \end{bmatrix} \begin{bmatrix} \psi_1 \\ \psi_2 \end{bmatrix} = \phi_1^* \psi_1 + \phi_2^* \psi_2$$

This notation also provides a way to describe the state vector of n uncorrelated quantum states, the *tensor product*:

$$|\phi\rangle \otimes |\psi\rangle = |\phi\rangle |\psi\rangle = |\phi\psi\rangle = \begin{bmatrix} \phi_1 \\ \phi_2 \end{bmatrix} \otimes \begin{bmatrix} \psi_1 \\ \psi_2 \end{bmatrix} = \begin{bmatrix} \phi_1\psi_1 \\ \phi_1\psi_2 \\ \phi_2\psi_1 \\ \phi_2\psi_2 \end{bmatrix}$$

Note that $|\psi\rangle^{\otimes n}$ represents the tensor product of n $|\psi\rangle$ quantum states:

$$|\psi\rangle^{\otimes n} = |\psi\rangle \otimes \cdots \otimes |\psi\rangle = |\psi\rangle \cdots |\psi\rangle = |\psi \cdots \psi\rangle = \begin{bmatrix} \psi_1 \\ \psi_2 \end{bmatrix} \otimes \cdots \otimes \begin{bmatrix} \psi_1 \\ \psi_2 \end{bmatrix}$$

2.2 Quantum Systems

*ToDo: Explain the Schrodinger equation, describe what a quantum system is and mention basic postulates of quantum mechanics (similar to what Nielsen and Chuang explain).

2.3 Qubits

In classical information theory and classical computing, a bit is the fundamental building block. Analogously, in quantum information theory and quantum computing, a quantum bit or qubit is the fundamental building block.

Physically, a qubit is a two-level quantum-mechanical system. The polarization of a single photon and the spin of the electron are examples of such systems.

Mathematically, a qubit is a linear combination of states $|\psi\rangle = \alpha|0\rangle + \beta|1\rangle$ where α and β are complex numbers known as amplitudes, and $|0\rangle$ and $|1\rangle$ are the computational basis states.

When a qubit is measured, the result is either $|0\rangle$ with probability $|\alpha|^2$ or $|1\rangle$ with probability $|\beta|^2$. Since the probabilities must sum to one, the qubit's state is normalized:

$$|\alpha|^2 + |\beta|^2 = 1$$

The computational basis states, which are analogous to the two values (0 and 1) that a classical bit may take, form an orthonormal basis represented by the following vectors:

$$|0\rangle = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$

$$|1\rangle = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$$

Using the previous definitions, we can see a qubit as a unit vector in two-dimensional complex vector space:

$$|\psi\rangle = \begin{bmatrix} \alpha \\ \beta \end{bmatrix}$$

2.4 Multi-Qubit Systems

**ToDo: Explain how multi-qubit systems are represented. Describe what a state vector is.*

2.5 Superposition

The principle of quantum superposition states that the most general state of a quantum-mechanical system is a linear combination of all distinct valid quantum states. A qubit is an example of a quantum superposition of the basis states $|0\rangle$ and $|1\rangle$.

A concrete example of a qubit in superposition that has the same probability of being measured as $|0\rangle$ or $|1\rangle$ is the following:

$$|\psi\rangle = \frac{1}{\sqrt{2}} (|0\rangle + |1\rangle)$$

**ToDo: Expand this section to show multi-qubit superposition.*

2.6 Quantum Logic Gates

In classical digital circuits, logic gates are the building blocks. Analogously, in the quantum circuit model of computation, quantum logic gates are the building blocks of quantum algorithms. Quantum gates act on qubits, transform them in different ways, and can be applied sequentially to perform complex quantum computations.

Quantum gates are unitary operators represented as $2^n \times 2^n$ unitary matrices where n is the number of qubits the gate operates on. Unitary matrices are complex square matrices \mathbf{U} which have the property that its conjugate transpose or adjoint \mathbf{U}^\dagger is also its inverse \mathbf{U}^{-1} :

$$\mathbf{U}^\dagger \mathbf{U} = \mathbf{U} \mathbf{U}^\dagger = \mathbf{U} \mathbf{U}^{-1} = \mathbf{I}$$

A quantum gate is applied to a qubit system by multiplying the gate's matrix representation by the qubits' state vector. This operation transforms the qubit system:

$$|\psi_1\rangle = \mathbf{U} |\psi_0\rangle$$

Applying a sequence of quantum gates is equivalent to performing a series of these multiplications. For example, applying gate \mathbf{U}_a followed by a gate \mathbf{U}_b to a state vector $|\psi\rangle$ is represented by the following expression where the gates closest to the state vector are applied first:

$$\mathbf{U}_b \mathbf{U}_a |\psi\rangle$$

Since matrix multiplication is associative, multiplying \mathbf{U}_a by \mathbf{U}_b produces a compound gate $\mathbf{U}_b \mathbf{U}_a$ that is equivalent to applying \mathbf{U}_a followed by \mathbf{U}_b :

$$\mathbf{U}_b \mathbf{U}_a |\psi\rangle = \mathbf{U}_b (\mathbf{U}_a |\psi\rangle) = (\mathbf{U}_b \mathbf{U}_a) |\psi\rangle$$

Note that all quantum gates are reversible since they are represented by unitary matrices. This means that for any gate, another gate exists that reverts the gate's transformation on a state vector:

$$|\psi\rangle = \mathbf{U}^\dagger (\mathbf{U} |\psi\rangle) = \mathbf{U}^\dagger \mathbf{U} |\psi\rangle = \mathbf{I} |\psi\rangle$$

**ToDo: Describe and show how gates are represented in graphical circuits.*

2.7 Single-Qubit Gates

Single-qubit gates are represented by 2×2 matrices.

*ToDo: List the most commonly used single-qubit gates and show how they transform a qubit.

2.8 Multi-Qubit Gates

*ToDo: List the most commonly used multi-qubit gates and show how they transform the qubits they act upon.

2.9 Entanglement

*ToDo: Define what entanglement is, describe why it is important, and show how qubits are entangled.

2.10 Interference

*ToDo: Define what interference is, describe why it is important, and show an example of interference.

2.11 Measurement

*ToDo: Define what it means to measure a qubit or a qubit system and show examples of measurements using different basis.

2.12 Quantum Advantage

*ToDo: Explain how quantum computers can solve a problem that a classical computer can't efficiently by exploiting superposition, entanglement, and interference.

Chapter 3

BASICS OF QUANTUM ERROR CORRECTION

This chapter explains the basic concepts to understand how quantum computations can be performed reliably in the presence of noise.

3.1 Noise and Error Correction

3.2 Bit Flip Code

3.3 Phase Flip Code

3.4 Shor Code

3.5 Fault-Tolerant Quantum Computation

3.6 Threshold Theorem

Chapter 4

SHOR'S SEMIPRIME INTEGER FACTORIZATION ALGORITHM

Shor's algorithm is a polynomial-time quantum algorithm for semiprime integer factorization. In comparison, the time complexity of the most efficient known classical factoring algorithm is superpolynomial.

We chose Shor's algorithm to because it is one of the most significant quantum algorithms, and because the number of qubits and the number of quantum operations required are proportional to the input size. The number of operations is particularly relevant since it makes the errors introduced by the physical gates an important consideration.

4.1 *Algorithm*

The problem that Shor's algorithm solves is the following: given a semiprime integer N , find its two prime factor p and q .

**ToDo: Describe the algorithm.*

4.2 *Quantum Subroutine*

**ToDo: Show the circuit representation of the quantum subroutine.*

4.3 *Q# Implementation*

The following Q# code presents a top-level implementation of Shor's algorithm. It is a slight modification to the implementation found in Microsoft's quantum samples GitHub repository.

**ToDo: Add reference to Microsoft's quantum samples repository.*

*ToDo: Breakdown the implementation into different sections and describe each one (similar to what is done in the "Learn Quantum Computing with Python and Q#" book).

*ToDo: Consider creating a Q# language option for lstlisting.

```
@EntryPoint()
operation FactorSemiprimeInteger (N: Int) : (Int, Int) {
    // TODO: Paste implementation.
}
```

The following Q# code presents a top-level implementation of the period-finding subroutine.

```
@EntryPoint()
operation EstimatePeriod(N : Int, a : Int) : Int {
    // TODO: Paste implementation.
    return 0;
}
```

Chapter 5

ANALYSIS FRAMEWORK

5.1 *Framework*

*ToDo: This was originally placed in the Introduction.

*ToDo: Expand on the following structure of the thesis:

1. Implement an algorithm using fault-tolerant error-corrected gates.
2. For each hardware platform do the following:
 - (a) Use an open-system simulator to analyze and optimize the effectiveness of fault-tolerant error-corrected gates.
 - (b) Use a resource estimator to determine the maximum input size for the algorithm to run on a real NISQ device.
 - (c) Use a resource estimator to determine the characteristics that a device should have to run the algorithm for a specific input size, and determine what would be the runtime.
3. Compare the results for each hardware platform.

*ToDo: Briefly describe each step in the process.

*ToDo: Include results of default resources estimator in this section or consider showing the results, explanation and analysis in a separate chapter (preferred).

5.2 *Open-System Simulator*

*ToDo: Describe what is the purpose of using an open-system simulator and how it works.

5.3 *Resources Estimation Strategy*

The strategy we use for resources estimation is very similar to the one proposed by Soeken et al.[2]. The process is the following:

1. Implement a quantum algorithm using a high-level programming language (Q# in this case).
2. Verify the correctness of the implementation by executing the algorithm in a full state simulator.
3. Setup the simulator to estimate physical resources with the parameters that are specific to a hardware platform.
4. Analyze the results obtained from the resources estimator.

**ToDo: Explain the following*

- Resources Metrics: Describe the values obtained from the resources estimator (gate count, runtime, accumulated error), and how they are calculated.
- Gate Decomposition: Describe why logical-level gates have to be decomposed into physical-level gates.
- Limitations: Describe the limitations that this resources estimation has in regards to runtime (sum of the runtimes of individual gates rather than the critical path), and types of computations (trouble with mixed states).

5.4 *Extending Q# Simulation Infrastructure for Estimation of Physical Resources*

Microsoft's Quantum Development Kit (QDK) supports the implementation of custom simulators that can be used to run Q# programs. We leverage this capability and implement

a simulator that calculates the resources a quantum algorithm would require to be executed in a hardware platform with specific characteristics.

***ToDo: Explain the following**

- QDK Custom Simulators: Describe how custom simulators are implemented using diagrams and code snippets.
- Software Architecture of Physical Resources Estimator Simulator: Describe the software architecture using diagrams and code snippets.

Source code of a working version can be found in in GitHub.

Chapter 6

TRAPPED-IONS HARDWARE PLATFORM

*ToDo: Briefly describe this quantum computing platform.

6.1 *Native Gates and Platform Characteristics*

*ToDo: Enumerate the native gates that this platform implements, its characteristics (fidelity, gate time), and how logical gates are implemented (using circuits to illustrate them).

6.2 *Fault-tolerant analysis and optimization*

*ToDo: Show the use of an open-system simulator to analyze and optimize the effectiveness of fault-tolerant error-corrected gates.

6.3 *Resources Estimation Analysis*

*ToDo: Show (using tables and/or plots) how resources escalate as the size and pattern of the input changes.

Example of output of resources used by the Bernstein-Vazirani algorithm using a secret string of size 5:

Ion Platform Resource Estimation

Bernstein–Vazirani

PHYSICAL LAYER

Total Statistics

Qubits: 6

Gate Count: 38

Time: 1175

Error: 0.35000000000000003

Gate Statistics

R:

- Count: 35
- Time: 470
- Error: 0.23000000000000002

XX:

- Count: 3
- Time: 705
- Error: 0.12000000000000001

6.4 Analysis of Execution in NISQ Devices

*ToDo: Analyze what would be the maximum size (and difficulty of pattern) of the secret string that can be used in a current NISQ device based on this platform.

6.5 Analysis of Execution of Algorithm for Input of Specific Size

*ToDo: Use a resource estimator to determine the characteristics that a device should have to run the algorithm for a specific input size, and determine what would be the runtime.

Chapter 7

SUPERCONDUCTING HARDWARE PLATFORM

*ToDo: Briefly describe this quantum computing platform.

7.1 *Native Gates and Platform Characteristics*

*ToDo: Enumerate the native gates that this platform implements, its characteristics (fidelity, gate time), and how logical gates are implemented (using circuits to illustrate them).

7.2 *Fault-tolerant analysis and optimization*

*ToDo: Show the use of an open-system simulator to analyze and optimize the effectiveness of fault-tolerant error-corrected gates.

7.3 *Resources Estimation Analysis*

*ToDo: Show (using tables and/or plots) how resources escalate as the size and pattern of the input changes.

7.4 *Analysis of Execution in NISQ Devices*

*ToDo: Analyze what would be the maximum size (and difficulty of pattern) of the secret string that can be used in a current NISQ device based on this platform.

7.5 *Analysis of Execution of Algorithm for Input of Specific Size*

*ToDo: Use a resource estimator to determine the characteristics that a device should have to run the algorithm for a specific input size, and determine what would be the runtime.

Chapter 8

COMPARISON BETWEEN TRAPPED-IONS AND SUPERCONDUCTING HARDWARE PLATFORMS

*ToDo: Compare the results obtained from both hardware platforms and comment on the insights obtained.

Chapter 9

FUTURE WORK

*ToDo: Mention how this framework can be used to analyze and compare other hardware platforms.

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Appendix A

IMPLEMENTATION OF GENERIC PHYSICAL RESOURCES ESTIMATION FRAMEWORK

*ToDo: Add source code that implements the physical resources estimation framework.

Source code can also be found in GitHub.